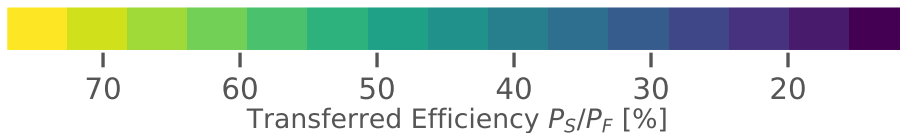
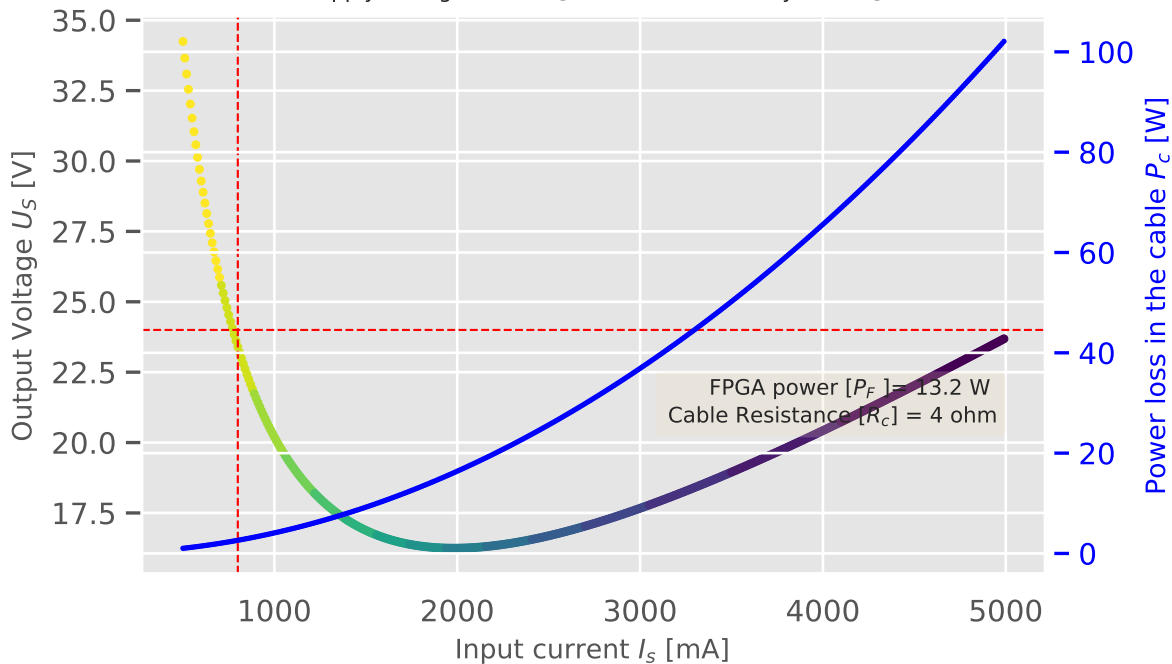
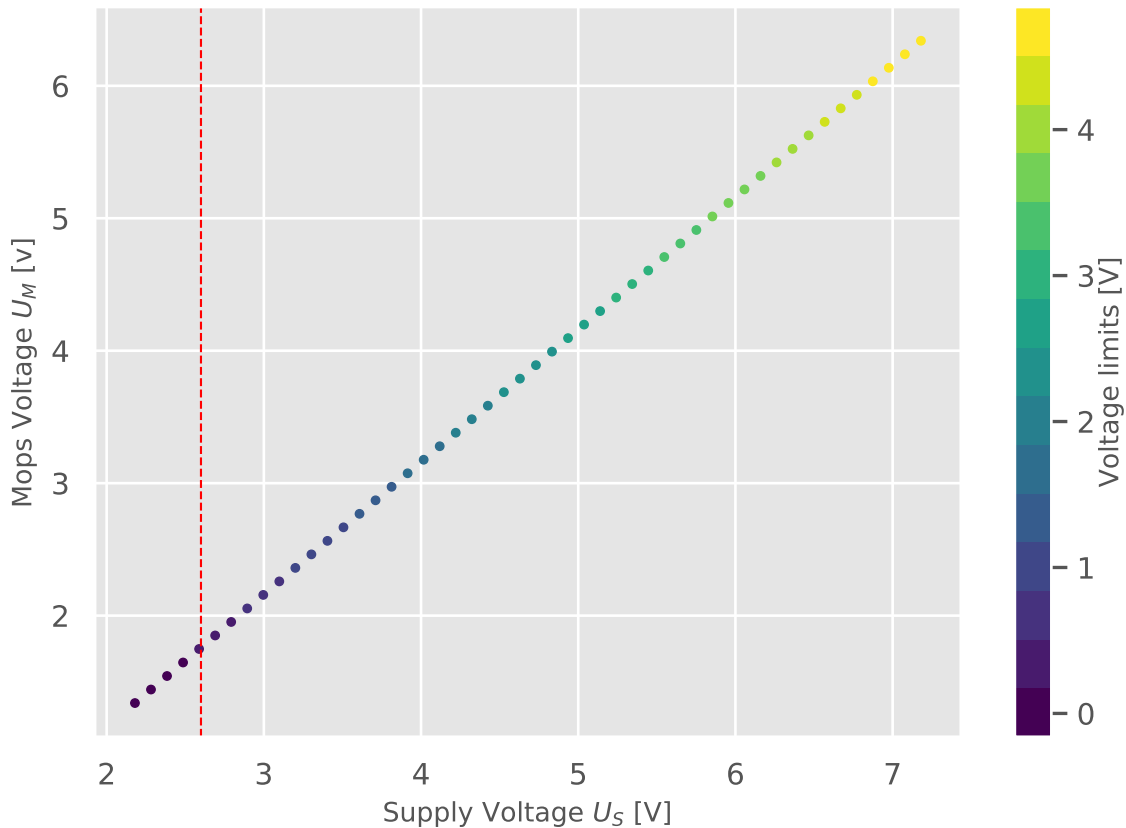


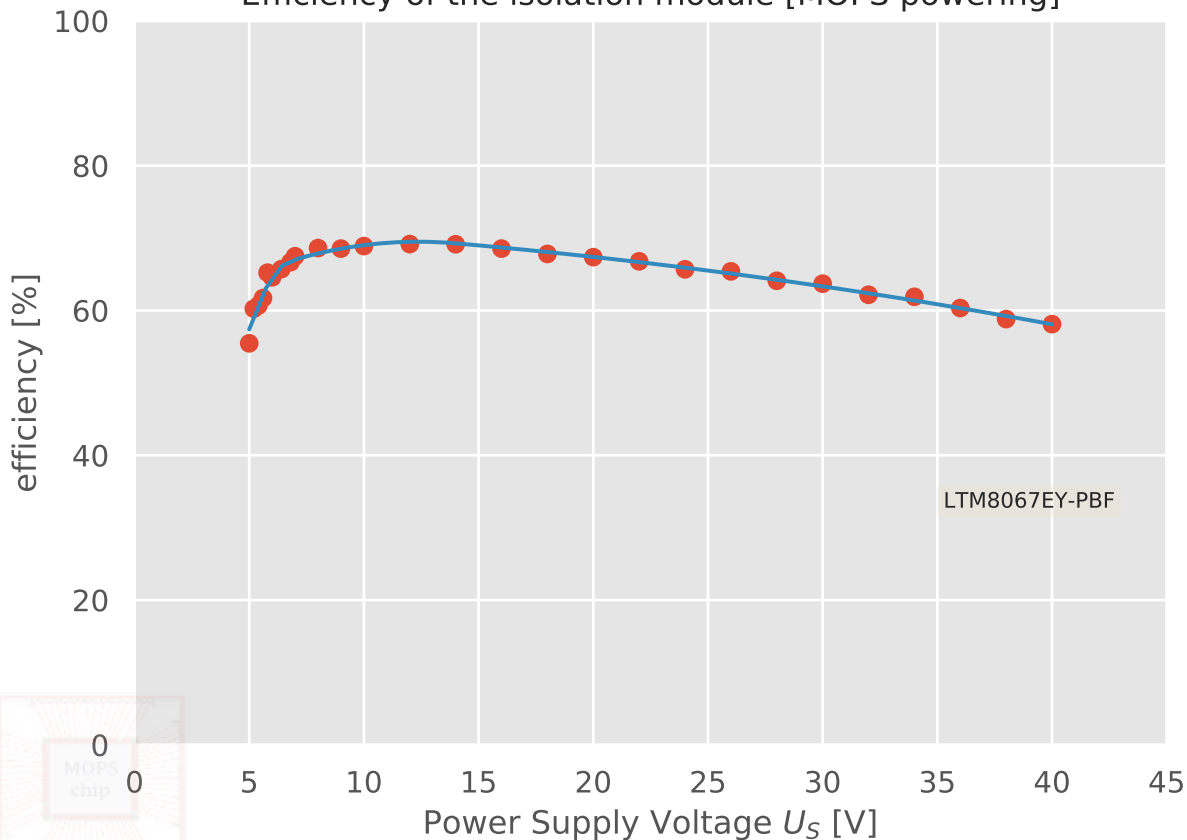
Supply Voltage needed [DC module efficiency is 82%]



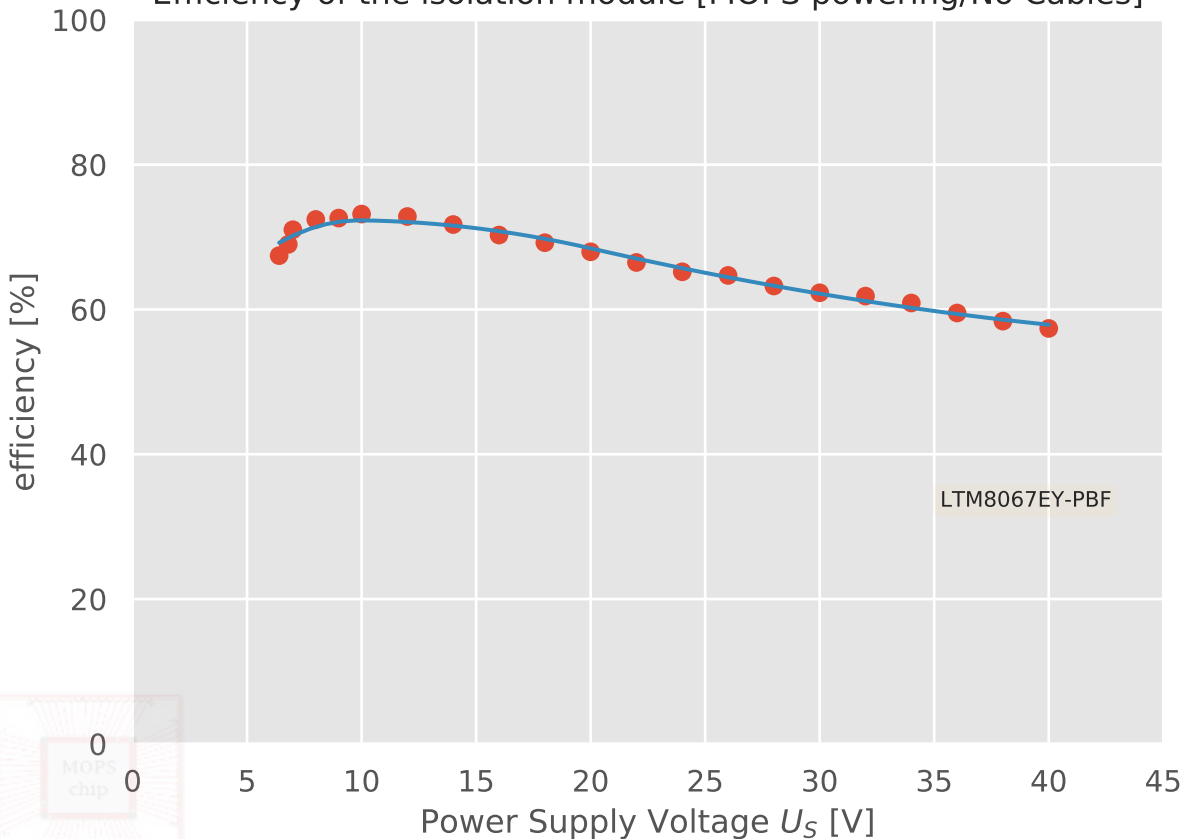
Voltage across one Mops U_M [V] When the other one is disconnected



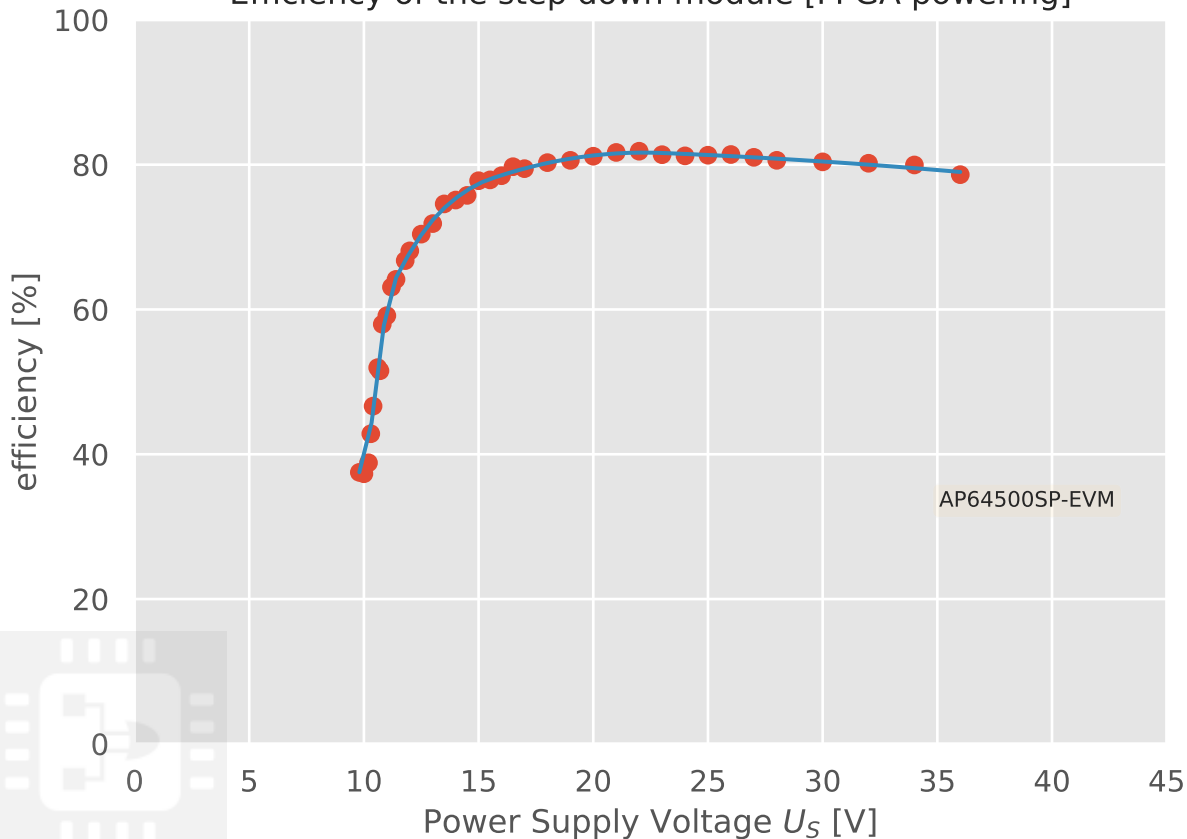
Efficiency of the isolation module [MOPS powering]



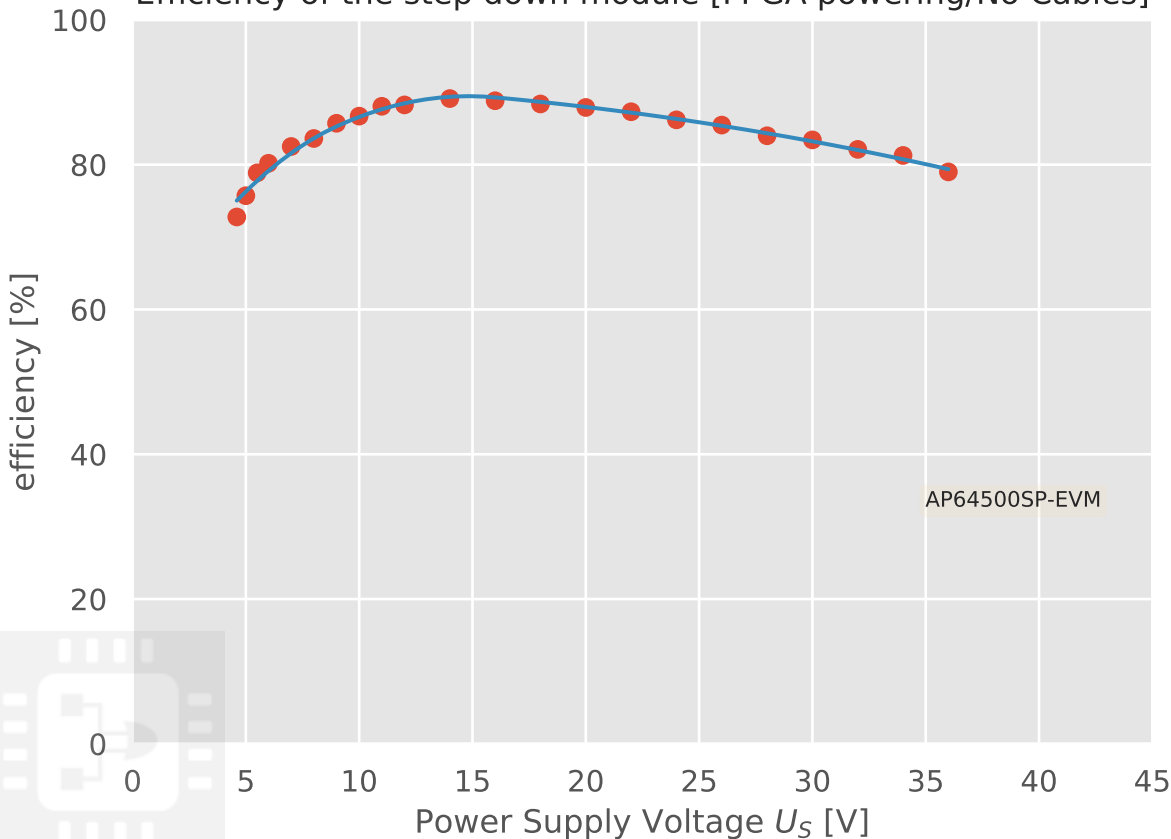
Efficiency of the isolation module [MOPS powering/No Cables]



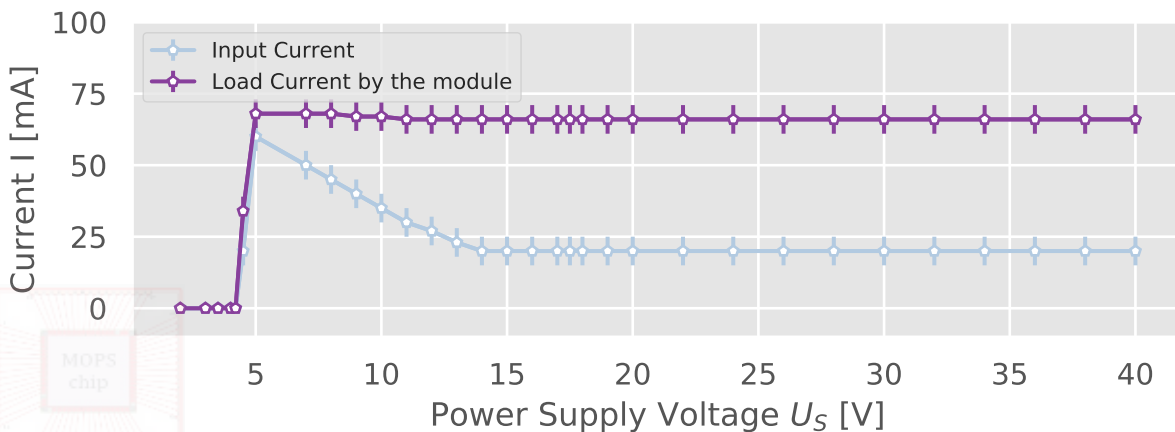
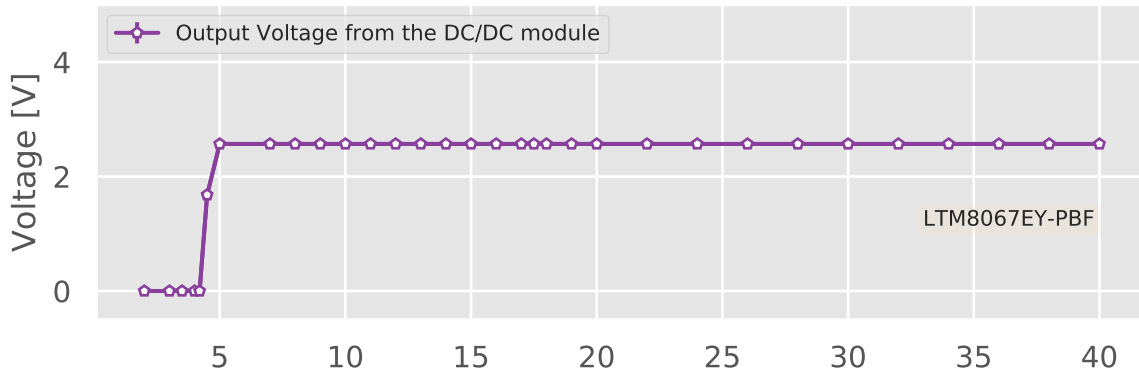
Efficiency of the step down module [FPGA powering]



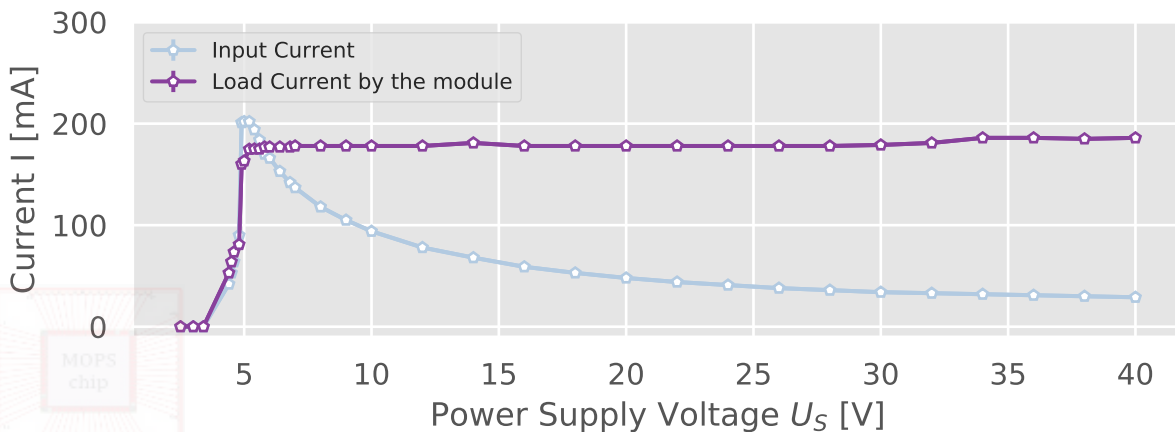
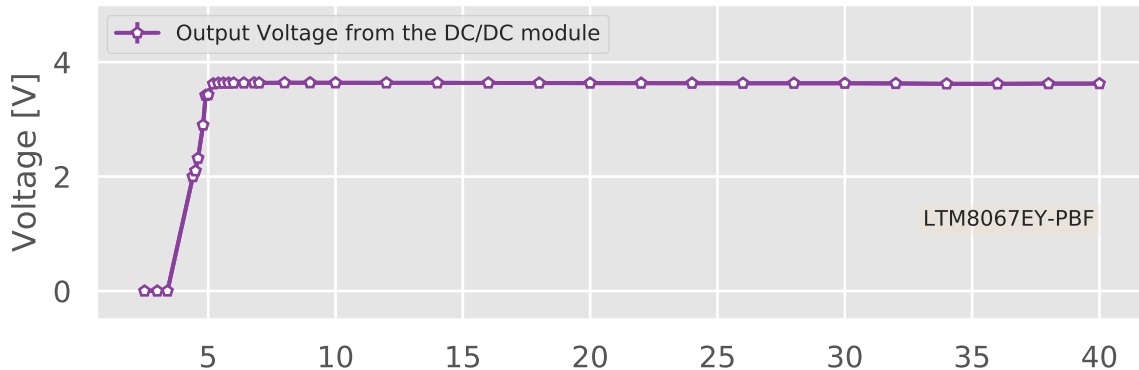
Efficiency of the step down module [FPGA powering/No Cables]



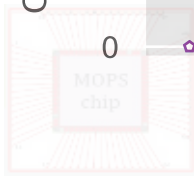
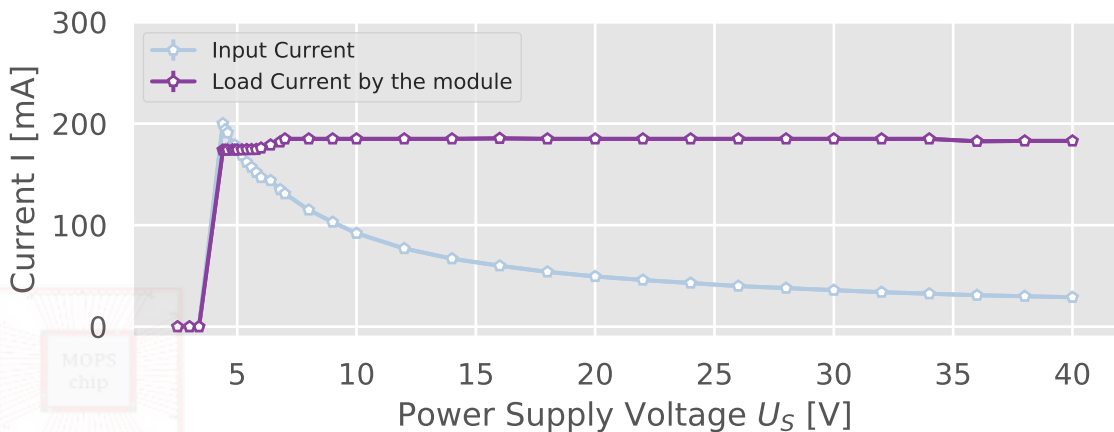
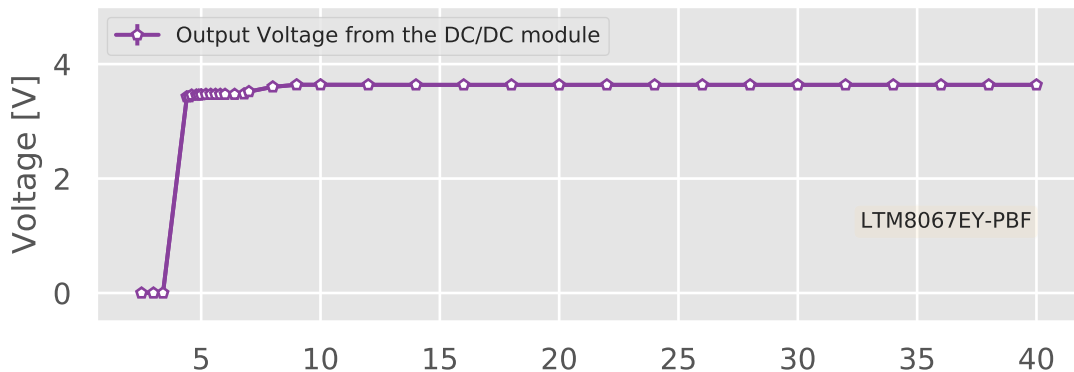
Testing results of the isolation module [MOPS powering]



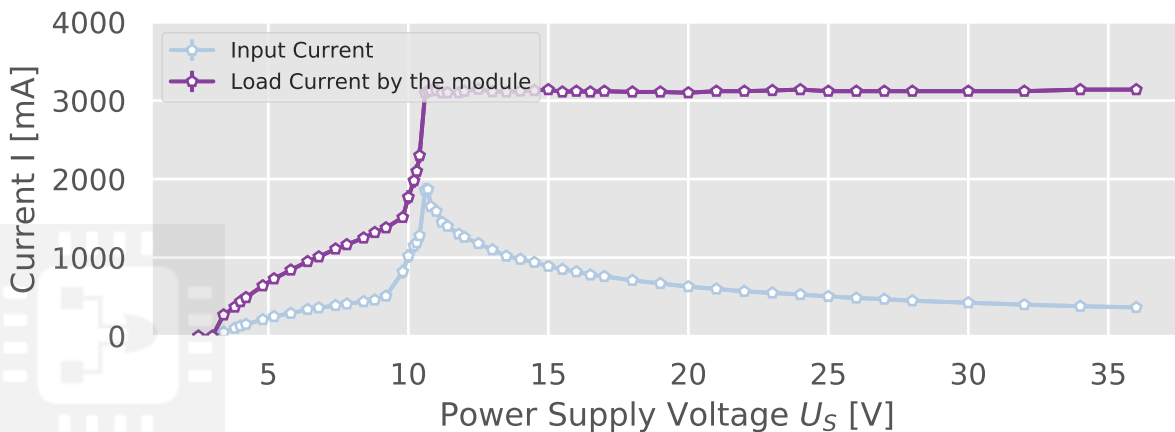
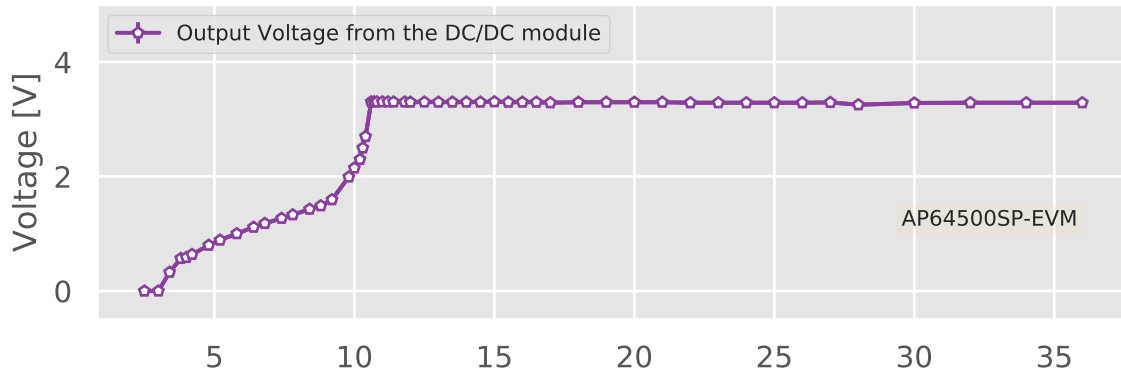
Testing results of the isolation module [MOPS powering]



Testing results of the isolation module [MOPS powering/No Cables]



Testing results of the step down module [FPGA powering]



Testing results of the step down module [FPGA powering/No Cables]

