

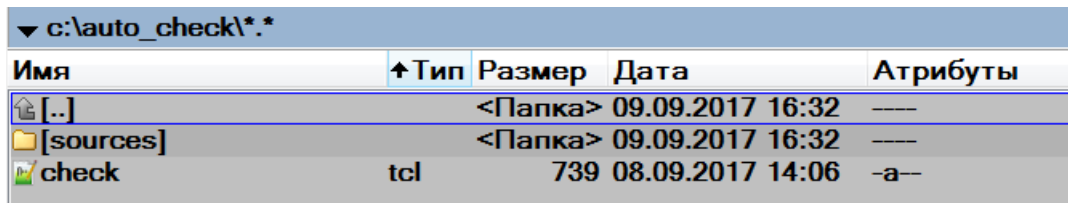
## Autochecking of lab source codes

In general the source code of a lab task requires autochecking, except those where it is explicitly said “not for autochecking”. The autochecking generates a number (a hash), one have to send this number in the corresponding form of online course. Each task is autochecked separately. E.g., if there are 4 tasks in the lab, than the source code for each task is to be checked. Finally, 4 hashes are required.

The autochecking is performed using tcl scripts for automatic Vivado project generation and simulation.

The steps to perform the autochecking (to get the hash) is the following.

1. Unpack the archive *check\_X.rar* (X – the lab and task number, e.g., 01 – archive for the first lab) to any folder on your PC. Note that the path to this folder should not contain whitespaces or Cyrillic symbols. Check that the script *check.tcl* is inside the folder.

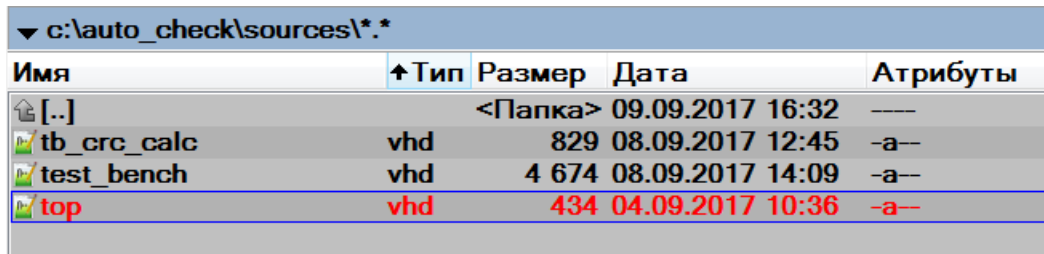


Имя	↑ Тип	Размер	Дата	Атрибуты
[..]		<Папка>	09.09.2017 16:32	----
[sources]		<Папка>	09.09.2017 16:32	----
check	tcl	739	08.09.2017 14:06	-a-

2. Add all your \*.vhd files into the *sources* folder. The VHDL entity declaration (entity name, input and output ports' names and bit widths) must be the one, which is proposed in the corresponding lab task description.

In the following example the autochecked entity is in the *top.vhd*. Files *test\_bench.vhd* and *tb\_crc\_calc.vhd* are already present in the *sources* folder.

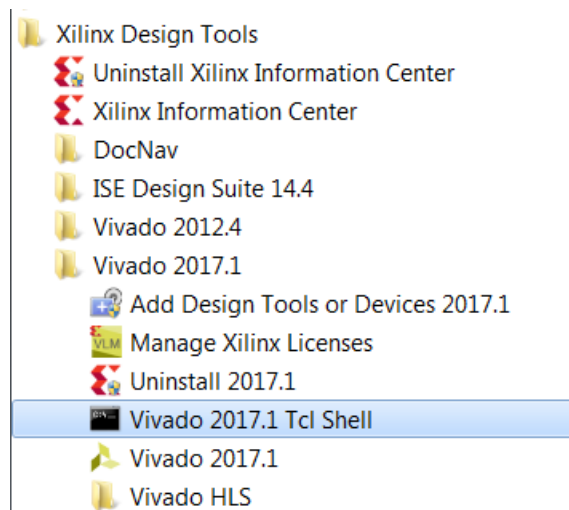
Add all your source files in case when the autochecked design consists of several entities.



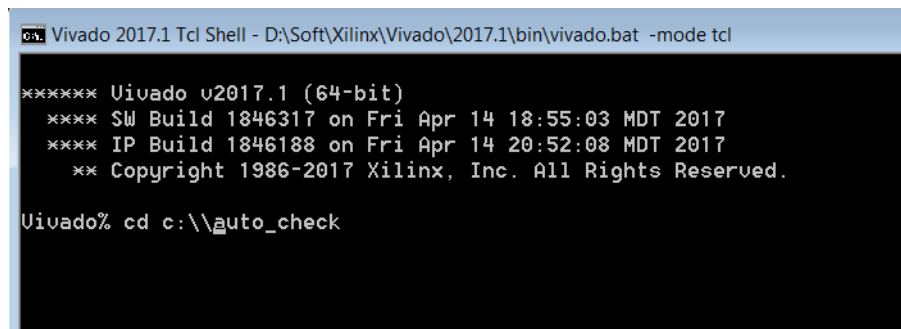
Имя	↑ Тип	Размер	Дата	Атрибуты
[..]		<Папка>	09.09.2017 16:32	----
tb_crc_calc	vhd	829	08.09.2017 12:45	-a-
test_bench	vhd	4 674	08.09.2017 14:09	-a-
top	vhd	434	04.09.2017 10:36	-a-

3. Run the Vivado Tcl Shell.

Run=>Xilinx Design Tools=>Vivado 20xx=> Vivado 20xx Tcl Shell.



4. The command line for tcl shell is opened. Change the current directory to the folder where the script *check.tcl* is located, using *cd* command. Note that symbols *\\* instead of *\* are used in the path definition.



```
GA Vivado 2017.1 Tcl Shell - D:\Soft\Xilinx\Vivado\2017.1\bin\vivado.bat -mode tcl

##### Uivado v2017.1 (64-bit)
##### SW Build 1846317 on Fri Apr 14 18:55:03 MDT 2017
##### IP Build 1846188 on Fri Apr 14 20:52:08 MDT 2017
##### Copyright 1986-2017 Xilinx, Inc. All Rights Reserved.

Uivado% cd c:\\auto_check
```

5. Run the autochecking script using *source* command.



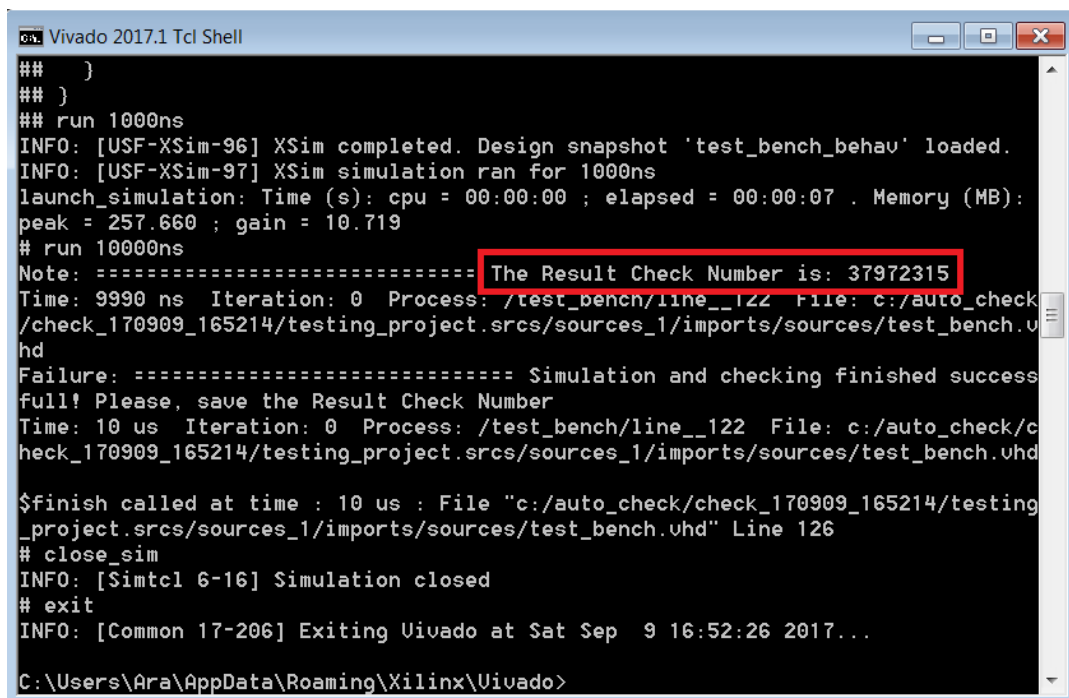
```
GA Vivado 2017.1 Tcl Shell - D:\Soft\Xilinx\Vivado\2017.1\bin\vivado.bat -mode tcl

##### Uivado v2017.1 (64-bit)
##### SW Build 1846317 on Fri Apr 14 18:55:03 MDT 2017
##### IP Build 1846188 on Fri Apr 14 20:52:08 MDT 2017
##### Copyright 1986-2017 Xilinx, Inc. All Rights Reserved.

Uivado% cd c:\\auto_check
Uivado% source check.tcl_
```

Note, that steps 4 and 5 could be performed by the following single command without *cd*: just *source {source c:\\auto\_check\\check.tcl}*.

6. Wait for autochecking to be fully completed. Copy the hash (it is in red on the following figure). In the following example the hash is 37972315. Note, that the new folder is created in the folder with *check.tcl* script after the check. This new folder contains the Vivado project, which was automatically created for autochecking.



```
GA Vivado 2017.1 Tcl Shell

##  )
##  )
## run 1000ns
INFO: [USF-XSim-96] XSim completed. Design snapshot 'test_bench_behav' loaded.
INFO: [USF-XSim-97] XSim simulation ran for 1000ns
launch_simulation: Time (s): cpu = 00:00:00 ; elapsed = 00:00:07 . Memory (MB):
peak = 257.660 ; gain = 10.719
# run 10000ns
Note: ===== The Result Check Number is: 37972315
Time: 9990 ns Iteration: 0 Process: /test_bench/line__122 File: c:/auto_check
/check_170909_165214/testing_project.srscs/sources_1/imports/sources/test_bench.u
hd
Failure: ===== Simulation and checking finished success
full! Please, save the Result Check Number
Time: 10 us Iteration: 0 Process: /test_bench/line__122 File: c:/auto_check/c
heck_170909_165214/testing_project.srscs/sources_1/imports/sources/test_bench.uhd

$finish called at time : 10 us : File "c:/auto_check/check_170909_165214/testing
_project.srscs/sources_1/imports/sources/test_bench.uhd" Line 126
# close_sim
INFO: [Simtcl 6-16] Simulation closed
# exit
INFO: [Common 17-206] Exiting Vivado at Sat Sep 9 16:52:26 2017...

C:\Users\Ara\AppData\Roaming\Xilinx\Uivado>
```