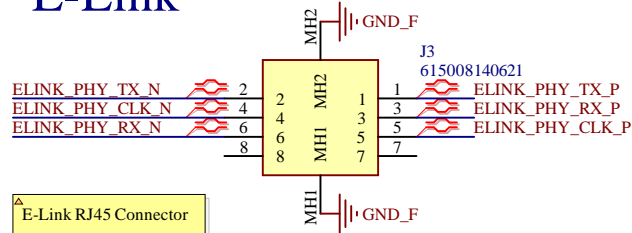
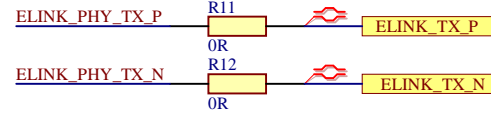


Title			SOM Board Connectors
Size	Number	Revision	
A3		1	
Date:	3.11.2022	Sheet of	
File:	C:\Users\... \FPGA Connectors.SchDoc	Drawn By:	

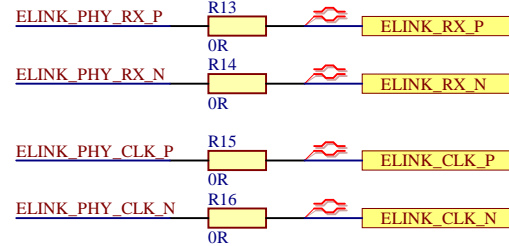
## E-Link



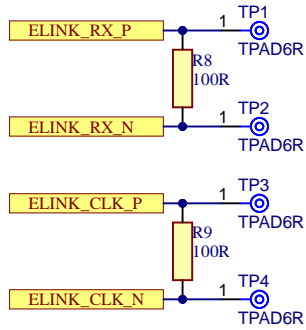
E-Link RJ45 Connector  
Pinout not finalized  
=> T568A  
Pair 1 (Blue) - CLK  
Pair 2 (Orange) - RX  
Pair 3 (Green) - TX



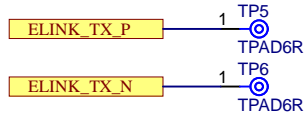
For Series Termination Tests. But first assemble with 0R resistors



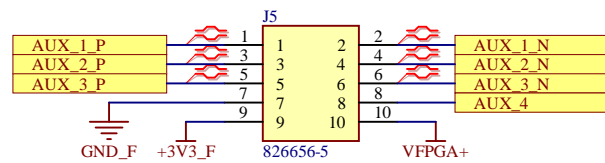
For AC Coupling Tests. But first assemble with 0R resistors



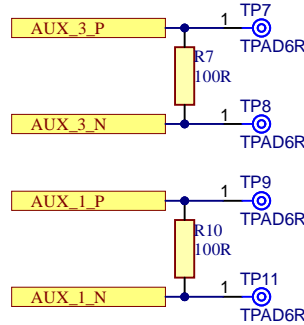
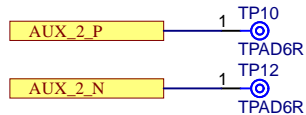
100R termination



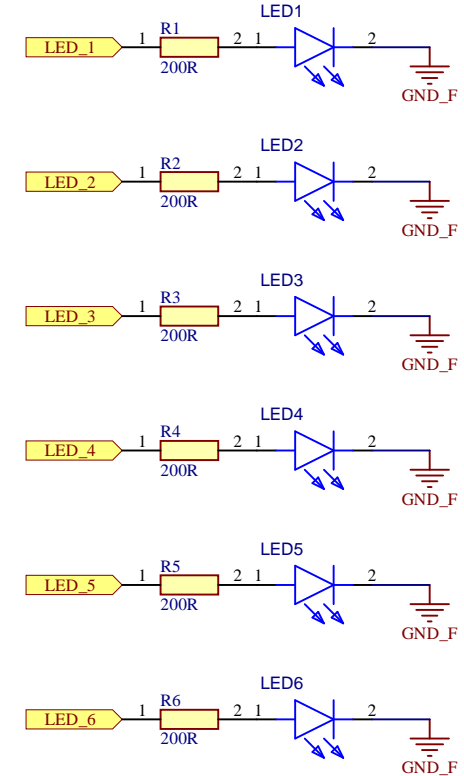
## Auxillary Connector



Set up as diff pairs, but can be all used as single ended

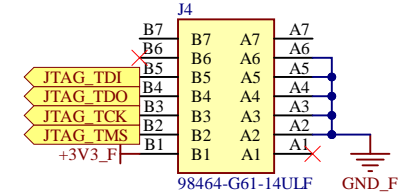


## LEDs

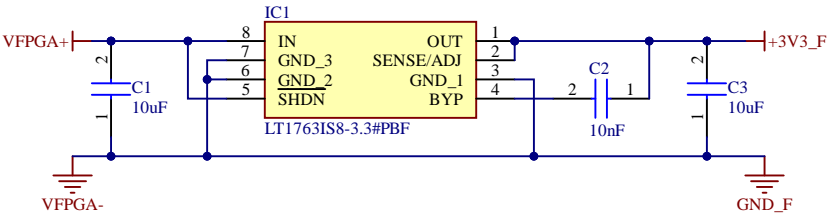


6,7mA per LED  
9mW heat dissipation in resistor

## Debug Connector JTAG



Title		
Frontpanel IO		
Size	Number	Revision
A4		1
Date:	3.11.2022	Sheet of
File:	C:\Users\...\FrontPanel.SchDoc	Drawn By:



Title			Power
Size	Number	Revision	
A4		1	
Date:	3.11.2022	Sheet of	
File:	C:\Users\...\Power.SchDoc	Drawn By:	

