



## PROJECT SPECIFICATION

**Automotive door control system design****Provide Fully Static Design**

CRITERIA	MEETS SPECIFICATIONS
Read project requirements	<p>Hardware requirements:</p> <ol style="list-style-type: none"><li>1. Two microcontrollers connected via CAN bus</li><li>2. One Door sensor (D)</li><li>3. One Light switch (L)</li><li>4. One Speed sensor (S)</li><li>5. ECU 1 connected to D, S, and L, all input devices</li><li>6. Two lights, right (RL) and left (LL)</li><li>7. One buzzer (B)</li><li>8. ECU 2 connected to RL, LL, and B, all output devices</li></ol> <p>Software requirements:</p>

<p><b>CRITERIA</b></p>	<p><b>MEETS SPECIFICATIONS</b></p> <ol style="list-style-type: none"> <li>1. ECU 1 will send status messages periodically to ECU 2 through the CAN protocol</li> <li>2. Status messages will be sent using Basic Communication Module (BCM)</li> <li>3. Door state message will be sent every 10ms to ECU 2</li> </ol>
	<ol style="list-style-type: none"> <li>4. Light switch state message will be sent every 20ms to ECU 2</li> <li>5. Speed state message will be sent every 5ms to ECU 2</li> <li>6. Each ECU will have an OS and application SW components</li> <li>7. If the door is opened while the car is moving → Buzzer ON, Lights OFF</li> <li>8. If the door is opened while the car is stopped → Buzzer OFF, Lights ON</li> <li>9. If the door is closed while the lights were ON → Lights are OFF after 3 seconds</li> <li>10. If the car is moving and the light switch is pressed → Buzzer OFF, Lights ON</li> <li>11. If the car is stopped and the light switch is pressed → Buzzer ON, Lights ON</li> </ol> <p>You should draw and deliver the system schematic (Block Diagram) according to your requirements understanding, a screenshot is required</p>

CRITERIA	MEETS SPECIFICATIONS
2- Static design analysis	<p>For ECU 1:</p> <ol style="list-style-type: none"><li>1. Make the layered architecture</li><li>2. Specify ECU components and modules</li><li>3. Provide full detailed APIs for each module as well as a detailed description for the used typedefs</li><li>4. Prepare your folder structure according to the previous points</li></ol> <p>For ECU 2:</p> <ol style="list-style-type: none"><li>1. Make the layered architecture</li><li>2. Specify ECU components and modules</li><li>3. Provide full detailed APIs for each module as well as a detailed description for the used typedefs</li><li>4. Prepare your folder structure according to the previous points</li></ol> <p>You should deliver a pdf file containing all your work and a video recording where you</p>

	will discuss your work (maximum 3min long)
CRITERIA	MEETS SPECIFICATIONS

### Provide Fully Dynamic design

CRITERIA	MEETS SPECIFICATIONS
Dynamic design analysis	<p>For ECU 1:</p> <ol style="list-style-type: none"><li>1. Draw a state machine diagram for each ECU component</li><li>2. Draw a state machine diagram for the ECU operation</li><li>3. Draw the sequence diagram for the ECU</li><li>4. Calculate CPU load for the ECU</li></ol> <p>For ECU 2:</p> <ol style="list-style-type: none"><li>1. Draw a state machine diagram for each ECU component</li><li>2. Draw a state machine diagram for the ECU operation</li><li>3. Draw the sequence diagram for the ECU</li><li>4. Calculate CPU load for the ECU</li></ol> <p>Calculate bus load in your system: With what percentage of system bus was busy per 1 second</p>

CRITERIA	MEETS SPECIFICATIONS You should deliver a pdf file containing all your work and a video recording where you will discuss your work (maximum 5min long)

## Suggestions to Make Your Project Stand Out!

Tasks Pseudocode

Meets Specifications

For ECU 1:

1. Write Pseudocode for each ECU component

For ECU 2:

1. Write Pseudocode for each ECU component

You should deliver all ECUs components .c and .h files

