

1. Using analytical methods calculate the following for the given set of tasks:

- Calculate the system hyperperiod: which value = Least Common Multiplier of all tasks periodicities:

Button_1_PERIOD	50
Button_2_PERIOD	50
Transmitte_PERIOD	100
Receive_PERIOD	20
LOAD_1_PERIOD	10
LOAD_2_PERIOD	100

Hyperperiod=100

- Calculate the CPU load:
 - "button1_monitor" & "button2_monitor" tasks execution time: 14 uSec (2 Hyperperiod)
 - "periodic transmitter" task execution time: 18.8uSec (1 Hyperperiod)
 - "UART receiver" task execution time: 22.3 uSec (5 Hyperperiod)
 - "load1_simulator" and "load2_simulator" tasks execution time: 5 mSec (10 Hyperperiod) and 12 mSec (1 Hyperperiod)

$CPU_L = ((14 \mu s * 2) * 2 + 18.8 \mu s + 22.3 \mu s * 5) + 5 \text{ ms} * 10 + 12 \text{ ms} / 100 \text{ ms}) * 100\%$

$= 136 \mu s + 50 \text{ ms} + 12 \text{ ms} / 100 \text{ ms} * 100\% = 62.136\%$

- Check system schedulability using URM:

$U \leq n[2^{1/n} - 1]$, $n=6 \rightarrow U_{rm} = 6(2^{1/6} - 1) = 0.734$

$U = \sum C_i/P_i = 14 \mu s / 50 \text{ ms} + 14 \mu s / 50 \text{ ms} + 18.8 \mu s / 100 \text{ ms} + 22.3 \mu s / 20 \text{ ms} + 5 \text{ ms} / 10 \text{ ms} + 12 \text{ ms} / 100 \text{ ms} = 0.620748$

, Then $U < U_{rm} \rightarrow$ it's **Schedulable** Systems.

- Check system schedulability using time demand analysis techniques (Assuming the given set of tasks are scheduled using a fixed priority rate-monotonic scheduler):

- Assumes only periodic tasks are used
- $D \leq P$
- Zero context switch time
- Equation

$$w_i(t) = e_i + \sum_{k=1}^{i-1} \left\lceil \frac{t}{p_k} \right\rceil e_k \quad \text{for } 0 < t \leq p_i$$

W = Worst response time
E = Execution time
P = Periodicity
T = Time instance

T1= Button_1_PERIOD	50,	E=14 μ s
T2= Button_2_PERIOD	50,	E=14 μ s
T3= Transmitter_PERIOD	100,	E=18.8 μ s
T4= Receive_PERIOD	20,	E=22.3 μ s
T5= LOAD_1_PERIOD	10,	E=5ms
T6= LOAD_2_PERIOD	100,	E=12ms

- Assume the following task using a fixed priority (High to low):
- T5 {P:10, E=5ms}, T4 {P:20, E=22.3 μ s}, T1 {P:50, E=14 μ s}, T2 {P:50, E=14 μ s}, T3 {P:100, E=18.8 μ s}, T6 {P:100, E=12ms}

1. Task5 load_1_simulator:

$$W(1) = 5 + 0 = 5$$

$$W(2) = 5 + 0 = 5$$

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$$W(10) = 5 + 0 = 5$$

Assume deadline = P: $W(10) = 5 + 0 = 5 < 10$, So Task 5 is **Schedulable**.

2. Task 4 UART receiver: W(1) to W(20)

$W(20) = 22.3\mu\text{s} + (20/10)*5\text{ms} = 10.0223\text{ms} < 20$, So Task 4 is **Schedulable**.

3. Task1 button_1_monitor: W(1) to W(50)

$$W(50) = 14 \mu s + (50/20) * 22.3 \mu s + (50/10) * 5ms = 14 \mu s + 66.9 \mu s + 25ms \\ = 25.081 < 50, \text{ So Task 1 is Schedulable.}$$

4. Task2 button_2_monitor: W(1) to W(50)

$$W(50) = 14 \mu s + (50/50) * 14 \mu s + (50/20) * 22.3 \mu s + (50/10) * 5ms \\ = 14 \mu s + 14 \mu s + 66.9 \mu s + 25ms \\ = 25.095 < 50, \text{ So Task 2 is Schedulable.}$$

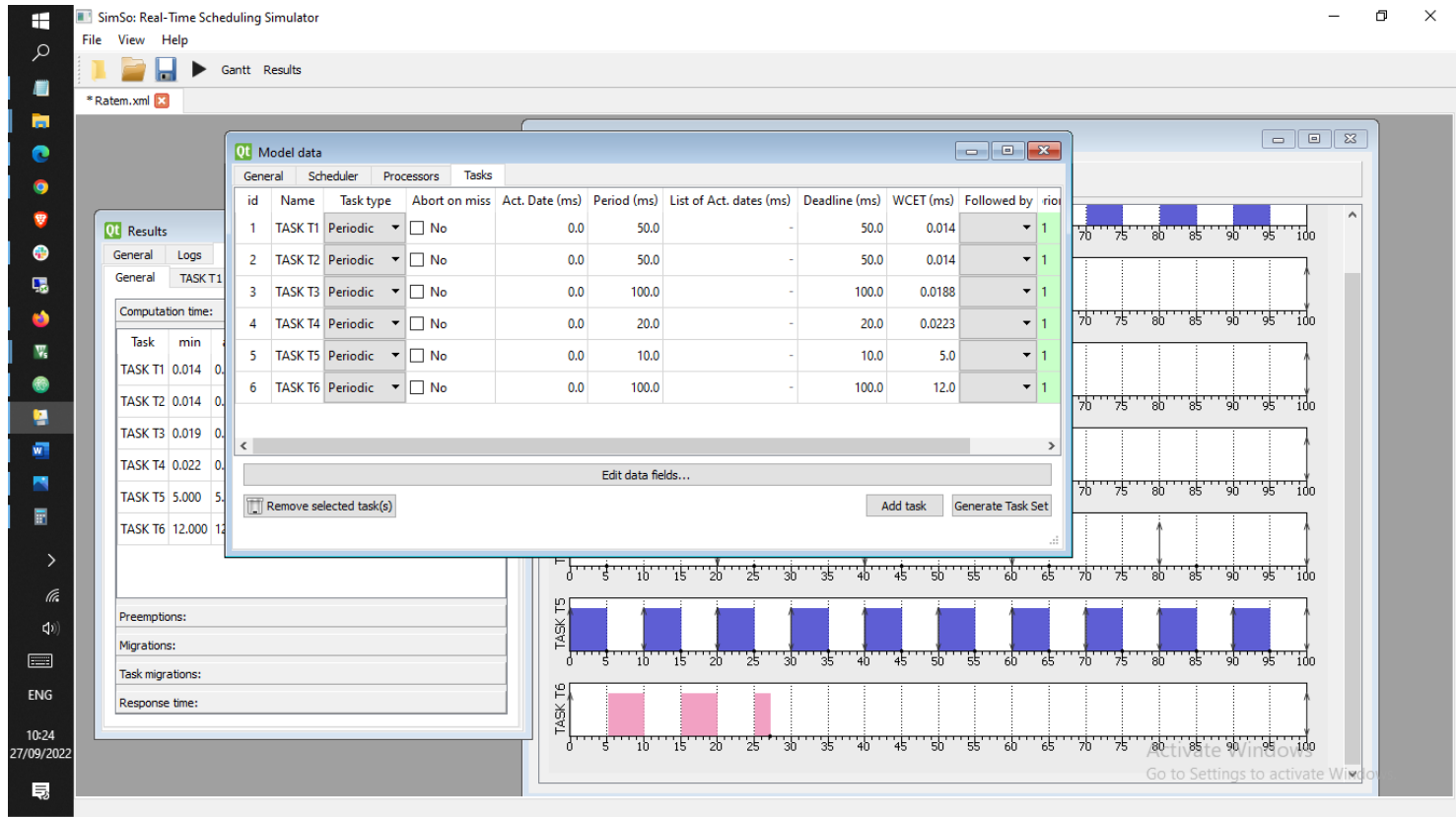
5. Task3 periodic transmitter: W(1) to W(100):

$$W(100) = 18.8 \mu s + (100/50) * 14 \mu s + (100/50) * 14 \mu s + (100/20) * 22.3 \mu s \\ + (100/10) * 5ms \\ = 18.8 \mu s + 28 \mu s + 28 \mu s + 111.5 \mu s + 50ms \\ = 50.186 < 100, \text{ So Task 3 is Schedulable.}$$

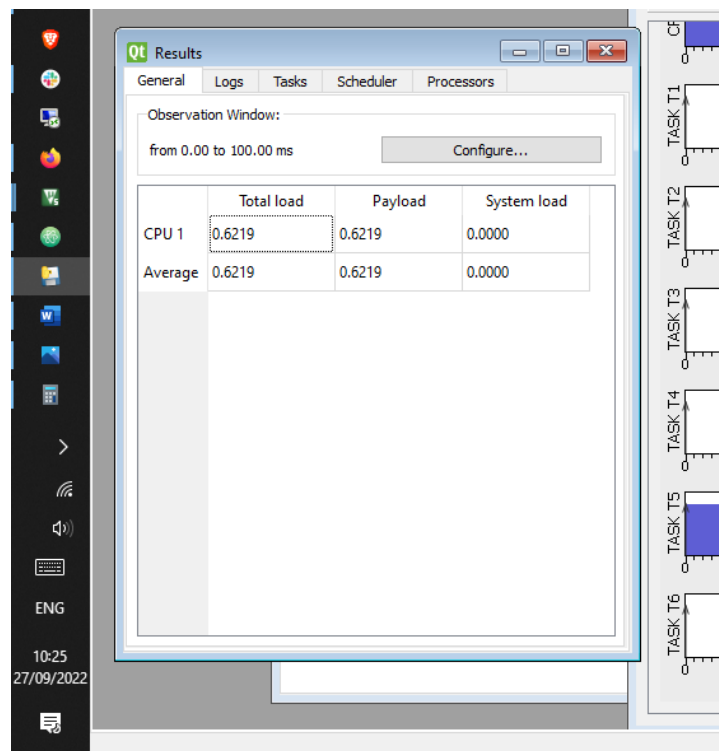
6. Task6 load_2_simulator w(1) to w(100):

$$W(100) = 12ms + (100/100) * 18.8 \mu s + (100/50) * 14 \mu s + (100/50) * 14 \mu s \\ + (100/20) * 22.3 \mu s + (100/10) * 5ms \\ = 12ms + 18.8 \mu s + 28 \mu s + 28 \mu s + 111.5 \mu s + 50ms \\ = 65.186 < 100, \text{ So Task 6 is Schedulable.}$$

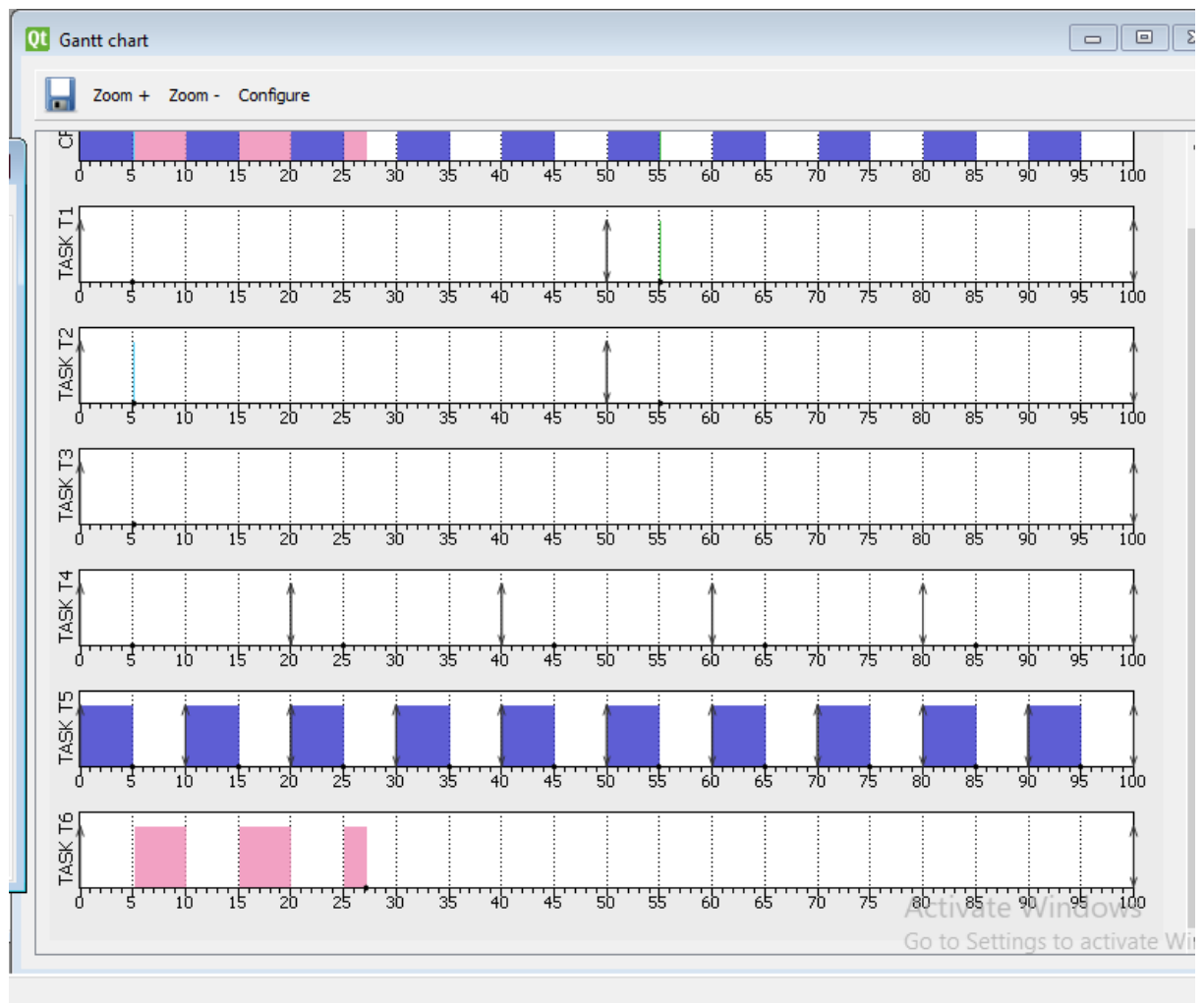
2. Using Simso offline simulator, simulate the given set of tasks assuming:



- CPU Load:



- Gantt chart



- CPU Load and Logic Analyzer

The CPU load is 62% as showed, so the system is not too much loaded and worked successful.

