

Spring 2022 EE214 Experiment 1

Diodes and Rectifiers

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1 Introduction

In this experiment, the characteristics of different diodes and rectifiers are investigated. First, the i-v characteristics of 3 different diodes are expected to be observed. Then, the behavior of the half-wave is expected to be experimented with. Lastly, observations are made on clamper and zener regulator circuits. The results of the experimentation are presented in this document.

2 Experimental Results and Discussion

The results of the experiment are discussed in the following steps.

2.1 Step 1

In this step, the circuit schematic given in Figure 1 is constructed on the breadboard. The analog signal generator is used for floating output as the signal supply.

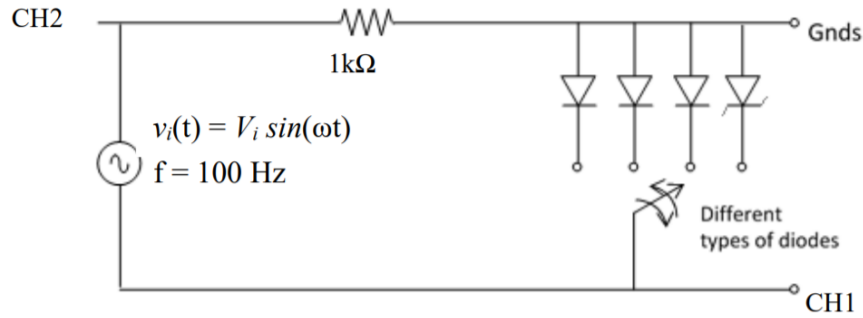


Figure 1: Circuit schematic for the step 1

2.1.1 a)

The diode models, [AA119](#), [BA159](#), and [BZX55C-6V2](#) are used. The probes of the oscilloscope is connected to the nodes indicated in Figure 1. The resulting graph is plotted as given in Figure 2, 3, and 4 for [AA119](#), [BA159](#), and [BZX55C-6V2](#) respectively.

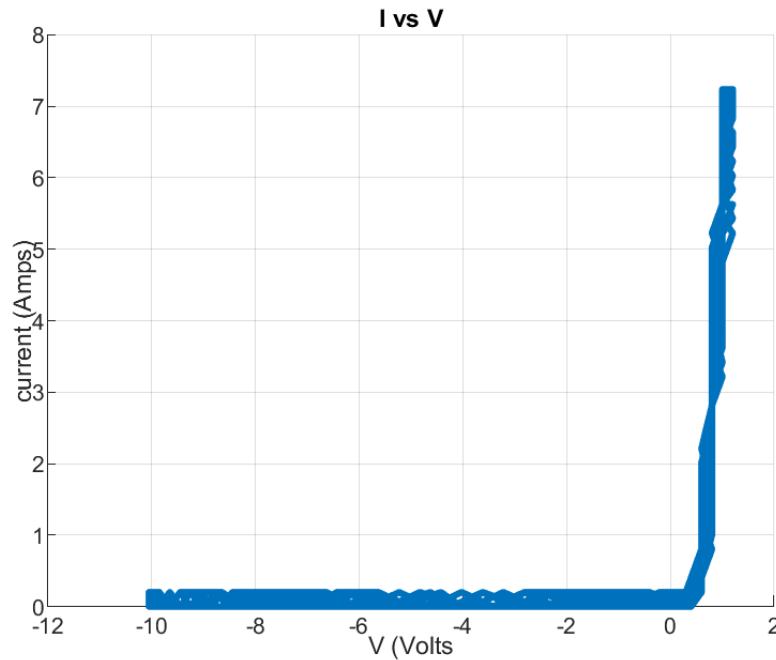


Figure 2: i-v characteristics of AA119

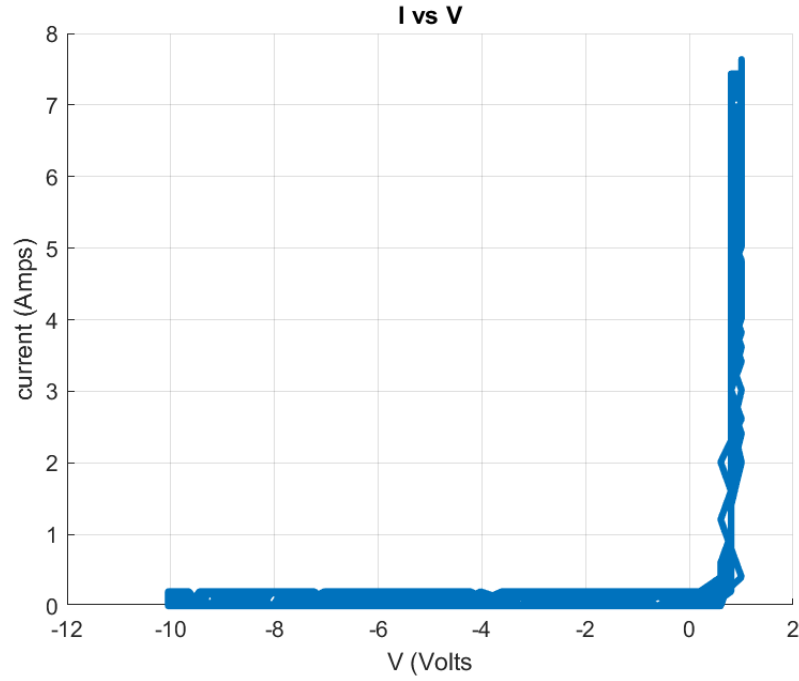


Figure 3: i-v characteristics of BA159

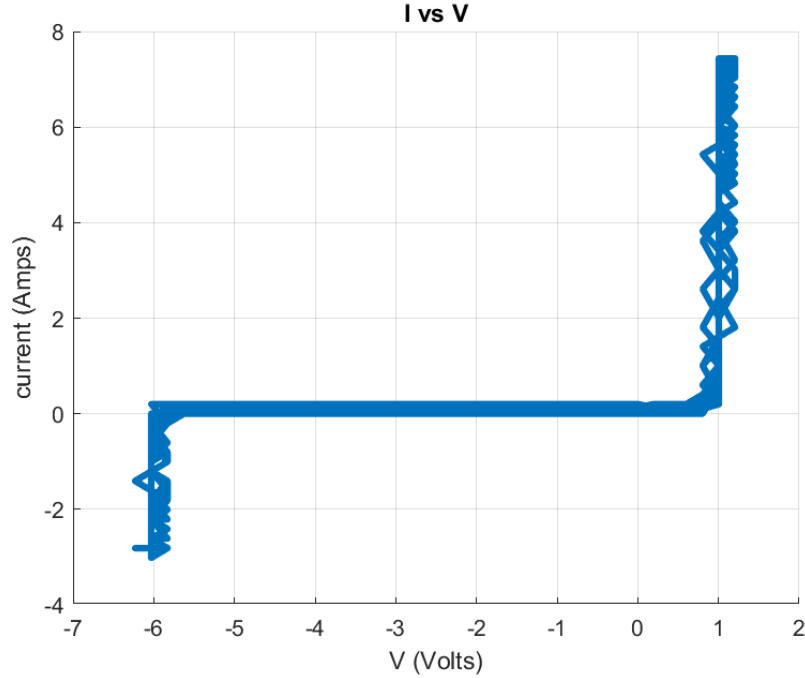


Figure 4: i-v characteristics of BZX55C-6V2

Using those plots, the piecewise parameters of the diodes are obtained by virtue of the cursors of the oscilloscope. The parameters of diode AA119 are given in Table 1.

Table 1: Piecewise parameters of diode AA119

V_{on}	350 mV
r_f	0.17 Ω
r_r	86 Ω

The obtained parameters of diode AA119 are given in Table 2.

Table 2: Piecewise parameters of diode BA159

V_{on}	973.5 mV
r_f	0.625 Ω
r_r	86.2 Ω

The obtained parameters of diode BZX55C-6V2 are given in Table 3.

Table 3: Piecewise parameters of diode BZX55C-6V2

V_{on}	752 mV
V_z	5.92V
r_f	0.05 Ω
r_r	0.156 Ω

So, the fundamental i-v characteristics of 3 different diodes are obtained and analyzed using the plot.

2.1.2 b)

In this part, the frequency of the signal generator is adjusted, and the i-v characteristics are observed again. The plot given in Figure 5 is obtained.

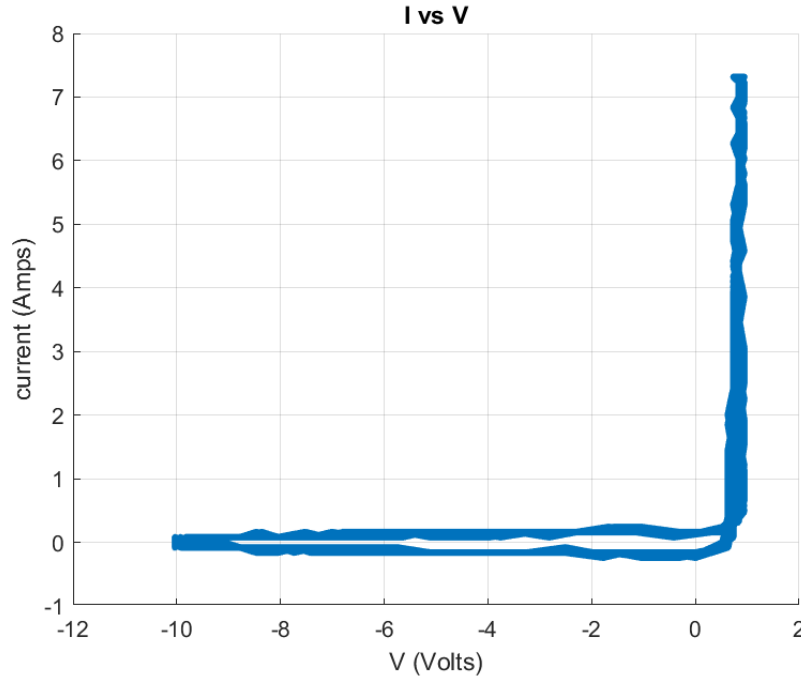


Figure 5: i-v characteristics of BA159 at 10khz

It can be said that there is a slight separation on horizontal lines in a plot different than the lower frequency plot. We can call the separation of lines in this i-v graph as hysteresis effect. When we increased the frequency to 10kHz, we observed a hysteresis effect on the i-v characteristics of the diode on the DSO screen. We observe this effect since our diode can not change its state as fast as our high-frequency voltage source supply. To solve this problem and to be able to use diodes at high frequencies, there are "high speed" or "switching" diodes. These diodes can be used in high frequencies without observing the hysteresis effect.

2.2 Step 2

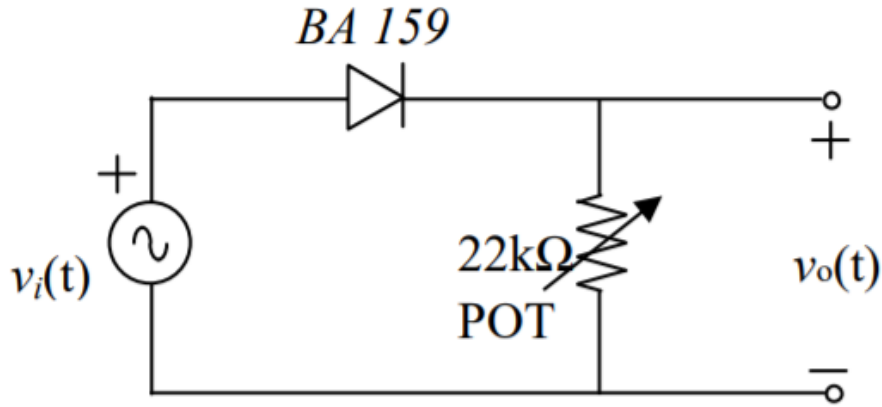


Figure 6: Circuit schematic for the step 2

For this part, we set the half-wave rectifier circuit given in Figure 6 and adjusted signal generator as $v_i(t) = 2\sin(2000\pi t)V$

2.2.1 a)

POT is set to $1k\Omega$, and output and input voltage waveforms on the graph indicated in Figure 7 is obtained.

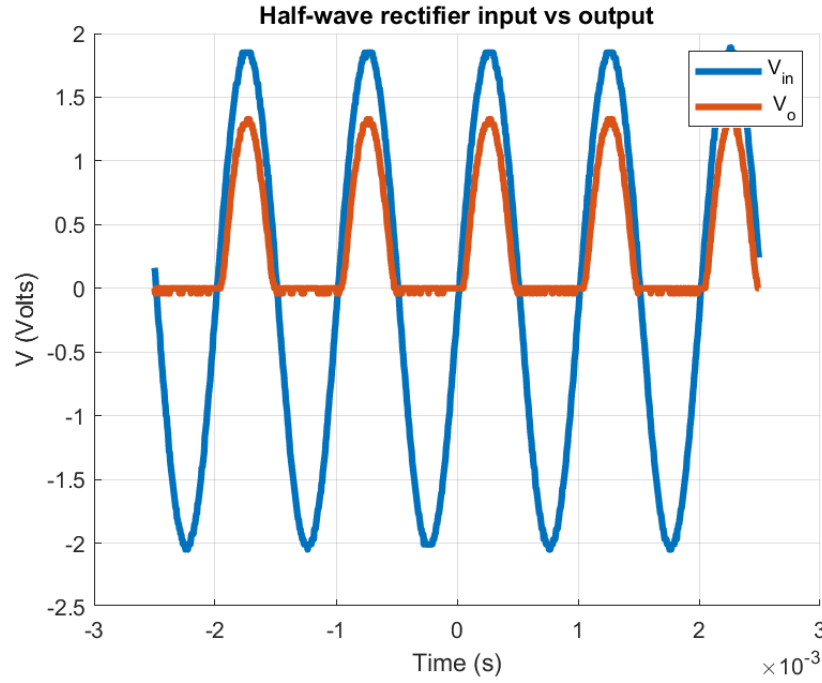


Figure 7: Half-wave rectifier with BA159

If one looks at the graph carefully, it can be realized that the output voltage is slightly lower than the input voltage since the diode consumes energy, so it has an internal resistance. Then we measured the DC_{rms} of output value as 730mV using cursors of DSO. The output voltage waveform on the DSO screen is in a similar shape to the input waveform in positive cycles but zero when input is in the negative cycle.

2.2.2 b)

In this part, in addition to the circuit given in Figure 6, a capacitor of $10\mu F$ is connected parallel to the potentiometer. Also the input signal is adjusted to $10\sin(20\pi t)$. First, POT is set to $10k\Omega$, and output and input voltage waveforms on the graph presented in Figure 8 are obtained.

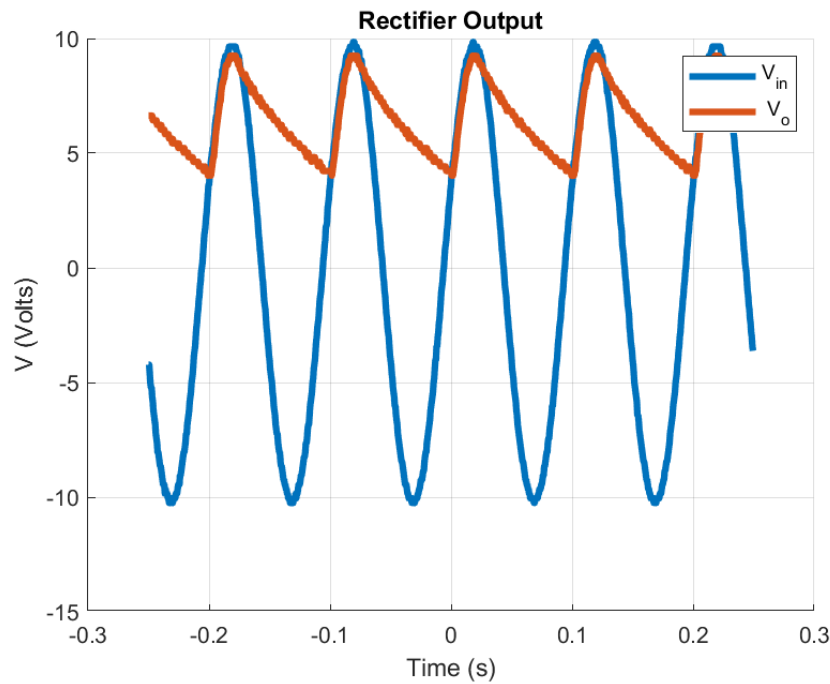


Figure 8: Half-wave rectifier with BA159 with pot set to 10K Ω

For this plot, it can be said that because of the discharging process of the capacitor, the falling edge of the output signal is different from the case without a capacitor. The pot is adjusted to a maximum ($18K\Omega$), and the plot given in Figure 9 is obtained.

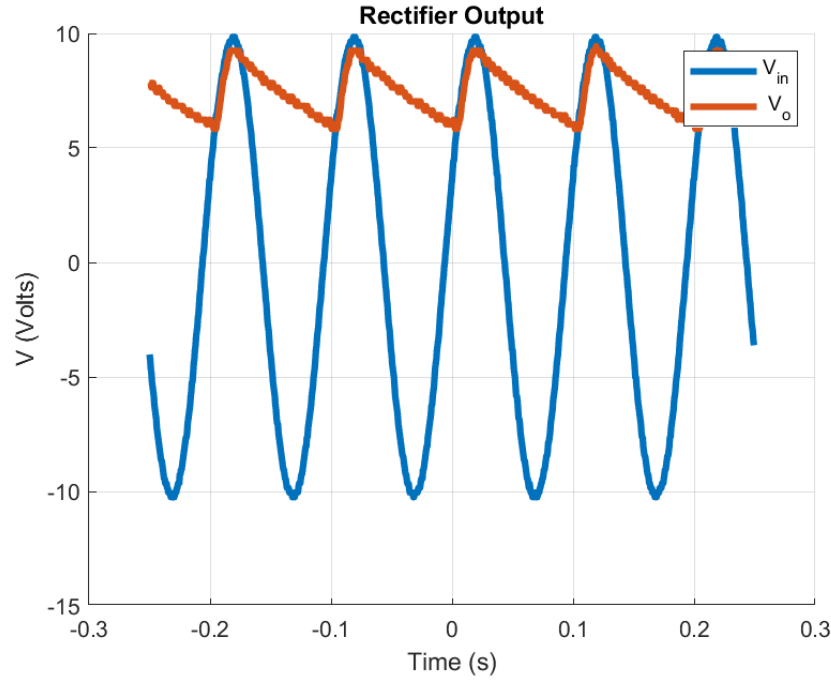


Figure 9: Half-wave rectifier with BA159 with pot set to 18K Ω

Here it can be said that ripple voltage is decreased as it moved from 10K Ω . Lastly, the potentiometer is set to a minimum (1.2K Ω). The plot given in Figure 10 is obtained.

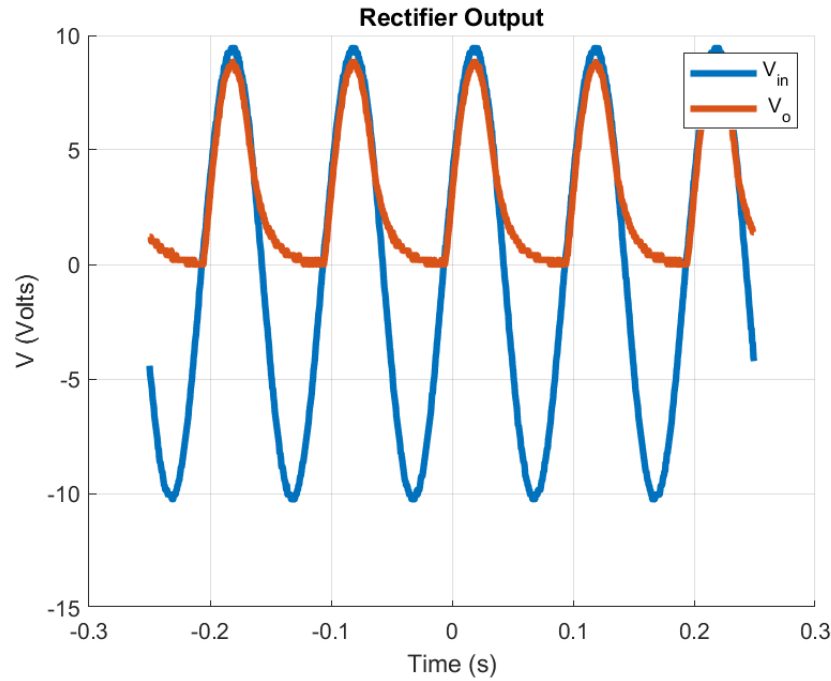


Figure 10: Half-wave rectifier with BA159 with pot set to 1.2K Ω

Here the ripple voltage is higher than in other cases and equal to the case in which the capacitor is absent.

As a result, the V_{DC} , V_r values are recorded and given in Table 4.

Table 4: Piecewise parameters of diode BZX55C-6V2

R (Ohms)	C (μ Farads)	V_{DC} (Volts)	$V_{r,pp}$ (Volts)
10K	10	7.03	5.2
18K	10	6.93	4.8
1.2K	10	7.05	8.8

To decrease the ripple voltage in the case of constant resistance, there are three options that can be done. First, the capacitance of the capacitor can be increased so that it would take longer for the resistor to consume its energy. Secondly, if the frequency of the input is increased, the time interval of discharging drops, so ripple voltage may decrease. Lastly, even though it is not feasible, a diode with a higher internal capacitance can be used to decrease ripple voltage.

2.3 Step 3

In this third step, we set up the diode clamper circuit given in Figure 11 using the diode 1N4001 and set the voltage $v_i(t) = 10\sin(200\pi t)V$. Then it is plotted the input and output voltage waveforms on the graph given in Figure 12.

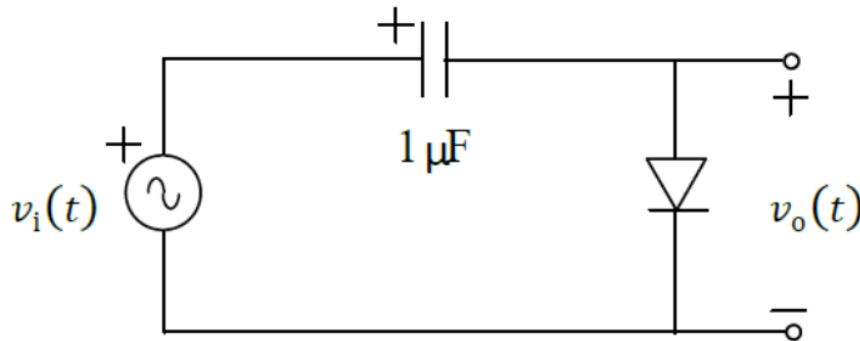


Figure 11: Circuit schematic for the step 3

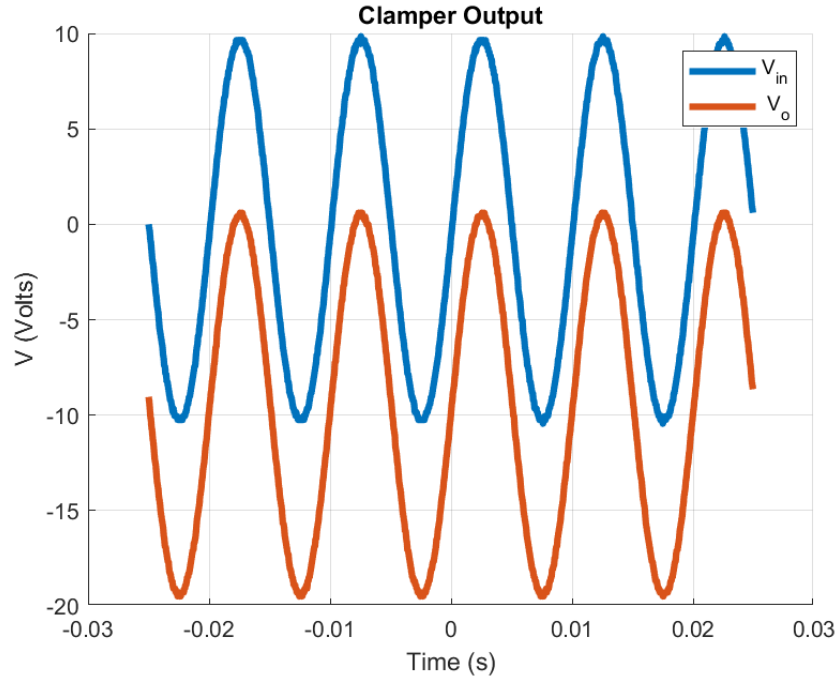


Figure 12: Clamper circuit output

From the above graph, we can derive that the output waveform is slightly shifted from of input waveform to below zero. Since the diode prevents the capacitor from being discharged, the peak value of output to be clamped above zero diodes should go into negative bias.

2.4 Step 4

In this last part, we set the circuit given in Figure 13 with the load resistance ($100\Omega + 1k\Omega POT$) where R_L varies between 600Ω - 1100Ω . While we are changing potentiometer resistance from minimum to maximum (100 to 1000Ω), the first output voltage has started to increase from $4V$ to $6V$, and then, We observed a constant output voltage of $6V$ even as we continued to increase R_L resistance. From these results, we can conclude that the zener diode gives constant output in reverse bias high voltages. Hence, zener diode has a regulation effect.

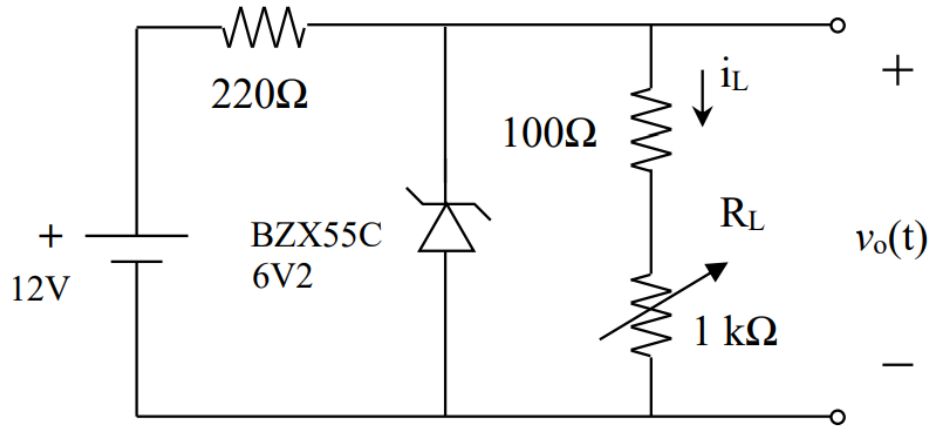


Figure 13: Circuit schematic for the step 4

3 Conclusion

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Appendix A

- PreLab Preprataion 6 hours
- Experimental Work 2 hours
- Report Writing 6 hours