

Spring 2022 EE214 Project Work

Final Report

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1 Introduction

In this document we present our work done to satisfy the requirements of the Term Project of EE214 laboratory course. According to the project description we are supposed to make a transmitter which produces different frequency tones (pure sinusoids) and collect them together, and a receiver which can be tuned to these frequencies. The received tone will be played with a speaker. So, in order to overcome the challenge of generating sinusoids with needed frequencies the square wave generator circuits are used. In the receiver unit , the bandpass filter with topology called dual-opamp is used as planned in the Preliminary Report. For the speaker unit a simple opamp amplifier is utilized.

2 Transmitter Unit

In the transmitter unit, sinusoidal signals with frequencies $1kHz$, $2kHz$, $3kHz$, $5kHz$, $6kHz$, $7kHz$, $9kHz$, $10kHz$, $11kHz$, $13kHz$, $14kHz$, $15kHz$ are aimed to be produced and then collected into a single waveform by avoiding to produce $4kHz$, $8kHz$, $12kHz$ frequencies.

2.1 Astable Multivibrator

In order to obtain a waveform that includes these all 12 sinusoids with different frequencies, $1kHz$ and $2kHz$ square waves are considered to be summed up based on the idea fourier transformation. The reason behind this is that the summing of sinusoids with frequencies $1kHz$, $3kHz$, $5kHz$, $7kHz$, $9kHz$, $11kHz$, $13kHz$, $15kHz$ results in $1kHz$ square wave waveform and similarly summing of the rest of the sinusoidal signals that must be produced is result in a $2kHz$ square wave signal. For this to be achieved, 2 square wave generator circuits were designed to generate $1kHz$ which is given in Figure 1, and $2kHz$ given in Figure 2 square waves. Since higher frequencies that constitute the square wave are low in their magnitude, generating and filtering them precisely is harder. Therefore, for example, one needs to generate $1kHz$ square wave precisely to be able to include a nice $13kHz$ and $15kHz$ sine waves. Also in order to be able to generate a square wave with smaller rise and fall time tl071 op-amp ([datasheet](#)) which has a big slew rate is used. Additionally for the same reason BS-170 ([datasheet](#))N channel enhancement mode MOSFETs are used as an inverter.

Frequency of the generated signal is calculated as follows when the necessary analysis is made:

$$\frac{1}{2RC}$$

where R is equivalent resistance between output and non-inverting terminals of the op-amp.

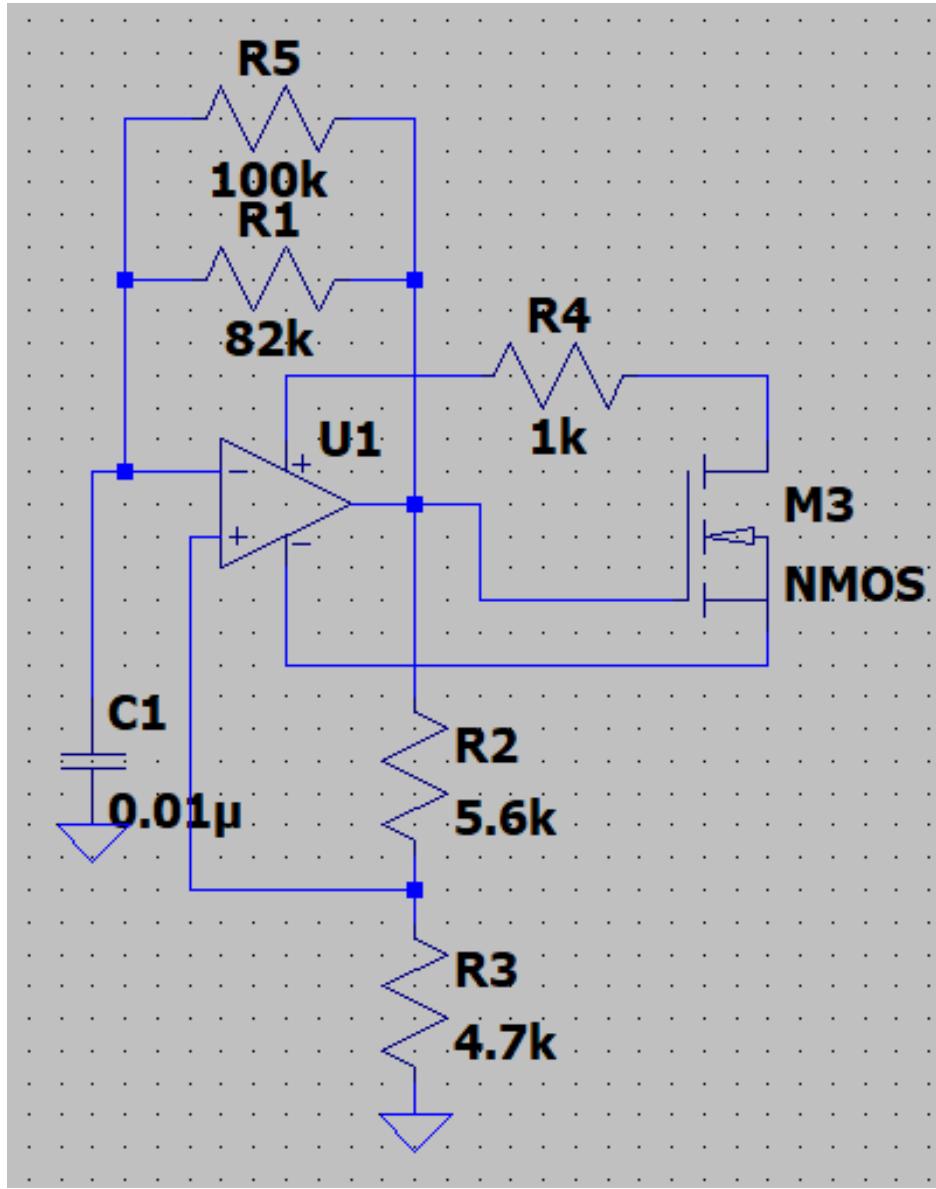


Figure 1: Op-amp Multivibrator for 1kHz

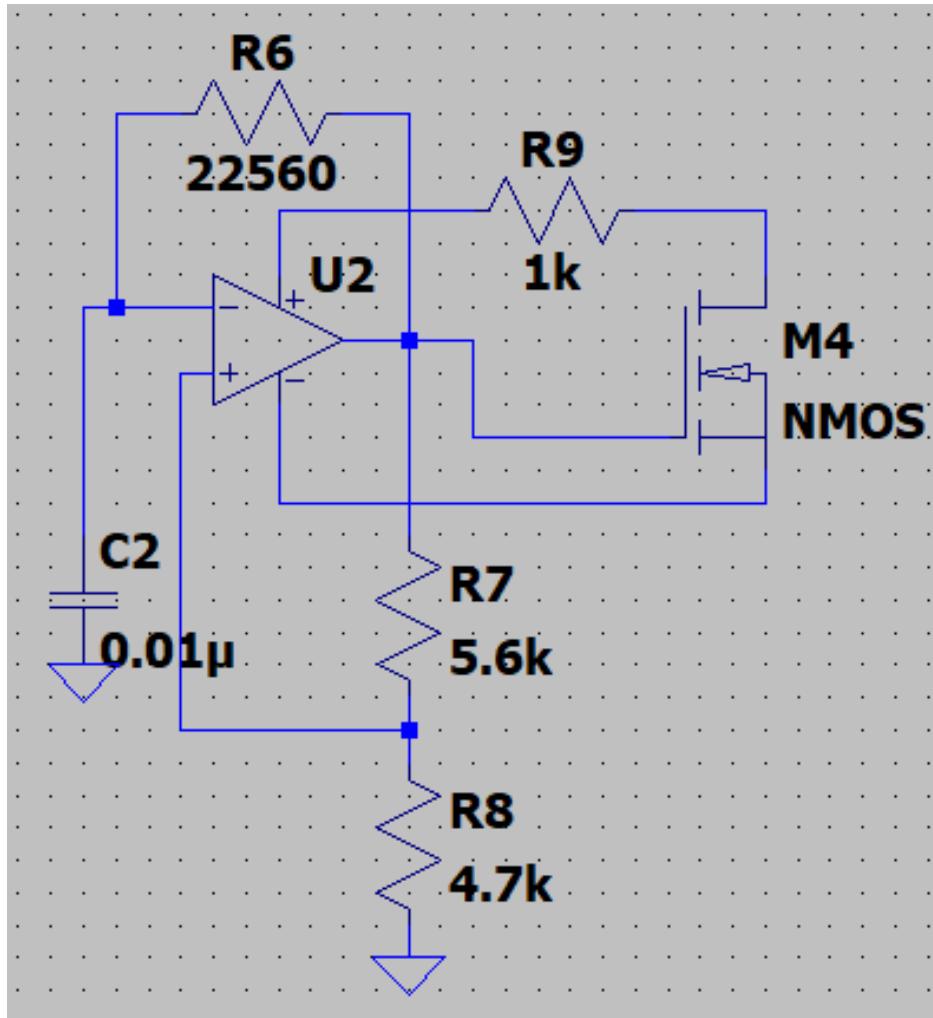


Figure 2: Op-amp Multivibrator for 2kHz

2.2 Summing Amplifier

After generating two square waves with the necessary sines, there needs to be a summing amplifier to obtain one single waveform. This is made by setting a summing op-amp circuit given in Figure 3 which gives an output equals to $-\frac{10k\Omega}{2.2k\Omega}(V_1 + V_2)$ where V_1 and V_2 are the outputs of the signals with 1kHz and 2kHz frequencies respectively.

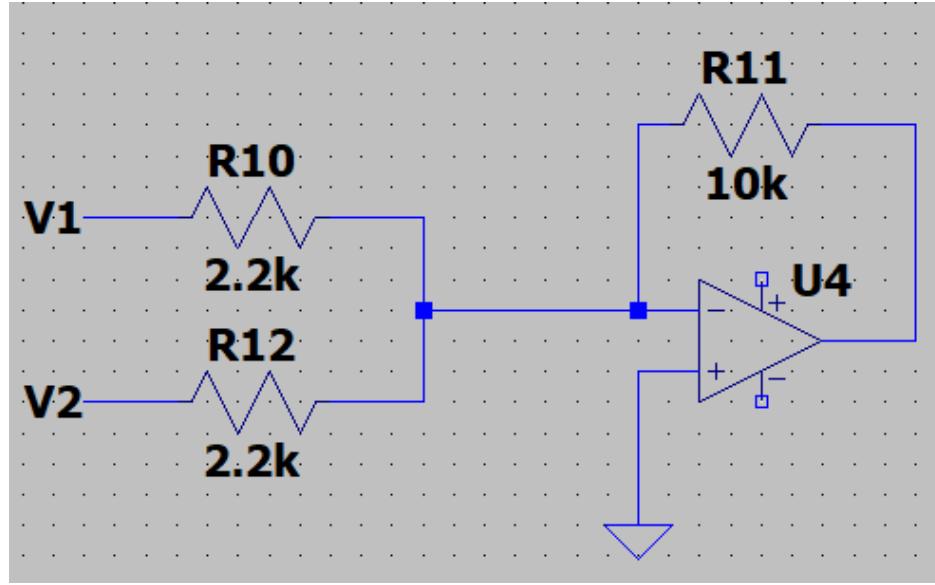


Figure 3: Summing Amplifier Circuit

3 Receiver Unit

In the receiver unit design phase, the design path described in the Preliminary report is followed. The reference point for the filter design was primarily the [analog filter design guide](#) prepared by Analog Devices company.

3.1 Dual-Opamp Topology

The reason why the dual-opamp bandpass filter is used is there are only two resistors that adjust the frequency, and fixing the value of the one is acceptable. Also, the quality factor can be adjusted almost independently from the resonant frequency. The transfer function of this topology can be expressed as follows;

$$\frac{H\omega_0^2}{s^2 + \alpha\omega_0 s + \omega_0^2}$$

The general schematic of the topology is given in Figure X .

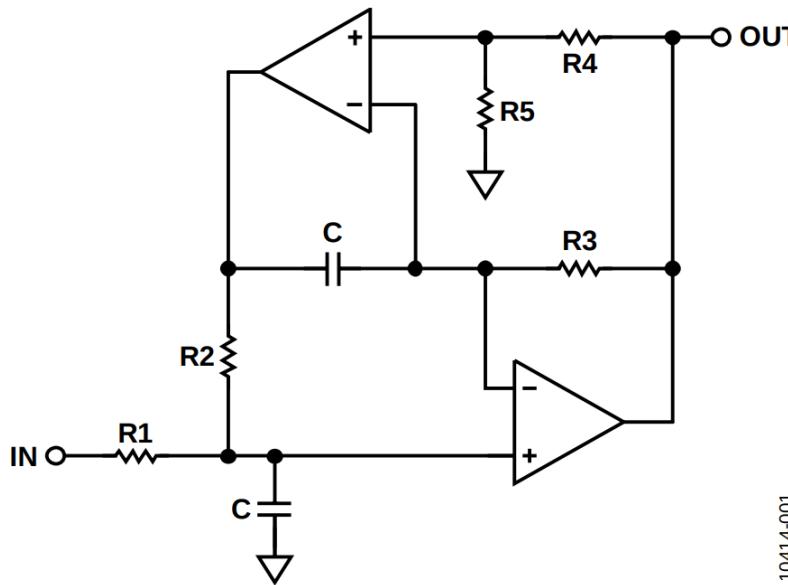


Figure 4: Dual-opamp topology schematic

3.2 Compononent Selection and Simulation Results

To be able to adapt the circuit efficiently to our purpose following goals are set. The design tables for the standard responses are not used to choose the component values freely. Instead, the responses are obtained through multiple simulations.

- The values of the passive components should be easy to supply.
- The R3 should be fixed so that the highest Q is obtained for 14-15 kHz frequencies since the harmonics of the transmitter signal are quite small compared to the 1 and 2 kHz.

So the values of R4 and R5 are fixed to 1k, and R1 is fixed to 100K. The capacitors are fixed to 10nF. Then series of simulations are made to find a suitable value for R3, which is fixed to 1K for better amplification of 14-15 kHz signals. Lastly, the values of the R2 are fine-tuned for every frequency. This is also important to determine which range of potentiometers to be used. The circuit schematic of the receiver is given in Figure X.

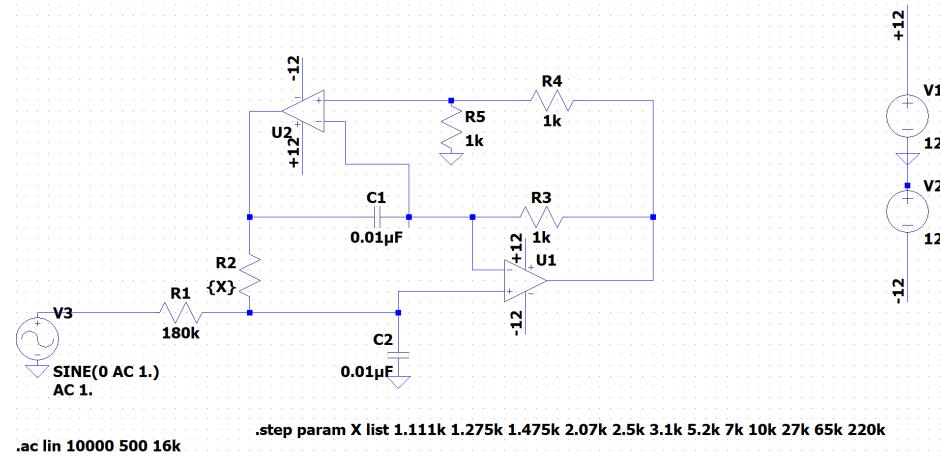


Figure 5: Receiver circuit schematic.

The frequency response for the receiver circuit obtained in LTSpice is given in Figure X.

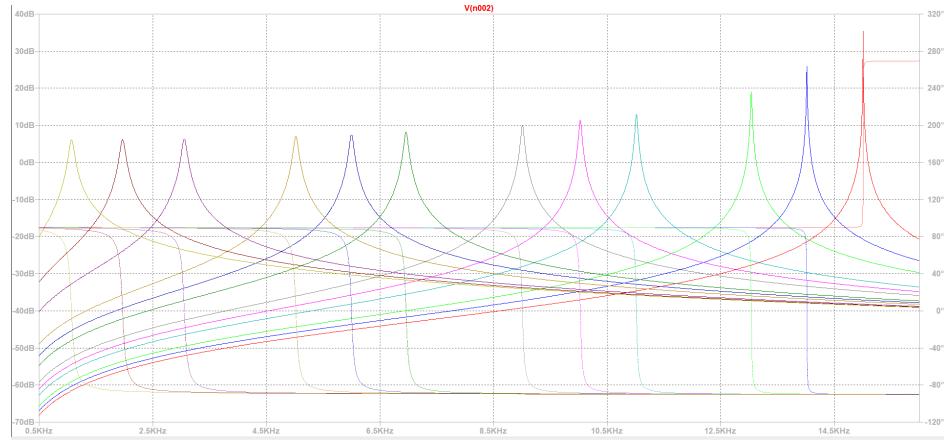


Figure 6: Receiver circuit frequency response.

4 Physical Prototype and Results

To be able to construct the designed circuit LM358 ([datasheet](#)) two opamp packaged in an ic) are used. To ensure an average of two opamp ic is sufficient to accomplish the needed filtering, double opamp ic's with the same pinout TL072 and TL082 are tested and obtained the same result. The physically prototyped version of the circuit is given in Figure X.

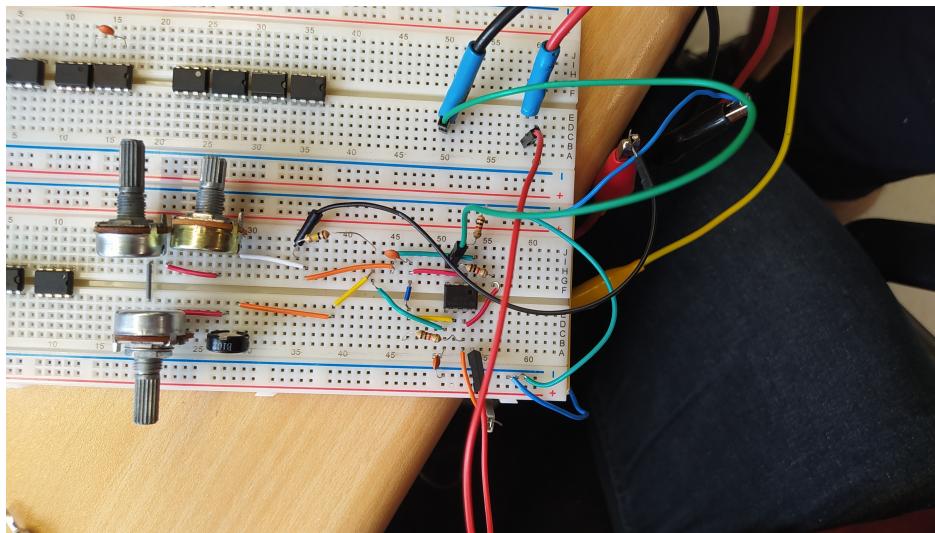


Figure 7: Receiver Unit Breadboard

Four potentiometers with 1K, 10K, 50K, and 250K are used in series for R2 to fine-tune the middle frequencies. So the one potentiometer bonus is achieved. As a result, an analog bandpass filter with adjustable middle frequency is obtained successfully. The filter is able to filter all needed signals accurately with approximately 30-40 dB difference. The output amplitude of the filter ranges from 6 Vpp to 2Vpp.

5 Speaker Unit

For the speaker unit, a simple approach is followed. Since the transmitter has decreasing harmonics amplitude and the receiver has increasing gain for increasing frequencies, the speaker amplification need not be higher than 2X. Also, a headphone speaker with 32R impedance is used, which has not needed for high current flow. As a result, an inverting opamp amplifier is used. The schematic is given in Figure X.

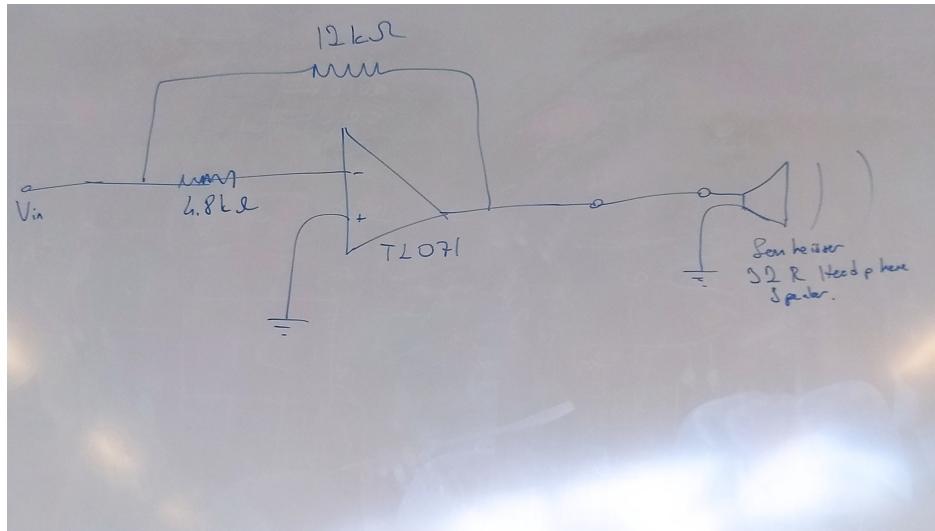


Figure 8: Speaker unit schematic

As a result, all needed signals are quite audible with (almost) sine tonality.

6 Conclusion

To conclude, EE214 Term Project is presented in this document by explaining generating, collecting, and filtering process of sinusoidal signals with 12 different frequencies. In order to obtain well results more appropriate components are preferred and additive circuit designs (such as MOSFET inverter) are used in order to obtain nice signals. Afterward, a filter circuit with dual-opamp topology is set. Lastly, a speaker unit is set using an amplifier circuit since there needs a matching between impedances of speaker input and the rest of the circuit.