**HACETTEPE UNIVERSITY**

**ELECTRICAL AND ELECTRONICS ENGINEERING**

**ELE227 FUNDAMENTALS OF DIGITAL SYSTEMS LABORATORY  
FALL 2019**

**TEMPLATE for a QUESTION on PRELIMINARY WORKS**

**(Size of blank areas is changeable according to your code size, RTL schematic size etc.)**

**1) Circuit Diagram**

**Draw your circuit block diagram here by your hand writing.**

**VHDL Implementation:**

**VHDL code goes here!**

**VHDL code goes here!**

**RTL Schematic:**

**Put your RTL schematic here. It should be readable!!**

**Test Bench Input:**

Test bench code goes here!

Test bench code goes here!

**Test Bench Result:**

**Put Test Bench reuslt graph into this area and make some comments.**