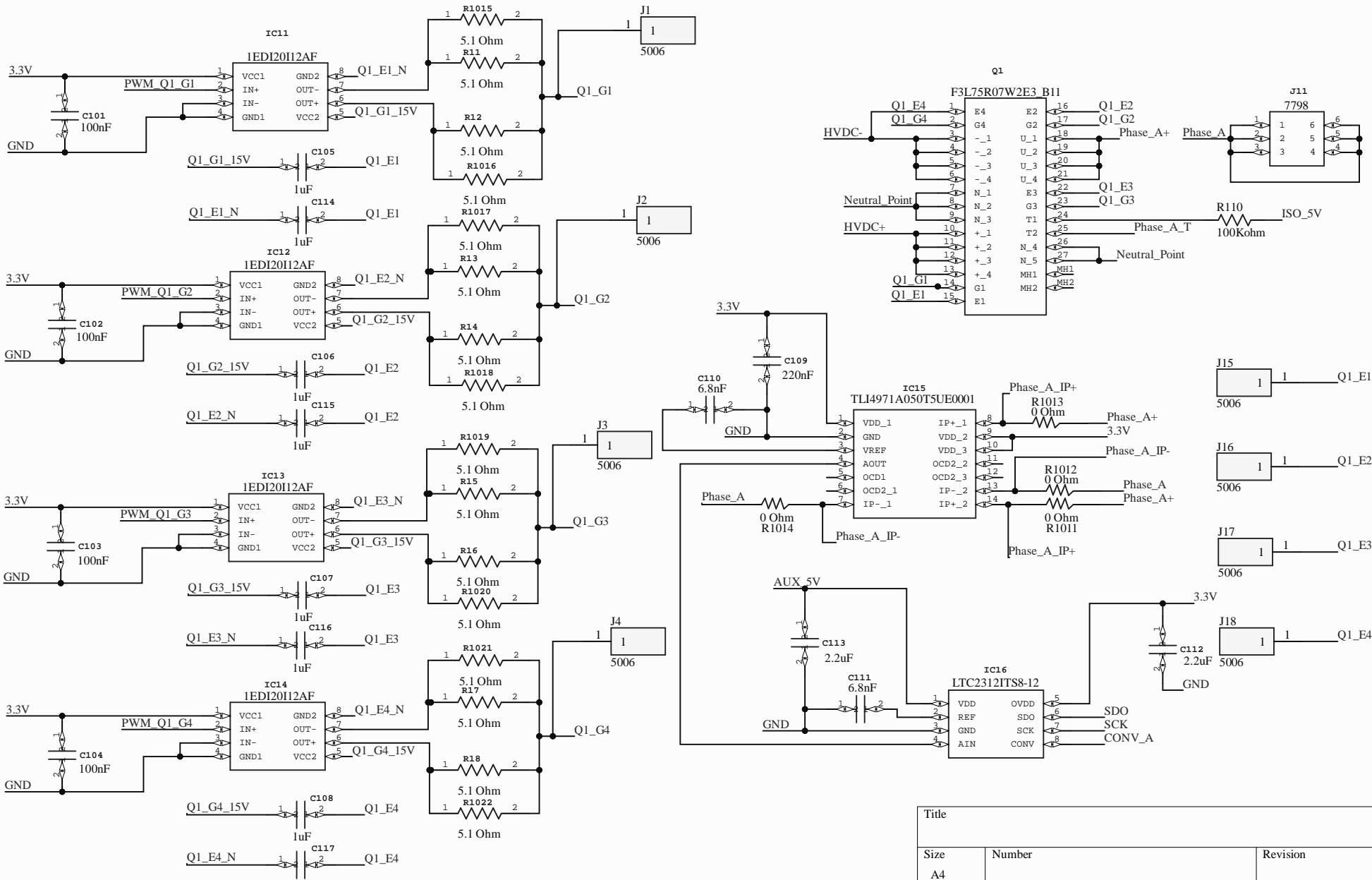


PHASE A MODULE, DRIVERS & CURRENT MEASUREMENT



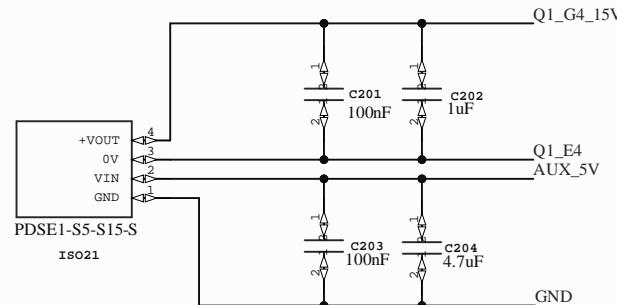
Title

Size	Number	Revision
A4		
Date:	1.31.2024	Sheet of
File:	C:\Users\...\S101_PhaseA_Module_and_DriverSchematicDoc	

PHASE A GATE DRIVERS' ISOLATED DC/DC CONVERTERS

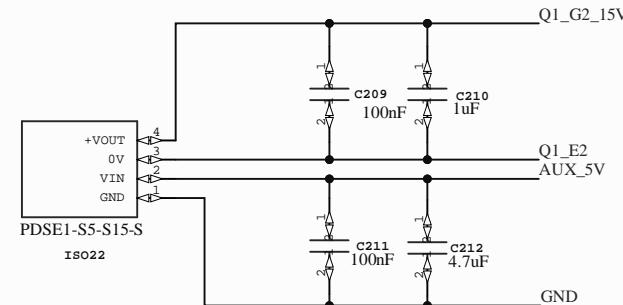
A

A



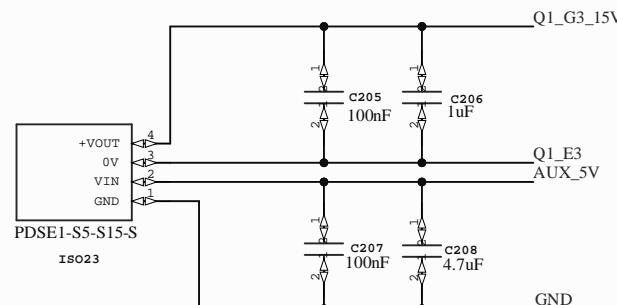
B

B



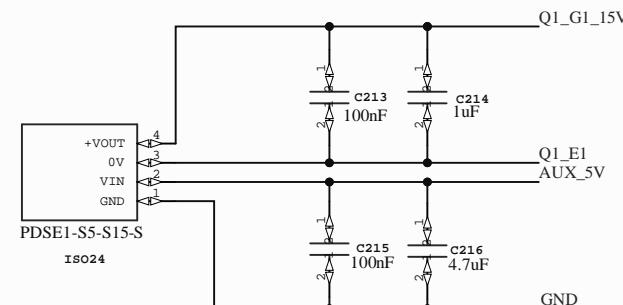
C

C



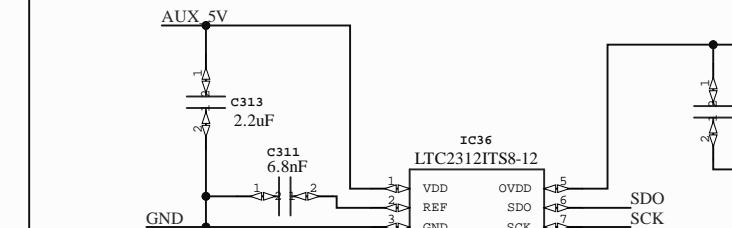
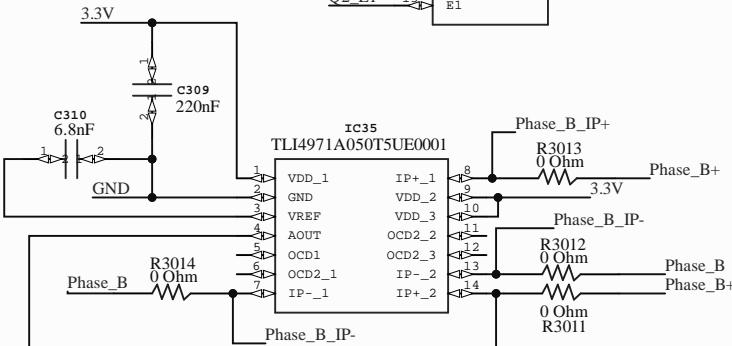
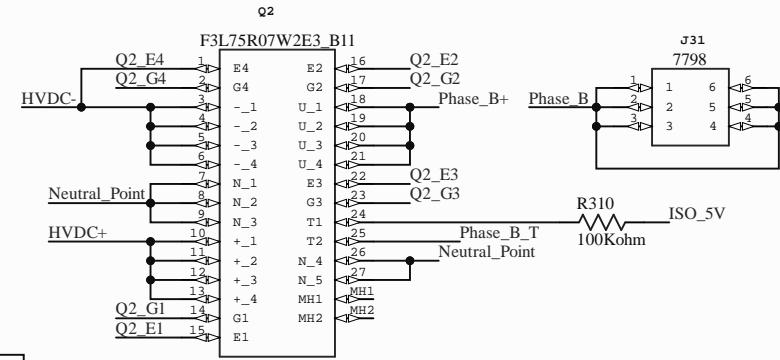
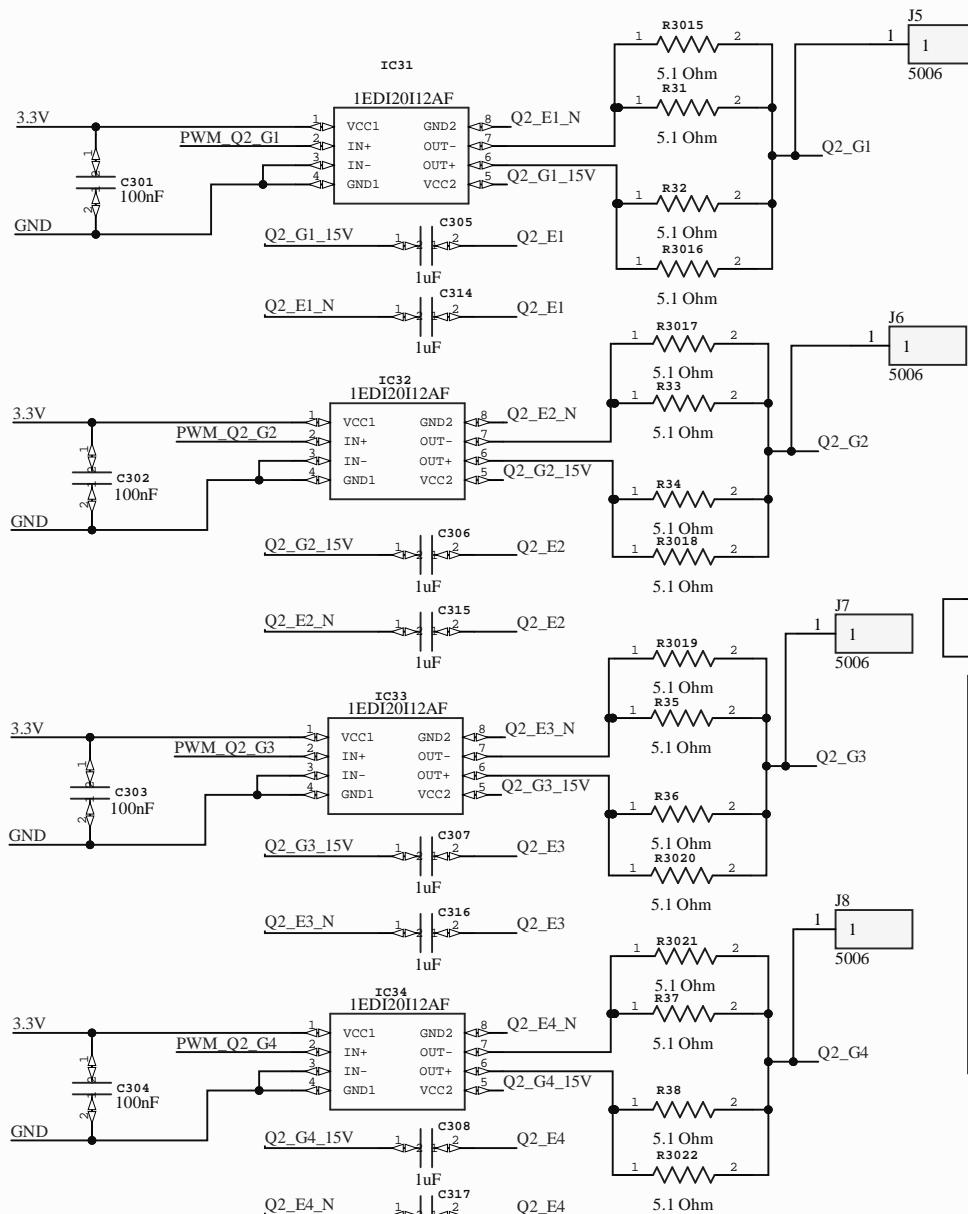
D

D



Title		
Size	Number	Revision
A4		
Date: 1.31.2024	Sheet of	
File: C:\Users\...\S102_PhaseA_Isolated_ConvertersSchDoc		

PHASE B MODULE, DRIVERS & CURRENT MEASUREMENT

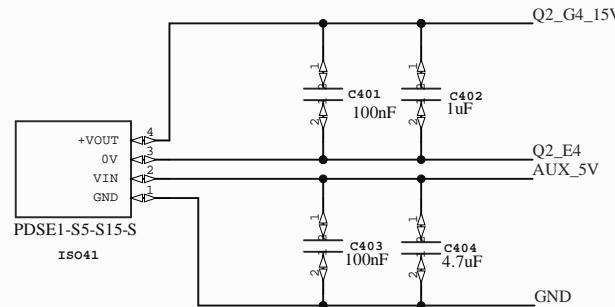


Title		
Size	Number	Revision
A4		
Date:	1.31.2024	Sheet of
File:	C:\Users\...\S103_PhaseB_Module_and_Driver\Doc	

PHASE B GATE DRIVERS' ISOLATED DC/DC CONVERTERS

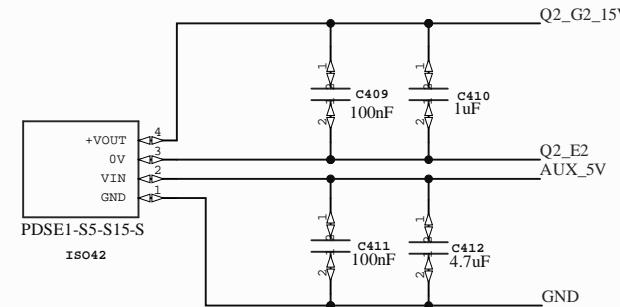
A

A



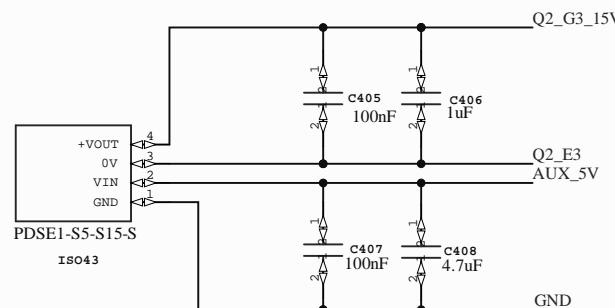
B

B



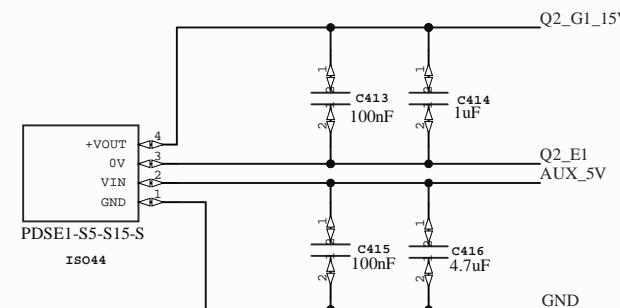
C

C



D

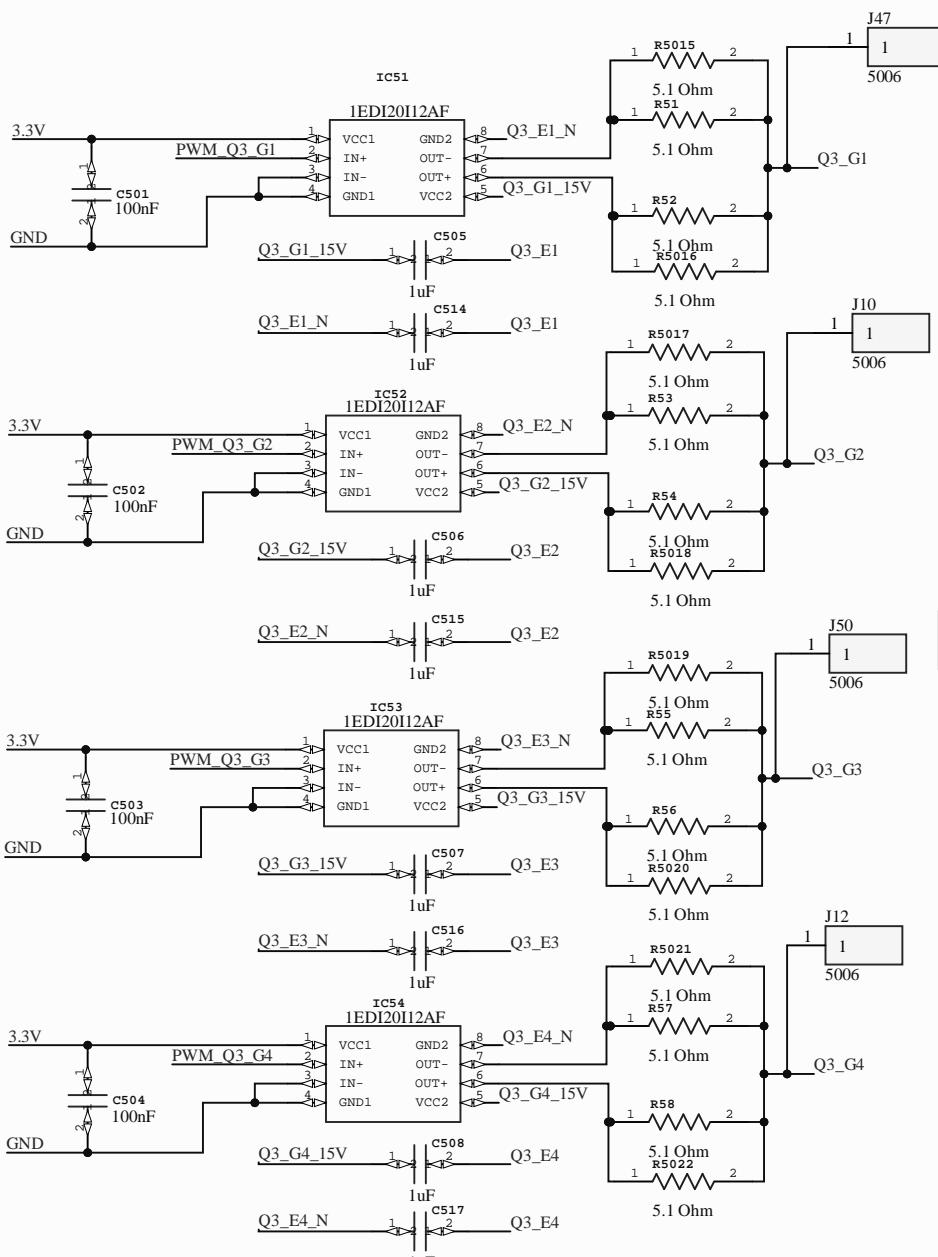
D



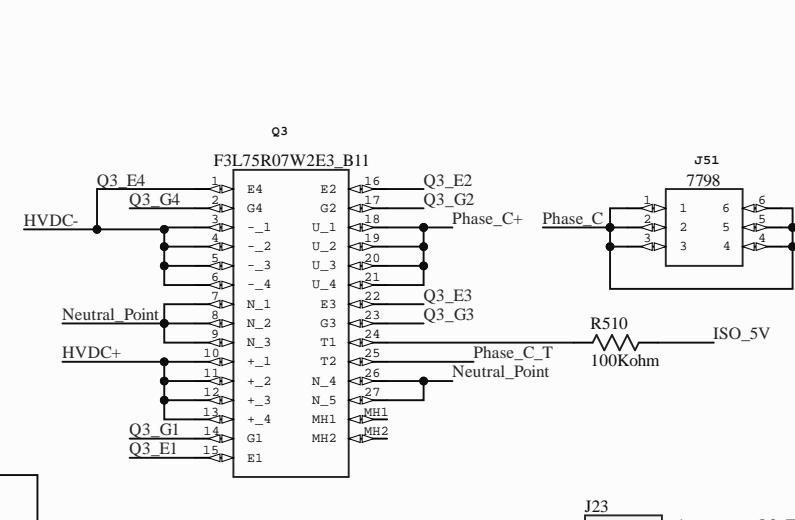
Title		
Size	Number	Revision
A4		
Date: 1.31.2024	Sheet of	
File: C:\Users\...\S104_PhaseB_Isolated_Converters_SchDoc		

PHASE C MODULE, DRIVERS & CURRENT MEASUREMENT

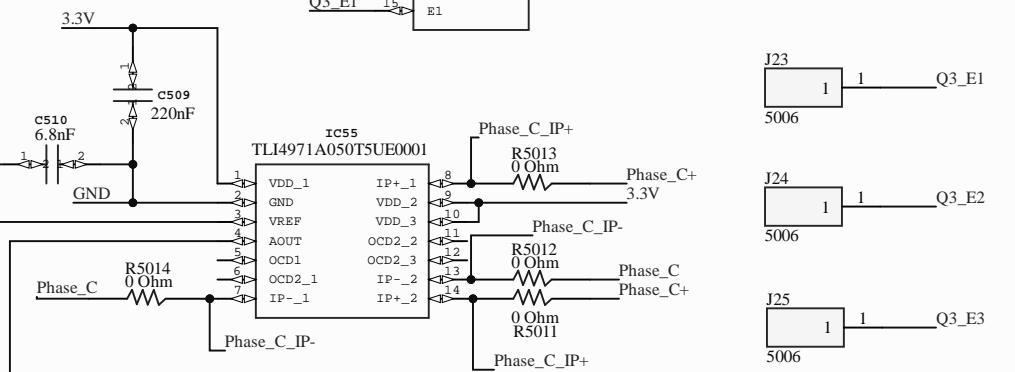
A



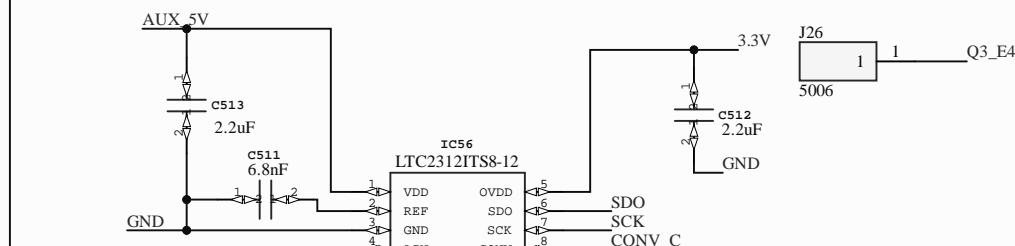
B



C



D

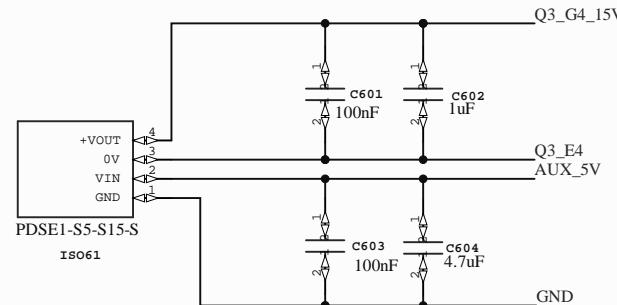


Title

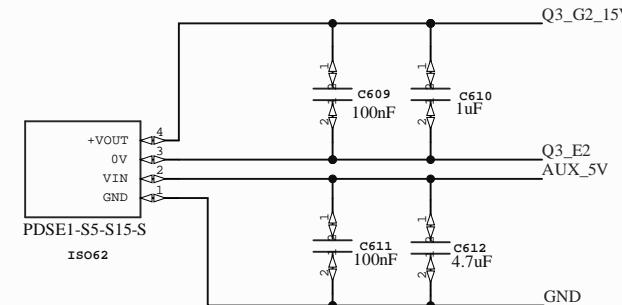
Size	Number	Revision
A4		
Date:	1.31.2024	Sheet of
File:	C:\Users\...\S105_PhaseC_Module_and_Driver\Drawings\Doc	

PHASE C GATE DRIVERS' ISOLATED DC/DC CONVERTERS

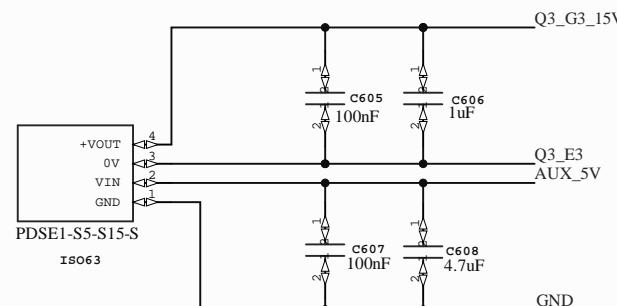
A



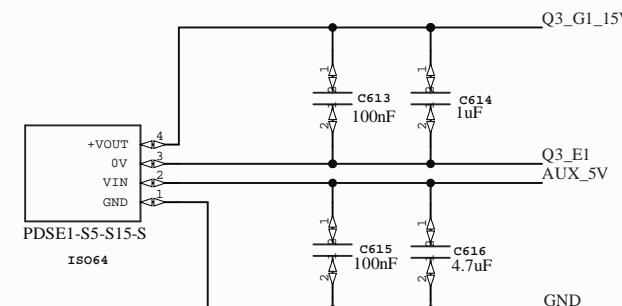
B



A



C



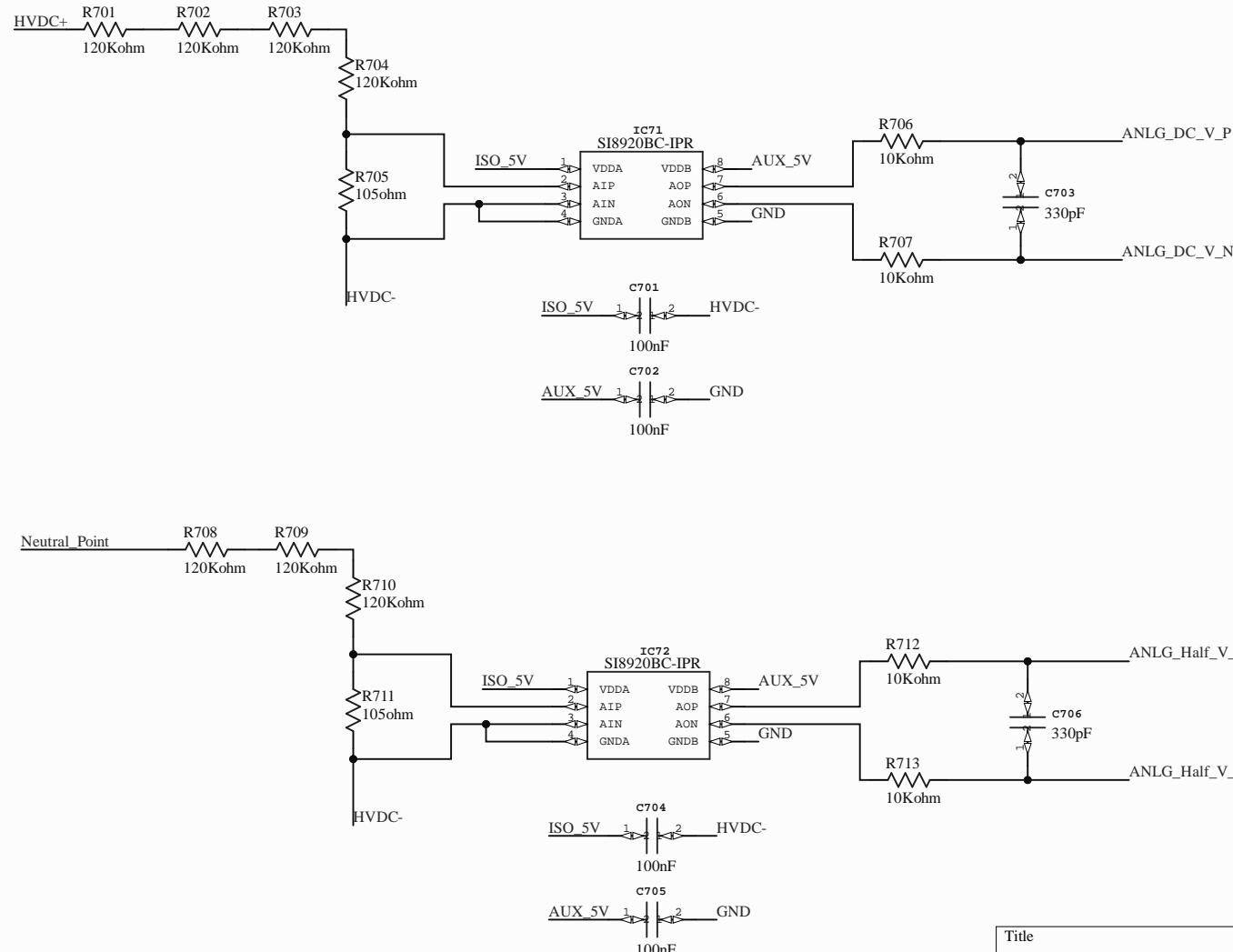
B

C

D

Title		
Size	Number	Revision
A4		
Date: 1.31.2024	Sheet of	
File: C:\Users\...\S106_PhaseC_Isolated_Converters_SchDoc		

INPUT & NEUTRAL POINT VOLTAGE MEASUREMENT



Title		
Size	Number	Revision
A4		
Date: 1.31.2024	Sheet of	
File: C:\Users\...\S107_Voltage_Monitor.SchD	Drawn By:	

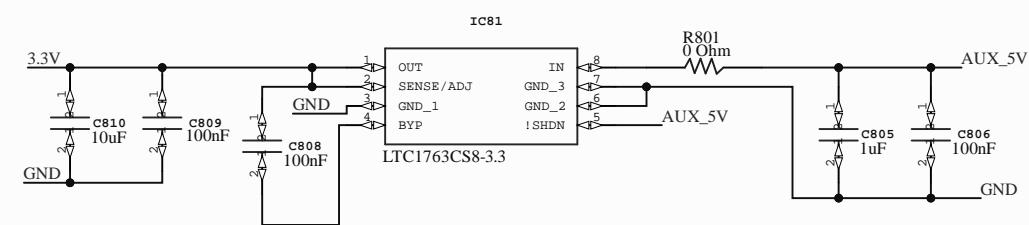
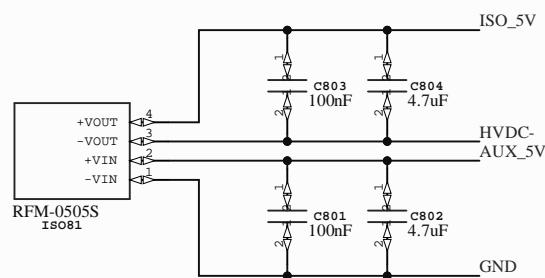
A

A

INTERNAL DC/DC CONVERTERS

B

B



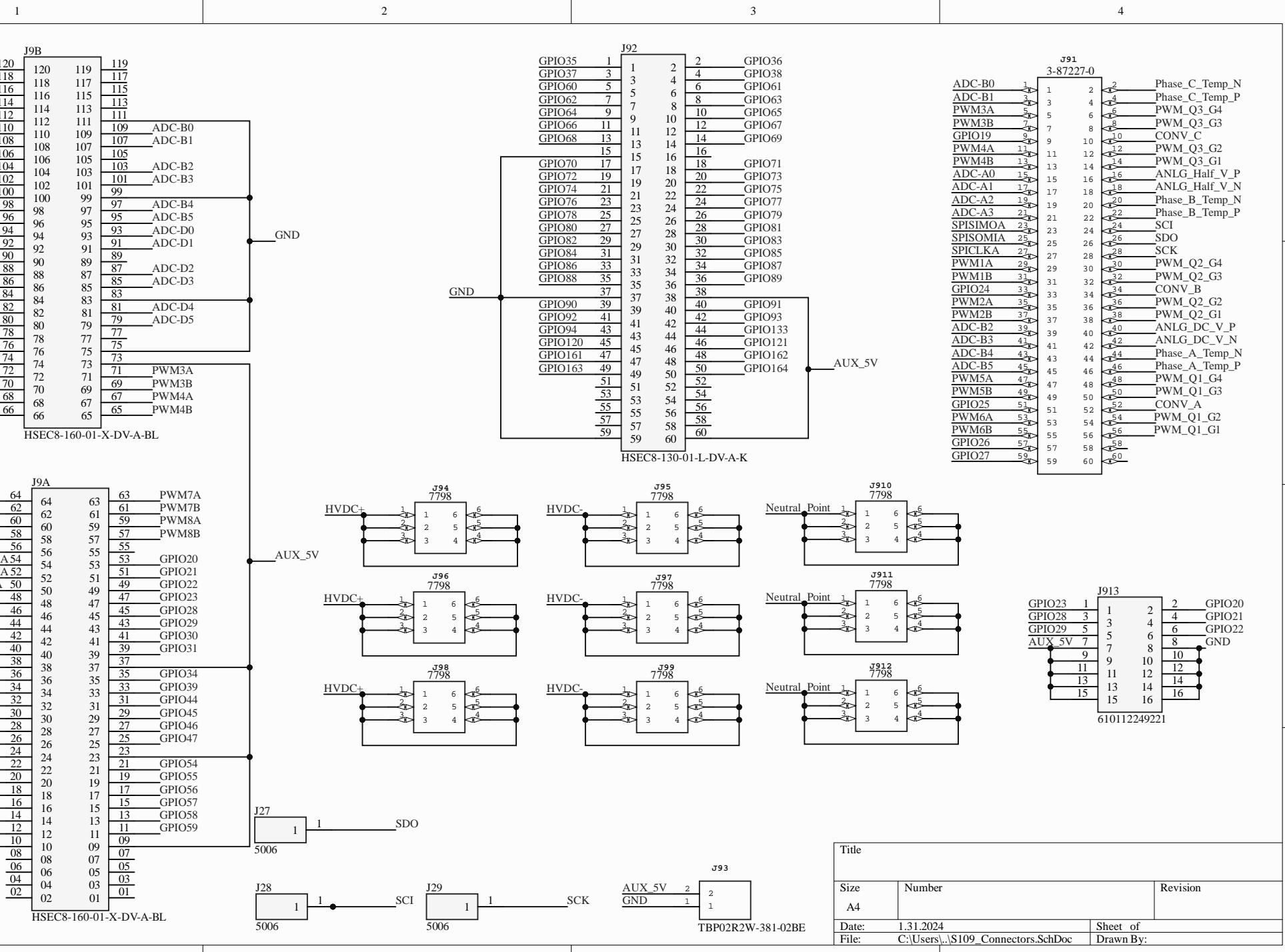
C

C

D

D

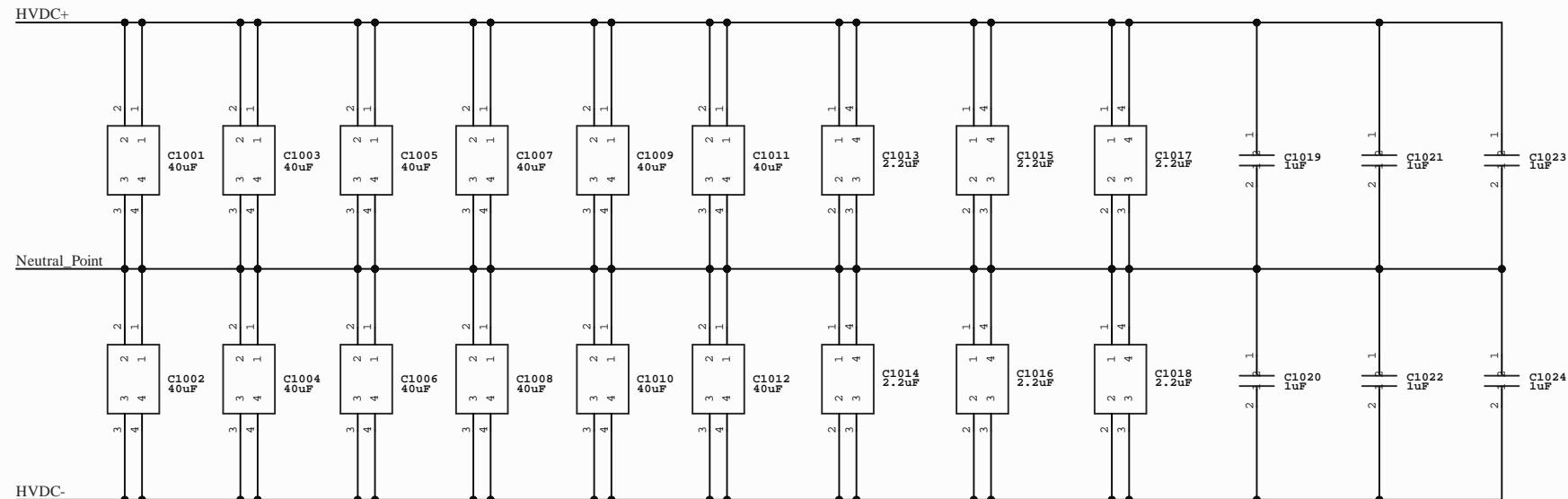
Title		
Size	Number	Revision
A4		
Date: 1.31.2024	Sheet of	
File: C:\Users\..\S108_Internal_Converters.Sch	Drawn By:	



A

A

HIGH VOLTAGE INPUT CAPACITORS



B

B

C

C

D

D

Title

Size

A4

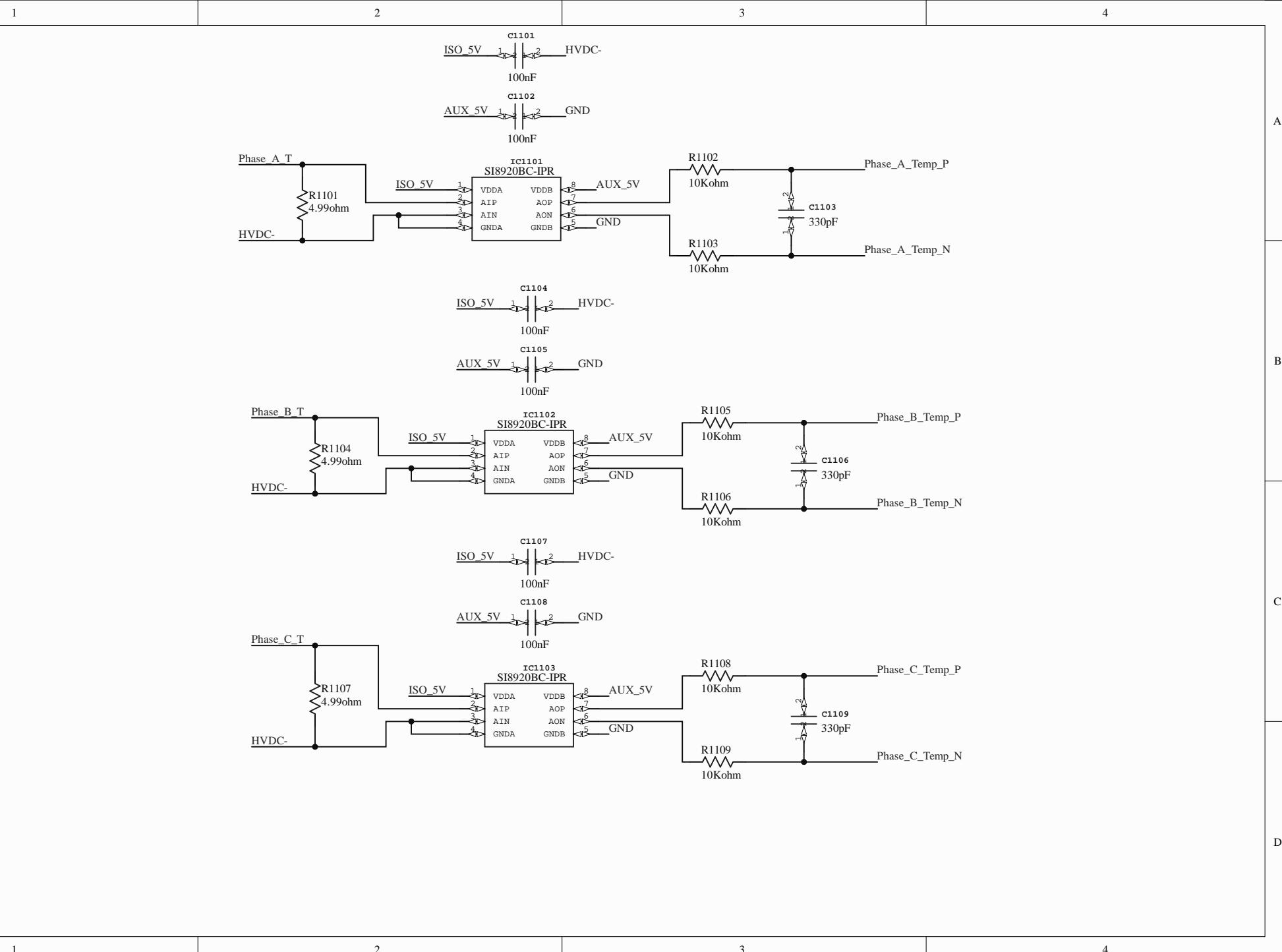
Number

Revision

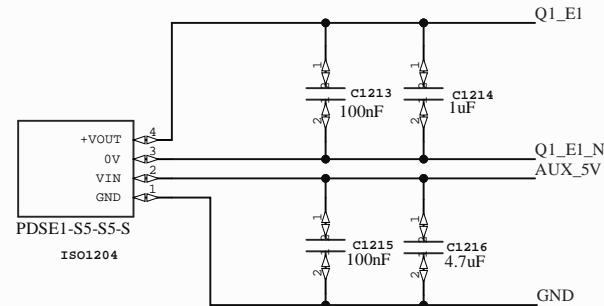
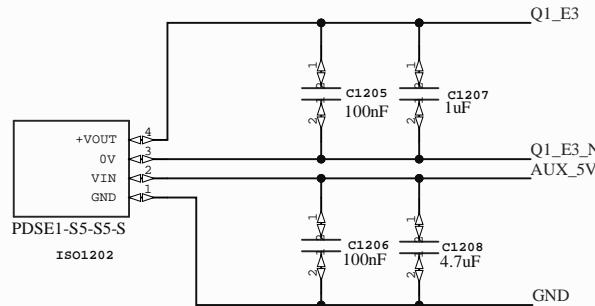
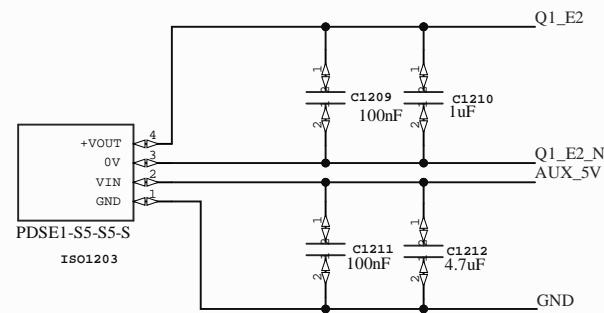
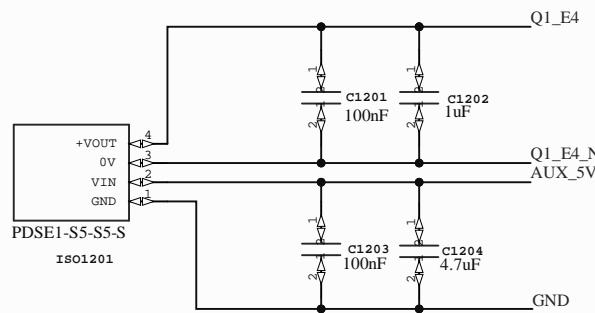
Date: 1.31.2024

Sheet of

File: C:\Users\...\S110_HVDC_Capacitors.SchDrawn By:

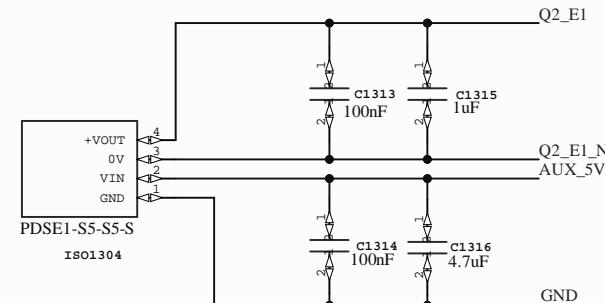
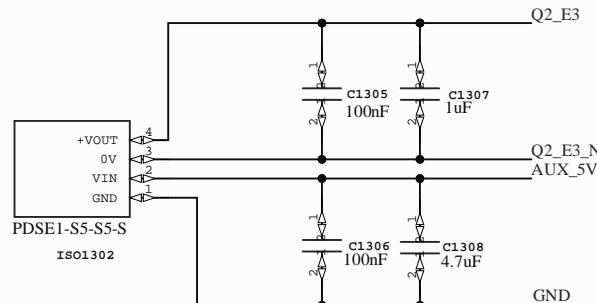
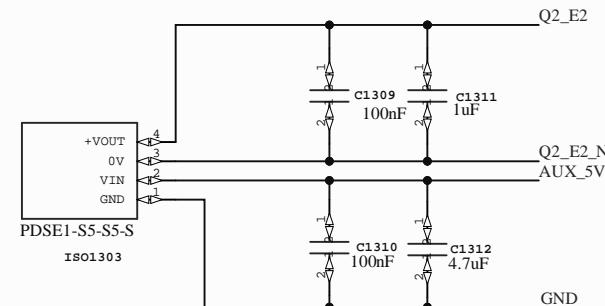
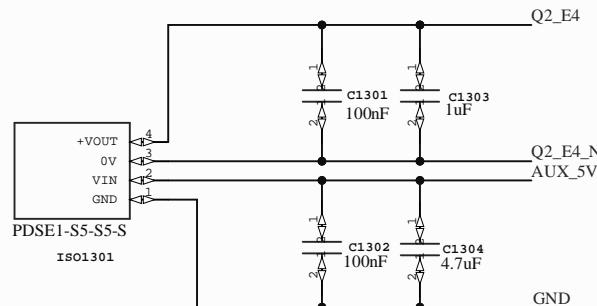


PHASE A GATE DRIVERS' ISOLATED NEGATIVE DC/DC CONVERTERS



Title		
Size	Number	Revision
A4		
Date: 1.31.2024	Sheet of	
File: C:\Users\...\S112_PhaseA_Isolated_Negative_Drivers_Converters.SchDoc		

PHASE B GATE DRIVERS' ISOLATED NEGATIVE DC/DC CONVERTERS



Title		
Size A4	Number	Revision
Date: 1.31.2024		Sheet of
File: C:\Users\...\S113_PhaseB_Isolated_Negat...dra...erters.SchDoc		

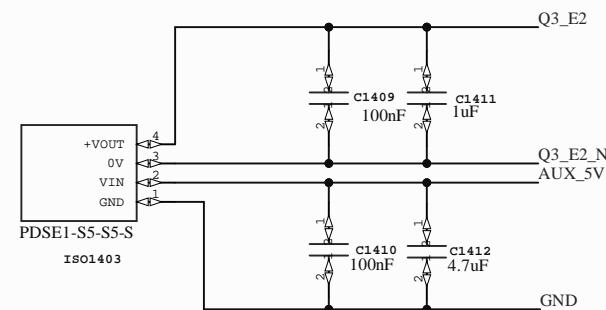
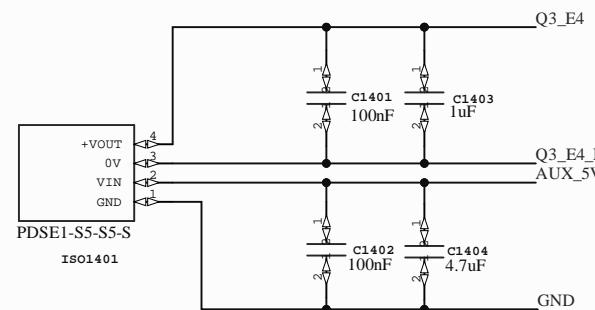
A

A

PHASE C GATE DRIVERS' ISOLATED NEGATIVE DC/DC CONVERTERS

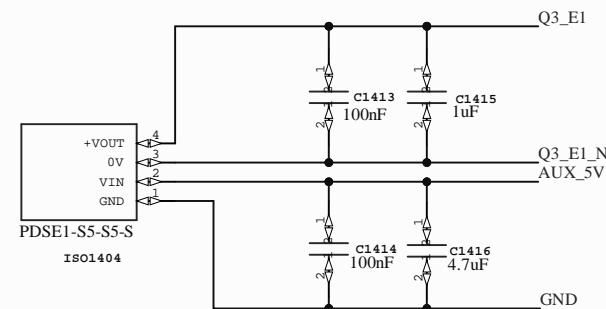
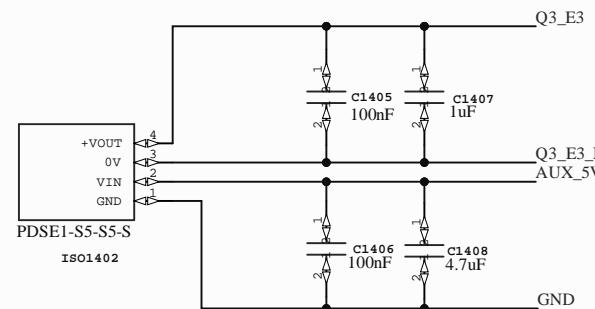
B

B



C

C



D

D

Title

Size

A4

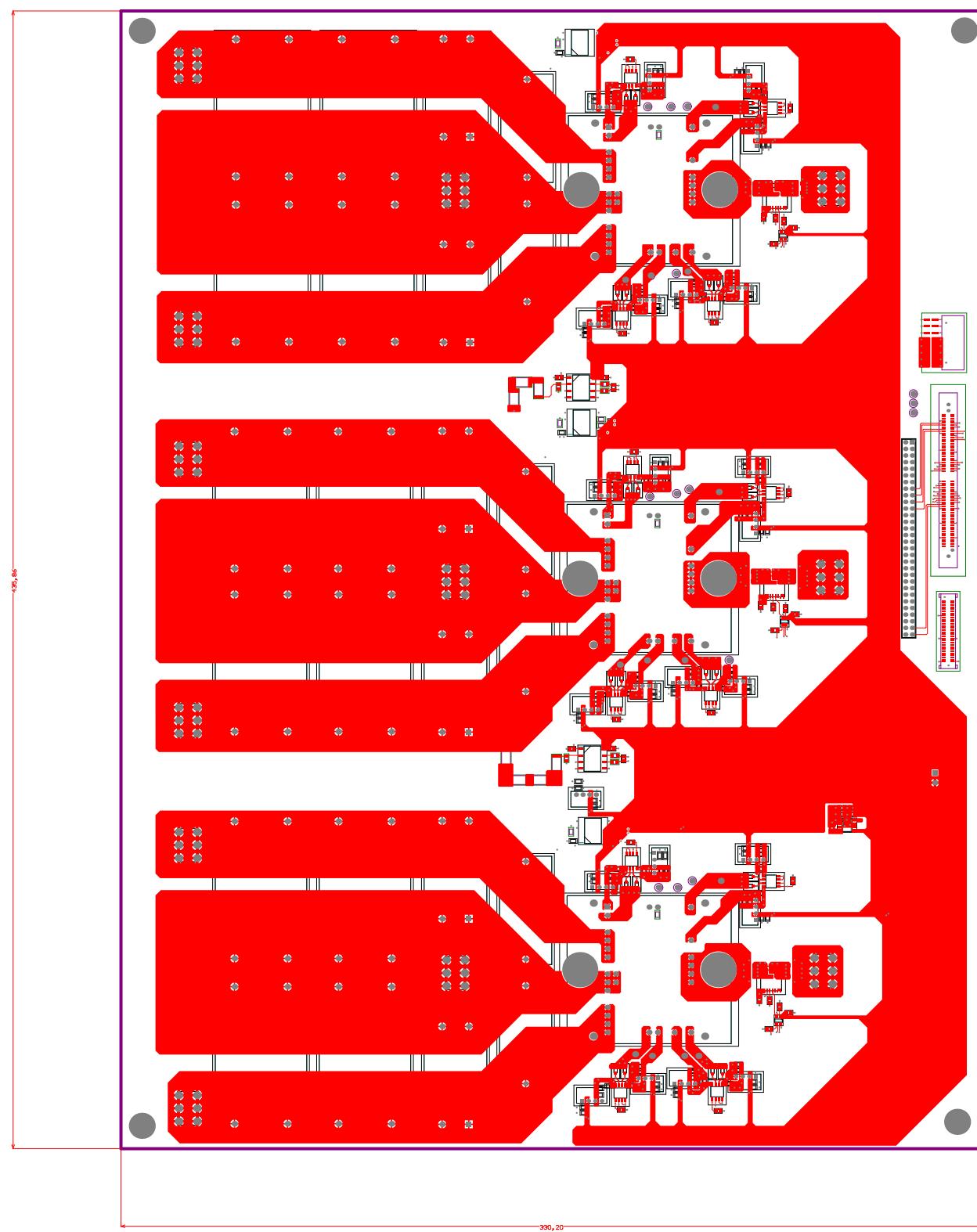
Number

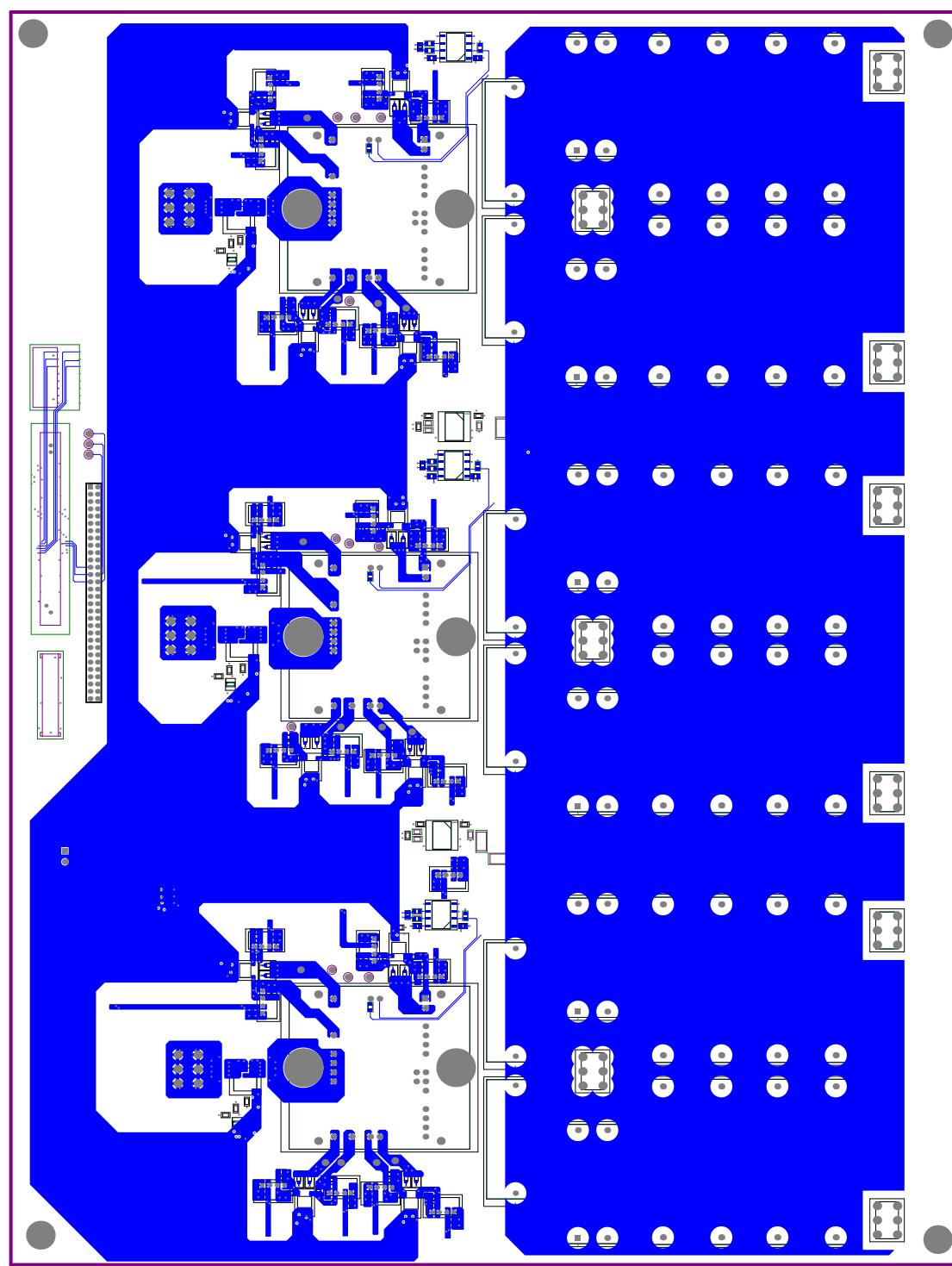
Revision

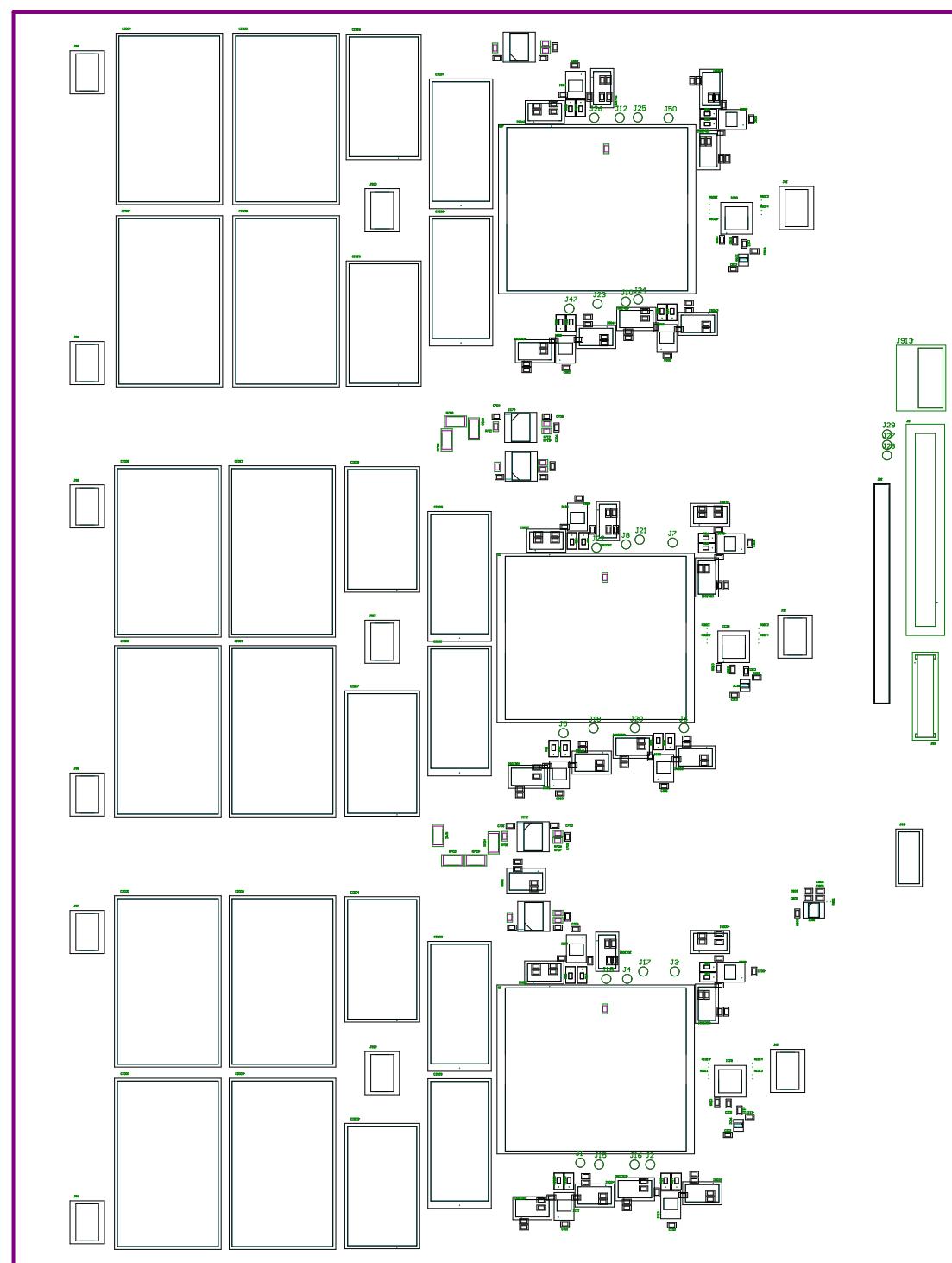
Date: 1.31.2024

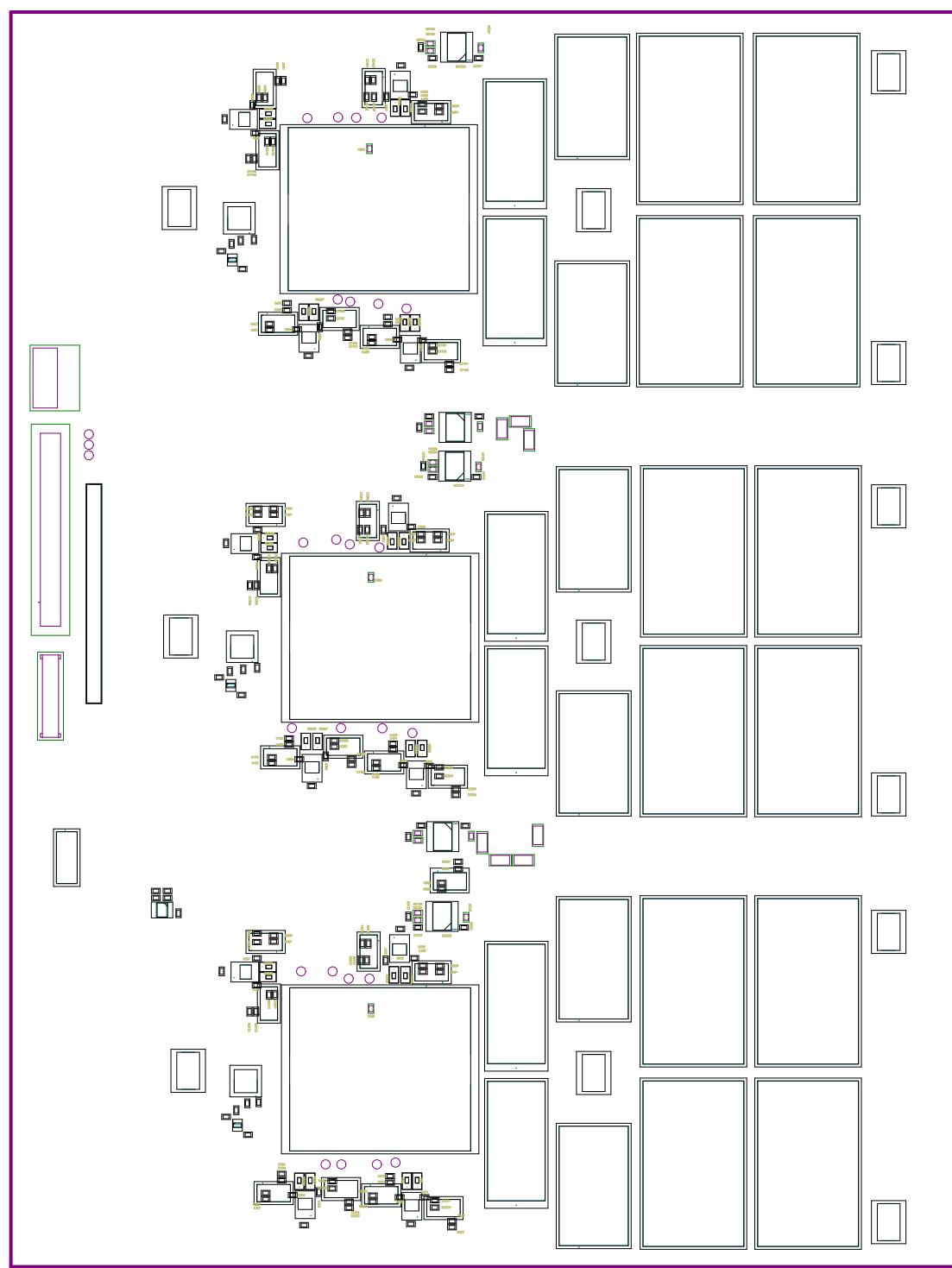
Sheet of

File: C:\Users\...\S114_PhaseC_Isolated_Negat...dra...ters.SchDoc









Board Stack Report