```
81 #7 segment display
82 set_property PACKAGE_PIN W7 [get_ports {segmentDisplay[0]}]
83     set_property IOSTANDARD LVCMOS33 [get_ports {segmentDisplay[0]}]
84 set_property PACKAGE_PIN W6 [get_ports {segmentDisplay[1]}]
85     set_property IOSTANDARD LVCMOS33 [get_ports {segmentDisplay[1]}]
86 set_property PACKAGE_PIN U8 [get_ports {segmentDisplay[2]}]
87     set_property IOSTANDARD LVCMOS33 [get_ports {segmentDisplay[2]}]
88 set_property PACKAGE_PIN V8 [get_ports {segmentDisplay[3]}]
89     set_property IOSTANDARD LVCMOS33 [get_ports {segmentDisplay[3]}]
90 set_property PACKAGE_PIN U5 [get_ports {segmentDisplay[4]}]
91     set_property IOSTANDARD LVCMOS33 [get_ports {segmentDisplay[4]}]
92 set_property PACKAGE_PIN V5 [get_ports {segmentDisplay[5]}]
93     set_property IOSTANDARD LVCMOS33 [get_ports {segmentDisplay[5]}]
94 set_property PACKAGE_PIN U7 [get_ports {segmentDisplay[6]}]
95     set_property IOSTANDARD LVCMOS33 [get_ports {segmentDisplay[6]}]
96
97 #set_property PACKAGE_PIN V7 [get_ports dp]
98     #set_property IOSTANDARD LVCMOS33 [get_ports dp]
99
100 set_property PACKAGE_PIN U2 [get_ports {anodes[0]}]
101     set_property IOSTANDARD LVCMOS33 [get_ports {anodes[0]}]
102 set_property PACKAGE_PIN U4 [get_ports {anodes[1]}]
103     set_property IOSTANDARD LVCMOS33 [get_ports {anodes[1]}]
104 set_property PACKAGE_PIN V4 [get_ports {anodes[2]}]
105     set_property IOSTANDARD LVCMOS33 [get_ports {anodes[2]}]
106 set_property PACKAGE_PIN W4 [get_ports {anodes[3]}]
107     set_property IOSTANDARD LVCMOS33 [get_ports {anodes[3]}]
```