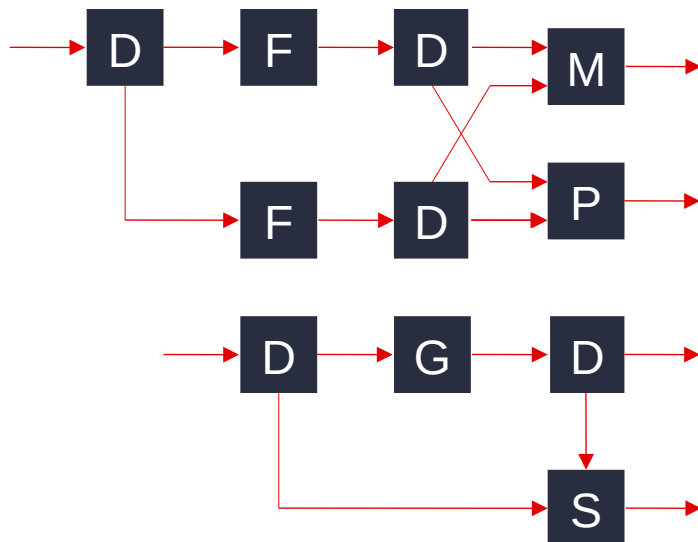# Hardware accelerated SIFT
Alexandre HONORAT, Vincent MORILLON
INSA Rennes, IETR, VAADER
https://github.com/ahonorat/HLS_SIFT_subkernel

▸ Summary of what you did

- Plan – Expanding Gaussian Difference
- Final project – Diff + Gradient

▸ Block diagram:

▸ What did you learn, or what was interesting/difficult?

Coded 2 HLS dataflow modules

Some difficulties on image conversion

▸ Results, Image?

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