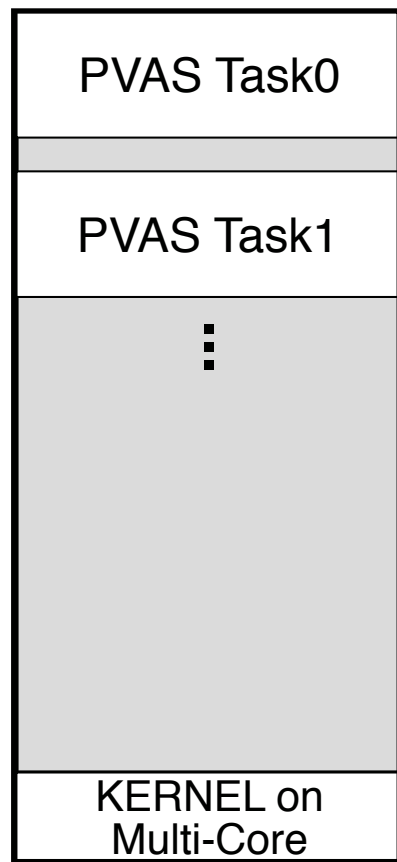
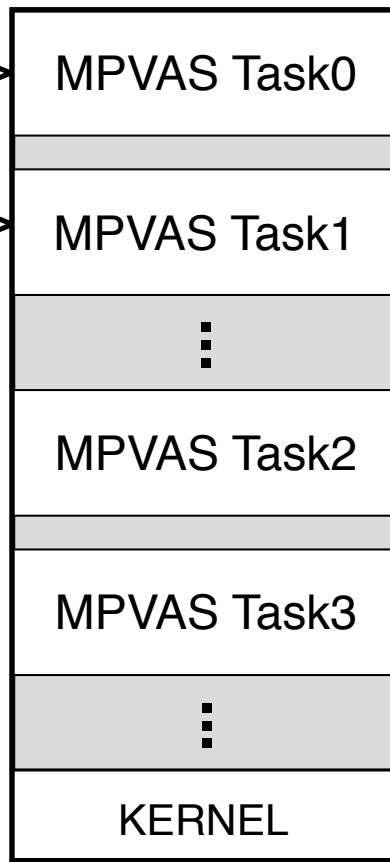


Address Space on  
Multi-core CPU



mmap →

MPVAS  
Address Space

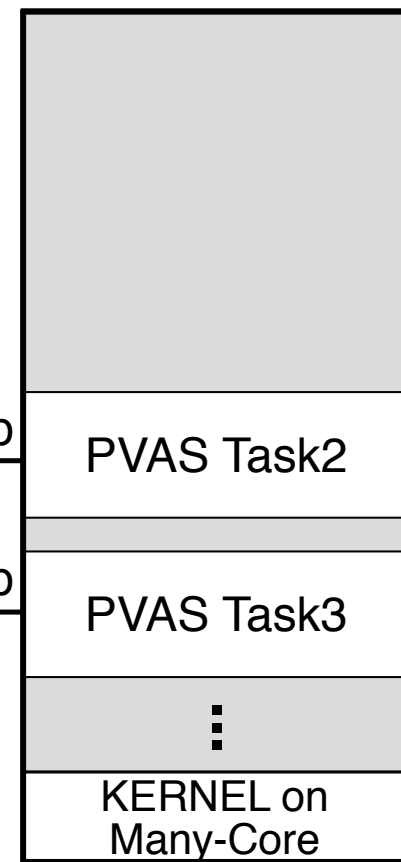


mmap →

← mmap

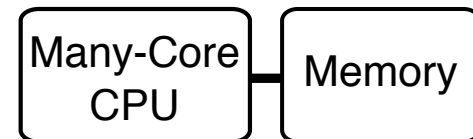
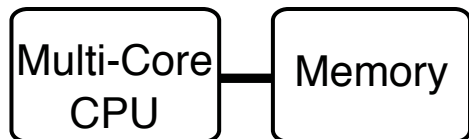
← mmap

Address Space on  
Many-core CPU



S/W

H/W



PCIe bus