



- Intel Offloading
- MPVAS: Xeon Phi \rightarrow x86 \rightarrow Xeon Phi (1st)
- MPVAS: Xeon Phi \rightarrow x86 \rightarrow Xeon Phi (2nd)
- MPVAS: x86 \rightarrow Xeon Phi \rightarrow x86 (1st)
- MPVAS: x86 \rightarrow Xeon Phi \rightarrow x86 (2nd)