

- MIPS 指令

add rd rs rt // $rd = rs + rt$

addi rt rs imm // $rt = rs + imm$

beq rs rt label // branch to label if ($rs == rt$)

blt rs rt label // branch to label if ($rs < rt$)

bne rs rt label // branch to label if ($rs \neq rt$)

j label // jump to label

lw rd address // load the word at address into rd

move rd rs // move rs to rd

nop // do nothing

or rd rs rt // $rd = rs \text{ OR } rt$

mul rd rs rt // $rd = rs * rt$

slt rd rs rt // if ($rs < rt$) $rd = 1$ else $rd = 0$

sne rd rs rt // if ($rs \neq rt$) $rd = 1$ else $rd = 0$

srl rd rt sa // $rd = rt$ shifted right by sa,

sll rd rt sa // $rd = rt$ shifted left by sa.

sub rd rs rt // $rd = rs - rt$

sw rt address // store the word in rt to address.