



Lim Boon Ping

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DATE OF BIRTH	30 October 1980	
NATIONALITY	Malaysia	
CAREER OBJECTIVE	My professional goal is to work with great team that has strong passion and enthusiasm, to create great companies, products, and businesses.	
RESEARCH INTEREST	I am primarily interested in the R&D of large-scale wired/wireless group communication and AV quality-of-services. In addition, I am interested in embedded system design.	
SALARY	Current Salary : MYR 7819 ; Expected Salary : MYR 9500	
EDUCATION	Multimedia University, Cyberjaya, Malaysia Master of Science in Information Technology, July 2006 <ul style="list-style-type: none">Thesis Topic: Mitigation of SYN-flooding attack using high speed programmable network processor B.Sc (Hons) of Information Technology, November 2002 <ul style="list-style-type: none">Major: Software EngineeringCGPA: 1st Class Honours (3.82 out of 4.00)Thesis title: Developing a distributed stock exchange application using CORBA. Disted-Stamford College Penang & University Science Malaysia Diploma in Computer Science and Information Technology, November 2000 <ul style="list-style-type: none">CGPA: 1st Class Honours (3.81 out of 4.00)	
PROFESSIONAL EXPERIENCE	Panasonic R&D Center Malaysia Senior R&D Engineer, Project Manager (August 2005 – current) <ul style="list-style-type: none">7++ years experience with C/C++ programming for linux-based PC and embedded system development.In-depth knowledge in network communication protocols (TCP/IP, UDP, RTP etc.)	

and network security, specified in DDoS.

- In-depth knowledge in tree/mesh-based construction and path optimization algorithm, real-time streaming system design and performance optimization.
 - In-depth knowledge and experience in prototype design for distributed client-server/P2P application.
 - Knowledge in embedded system (Intel IXP platform, Panasonic Uniphier platform).
 - Hold 6 US/JP patents filed to date with extensive worked on research problem identification, prior art search, patent idea generation, proposal defense and invention disclosure drafting.
 - Experience in developing software on PC-based and embedded linux platform (Panasonic *Uniphier* AM33/AM34 Processor) using C/C++.
 - [Application layer multicast-based HD video streaming](#) on Panasonic HD Telepresence System.
 - Major contributions are
 - [System design, implementation and test](#) of dual-source multi-receivers (up to 99 receivers) product prototype.
 - [bandwidth fair, QoS loss-handled optimized streaming path convergence](#) algorithm.
 - [Prediction tree-based network latency and bandwidth estimation](#) for predictive and non-intrusive network measurement.
 - [Kernel-based high speed packet replication](#) mechanism by hacking on linux kernel IP stack.
 - Call control middleware, integrating with Panasonic HD Telepresence System (SIP, AV QoS etc.) to support application layer multicast-based small group communication.
 - Wired/wireless multi-node AV streaming for targeted for IP camera, digital signage, mobile phone and tele-presense system
 - Major contributions are
 - [Wired/Wireless optimized path construction](#) algorithm, incorporating layer-2 parameter for cross-layer optimization.
 - [AV packet error resilience mechanism achieving near-zero loss rate AV QoS during route convergence](#) with predictive route convergence time, adaptive rate control and automatic retransmission request (ARQ).
 - [Loss, delay and jitter-resilient bandwidth measurement](#) mechanism.
 - [Automatic network calibration and Internet-like network traffic emulation](#) for network-based product verification and testing using automated test framework (based on Starbed by Japan Advanced Institute of Science & Technology JAIST).
 - Major contributions are
 - Define Internet-like network conditions: loss, delay, jitter, bandwidth criteria & expected path construction/AV quality.
 - Design a 30 nodes testbed consists of HD Presence reference boards & network emulator (netem, dummyNet).
 - Lead and conduct functional, performance, stress test via
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manual, automated and semi-automated test method.

- [On-demand MPEG2 TS-PS AV transcoding](#) for Blu-ray Recoder and DVD-Recorder playback compatibility on Panasonic Uniphier processor, integrated with Universal Plug-and-Play (UPnP) stack for automated AV transcoding service discovery, advertisement and control.
 - Analyzed and mapped Blu-ray metadata format to DVD IFO format.
 - Developed a prototype to demonstrate MPEG TS-PS transcoding and playback via supporting conversion of Blu-ray clip info, play list and stream metadata to DVD IFO format, using C on RedHat9.
 - Configured and cross-compiled prototype, HTTP server and UPnP framework to *Uniphier* embedded platform.
 - [Reliable AV content recovery system](#) with distributed AV content storage, XOR-pairing & reconstruction, integrated with UPnP for automatic corrupted data recovery in home network.
 - Designed and developed a prototype to demonstrate AV content aware XOR-ing with distributed pairing file search, parity processing and storage, and corrupted content reconstruction capabilities, leveraged on UPnP using C/C++ in Fedora Core 3.
 - [Distributed media service across CE devices, leveraging on remote AV plug-in for home network media access](#), integrated with Network AV Framework for AV data encode/decode/mux/demux/transcoding/down-sampling (distributed and collaborative AV communication and processing).
 - Identified challenges of expanding existing multimedia architecture to CE-based network environment.
 - Analyzed requirement and designed in detail architecture, components model, process flow, messaging scheme and API for distributed media service discovery and control.
 - Implemented PC-based prototype to demonstrate distributed media plug-in collaboration and practical problems/solutions addressed in patented idea, leveraged on UPnP using C/C++ in Fedora Core 3.
 - UPnP, CORBA and ICE middleware exploration & porting to *Uniphier* Platform for performance benchmark.
 - 6 years of R&D experiences in Panasonic Corporate R&D
 - Pioneer member of Panasonic Kuala Lumpur Laboratories,
 - Create working prototypes and demo systems to show capabilities of and practical problems addressed by our innovative technologies, drive technology transfers to product groups, and help create and execute incubation projects,
 - Design & develop R&D software prototype for next 3-5 years technology,
 - Participate in new project & business promotion to Panasonic Corporate & Domain Companies,
 - Drives patent activities and invention disclosures write-up,
 - Participate in IETF/IRTF standardization activities,
 - Project planning using SDLC and CMMi processes.
 - Oversee a team of 5 engineers specialized in AV Communication Protocol of Advanced Technology Development Group.
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- Manage groups of 1-3 engineers to drive patent and software development activities.
 - Articulate, compare, and implement solutions and alternative approaches based upon project management principles and R&D targets.
 - Main interface person to manage and negotiate with customers on project requirements, project deployment schedule, system design, functional & performance test scope.
 - Identify functional gaps and translates business problems and requirements into technical design.
 - Drive the team recruitment process to ensure timely acquisition of an effective talent pool in both research and development activities.
 - Regularly representing company to Japan for business promotion, project meetings, presentations, demonstrations and integration activities.
 - Coaching, mentoring and as a leading example to subordinates.
 - Consistently achieve Project Objectives and High Customer Satisfaction.

Multimedia University & Intel Technology Malaysia

Research Officer/ Intel Scholar
(September 2002 – July 2005)

- Designed and implemented SYN-flooding denial-of-service attacks detection and defense system on Intel IXP1200/2400 network processor platform.
- Analyzed and evaluated the performance and limitations of various proposed approaches in combating DDoS, especially towards SYN-flooding.
- Designed and developed detection and defense algorithms for syn-flooding attacks.
- Implemented algorithms on Intel IXP network processor using microcode at data plane and C on XScale processor at control plane.
- Experienced in configuring, compiling and using GCC cross compiler toolchain for embedded system development.
- Designed and set up a testbed environment for hardware-based performance benchmarking and evaluation.
- In-depth knowledge of TCP/IP and network security.
- Other tasks included research of topics for network processor introductory class and assist in teaching for undergraduates Computer Network lab sessions.

Equisys Solution

Co-founder
(December 2002 – July 2004)

- Startup a company that provides web-based solutions to government and private sector.
 - Plan the company's business plan and technology R&D strategy.
 - Design the technical architecture and solution for the company's products, which includes:
 - Multimedia Digital Archive Online Access System
 - Online Library & Knowledge Repository Management System
 - Condominium Community Portal
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- Manage the software development team which consists of 3 programmers and 1 designer on task scheduling, resource allocation and risk management.

IBM Malaysia

Intern

(May 2001 – Aug 2001)

PUBLICATIONS

Patent

1. Apparatus and Method for Network Latency Prediction Using Optimal Target Node Sampling
Japan Application No.: JP2010/185028
2. Apparatus and methods for Terminal Cluster Formation in Terminal Relay Type Multi-point Communication
Japan Application No.: JP2010/229920
3. (WO/2009/153945) COMMUNICATION CHANNEL BUILDING DEVICE AND N-TREE BUILDING METHOD
International Application No.: PCT/JP2009/002647
4. (WO/2009/098748) TERMINAL AND N-TREE CONSTRUCTING METHOD
International Application No.: PCT/JP2008/004017
5. Media Path Selection for Distributed Media Service
Japan Application No.: JP2007/249441
6. Automatic Consumer Electronics Devices Clustering and Selection at Home Heterogeneous Network for AV Content Aware Reliability Service
Japan Application No.: JP2006/240281

Book Chapter

Ettikan K.K, Jonathan Tan Ming Yu, Lim Boon Ping, Thilmee Baduge, Eiichi Muramoto. (2011). Overlay Multimedia Streaming Service for Consumer Electronics Devices. In John Buford, Advances in Next Generation Services and Service Architectures. River Publishers.

Internet Draft

"ALM API for Topology Management and Network Layer Transparent Multimedia Transport", draft-lim-irtf-sam-alm-api-00.txt.

Journal / Conference Paper

Lim Boon Ping, Jonathan Tan Ming Yu, Ettikan K.K, Thilmee Baduge and Eiichi Muramoto, "Network Latency & Bandwidth Estimation using High Accuracy Prediction Tree", submitted to IEEE Transaction of Consumer Electronics.

Thilmee Baduge, Lim Boon Ping, Kunio Akashi, Jason Soong, Kenichi Chinen, Ettikan K.K. and Eiichi Muramoto, "Functional and Performance Verification of Overlay Multicast Applications – a Product Level Approach", in proc. of IEEE Consumer Communications & Networking Conference, Jan 2010.

Khoa T.Phan, Nam Thoai, Eiichi Muramoto, Lim Boon Ping, Ettikan K.K., and Tan Pek Yew, "Treemap - The Fast Routing Convergence Method for Application Layer Multicast", in proc. of IEEE Consumer Communications & Networking Conference, Jan 2010.

Jonathan Tan Ming Yu, Lim Boon Ping, Ettikan K.K., Thilmee Baduge, Eiichi Muramoto, "Design and Implementation of Dynamic Join/Leave Application Layer Multicasting on PeaksProl", 27th Annual Panasonic Technology Symposium, Dec 2009.

Lim Boon Ping, Ettikan K.K, Lin En Shu, Truong Khoa Phan, Nam Thoai, Eiichi Muramoto and Tan Pek Yew, "Bandwidth Fair Application Layer Multicast for Multi-party Video Conference Application", in proc. of IEEE Consumer Communications & Networking Conference, Jan 2009.

Lim Boon Ping, Lin En Shu, Ettikan K.K, Eiichi Muramoto and Tan Pek Yew, "Application of the ALM Technology on PeaksPro II", 26th Annual Panasonic Technology Symposium, December 2008.

Lim Boon Ping, Ettikan K.K, Tan Pek Yew, Eiichi Muramoto and Taisuke Matsumoto, "Application Layer Multicast Video Conference", 25th Annual Matsushita Technology Symposium, December 2007.

Lim Boon Ping, Ettikan K.K, Tan Pek Yew, Miyake Yasushi and Naohisa Tanabe, "AV Content Reliability in Home Network", 24th Annual Matsushita Technology Symposium, December 2006.

Lim Boon Ping, Ang Boon Poah, Ettikan K.K, Lam Yoke Khei, "Performance Tests on Distributed Real-Time and Embedded Middleware", In Proceedings of M2USIC International Symposium, November 2006.

Ettikan Kandasamy Karuppiah, Lim Boon Ping and Tan Pek Yew, "Effect of AV Traffic on UPnP Advertisements in a Home Network Environment", In Proceedings of IEEE International Conference of Networking (ICON), September 2006.

Lim Boon Ping, Md Safi Uddin, "Synmon Architecture for SYN-flooding Defense Using Network Processor", In Proceedings of Asia Pacific Conference on Communication (APCC), October 2005.

Lim Boon Ping, Md Safi Uddin, "Statistical-based SYN-flooding detection using programmable network processor", In Proceedings of IEEE International Conference on Information Technology and Applications (ICITA), July 2005.

Lim Boon Ping, Md Safi Uddin, "A proposition towards mitigation Of DDoS attacks using high speed programmable network processor", In Proceedings of M2USIC International Symposium, October 2003.

Toufik Taibi, Lim Boon Ping, Ng Sheau Wen, Lai Kiat Sing, Chew Keow Lim, "Developing a distributed stock exchange application using CORBA", In Proceedings of IEEE Student Conference on Research and Development, August 2003.

LANGUAGE
PROFICIENCY

Chinese – Excellent, English – Good, Malay – Good, Japanese – Basic

AWARDS /
CERTIFICATION

2009 Panasonic R&D Center Malaysia Employee Best Performance Award
Grant recipient of Cradle Investment Programme for project entitled "Robotic Eye Inside Pipe"

	<hr/> <p>Mentor of Cradle Investment Programme by Malaysian Government (to nurture new entrepreneur) based on expertise in AV processing & networking.</p> <p>24th Annual Matsushita Technical Symposium Best Presentation Award</p> <p>Intel® Research Scholar with MYR ~100k grant awarded</p> <p>Sun Certified Java Programmer (Java 2 Platform 1.4)</p> <p>Full scholarship under PTPTN Malaysian Government Educational Fund for achieving First Class Honour Bachelor Degree</p> <p>Dean Award for all 7 semesters during Bachelor Degree</p> <p>Best Student Award for all 4 Semesters during Diploma</p> <hr/>
TRAININGS ATTENDED	<hr/> <ol style="list-style-type: none"> 1. Patent Filing & Invention Disclosure Drafting by Ella And Cheong, Sept 2005. 2. Patent Claim Writing Skills, by IP Academy Singapore, April 2006. 3. Serial ATA, November 2005. 4. Embedded Linux Hands On Workshop, May 2006. 5. Video Streaming Standards JPEG/MPEG/H.264 Hands On Workshop, December 2006. 6. Effective Win-win Negotiation, January 2007. 7. Effective Personal communication skills, March 2007. 8. NLP Break-Through Program, August 2007. <hr/>
REFERENCES	<hr/> <p>Dr. Ettikan Kandasamy Karrupiah</p> <p>General Manager, Principal Engineer of Panasonic R&D Center Malaysia</p> <p>ettikan.karuppiah@mimos.my</p> <p>Relationship: Direct superior in Panasonic R&D Center Malaysia.</p> <p>Mr. Eiichi Muramoto</p> <p>Senior Staff Engineer, Visual Communication Development Office, Panasonic Corporation</p> <p>muramoto.eiichi@jp.panasonic.com</p> <p>Relationship: Project Stakeholder for Application Layer Multicast R&D for Panasonic KX-VC 500 Telepresence System.</p> <p>Mr. Safi Uddin</p> <p>Monash University, Melbourne</p> <p>mdsafi@yahoo.com / Safi.Uddin@monash.edu</p> <p>Relationship: Master project co-supervisor.</p>