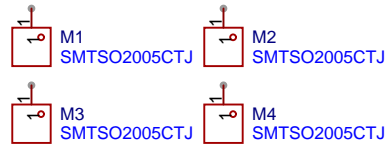
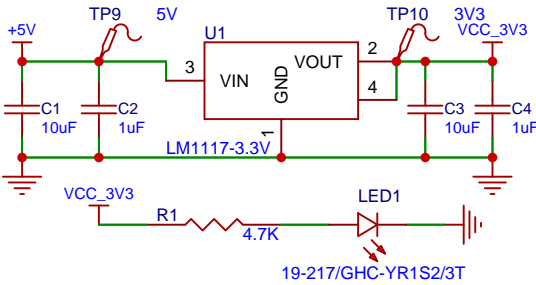


原理图	LT165A_24inch24320_V1.3		创建日期	2025-12-02
板子			更新日期	2026-02-09
绘制		LT165A_Demo_V1.3_1_20250911		
审阅				
	版本	尺寸	页 1 共 2	
	V1.0	A4		

## 安装孔

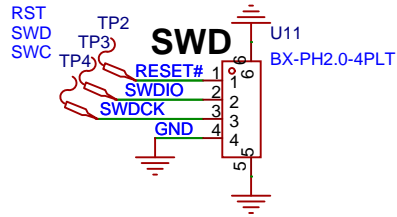


## 5V to 3.3V



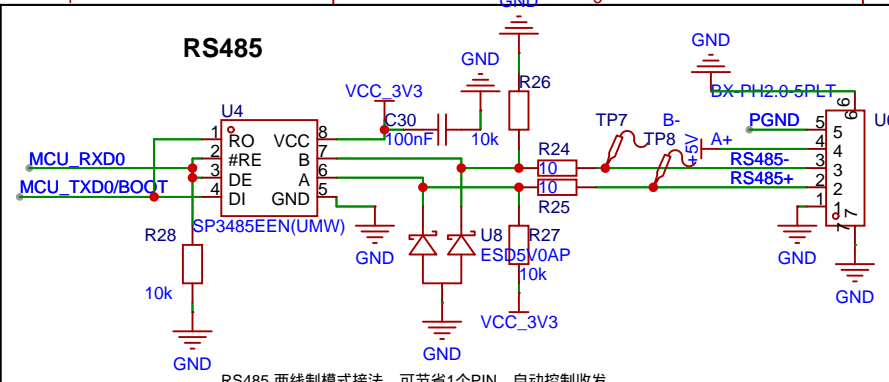
LDO 5->3.3V

## 烧录

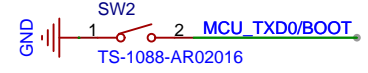


PCB必须预留接口或者测试点

## RS485



RS485 两线制模式接法, 可节省1个PIN, 自动控制收发  
半双工: TX管脚既当发送又当接收; RX管脚自动控制收发方向

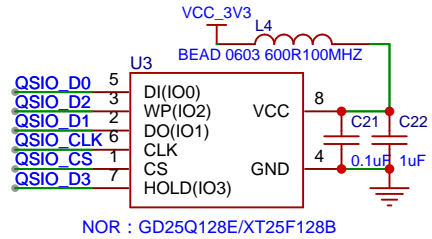


BOOT脚与TXD0共用,  
为了方便将BOOT脚短接到GND, 此处再单独引出BOOT与GND脚。



## 滤波电容

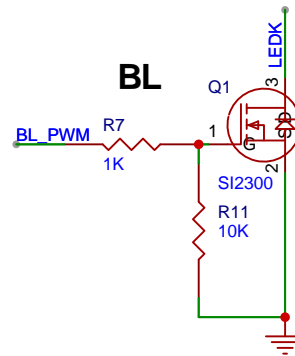
## FLASH



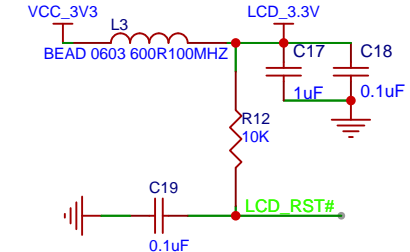
NOR: GD25Q128E/XT25F128B  
Layout时FLASH应尽量靠近LT165A

## 存储

## BL

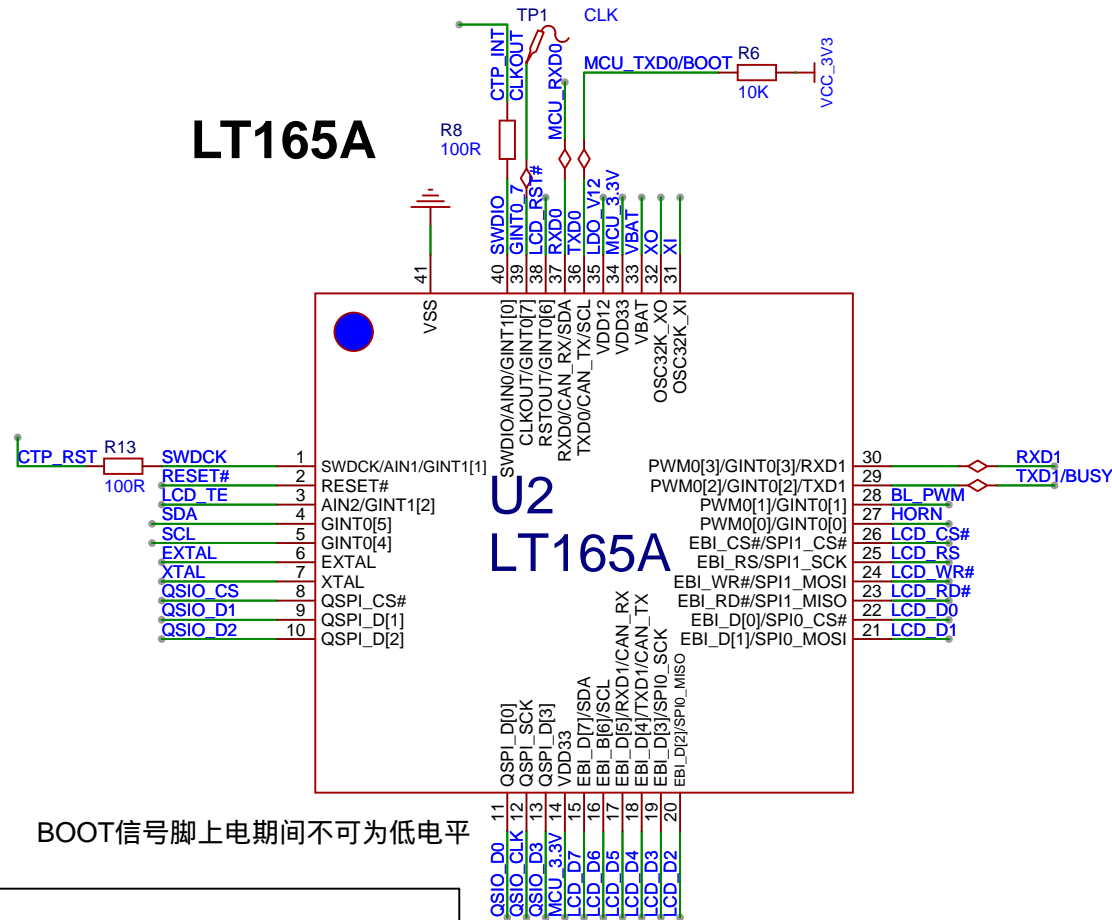


## 背光控制



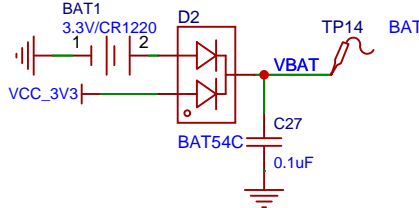
## 液晶复位

## LT165A

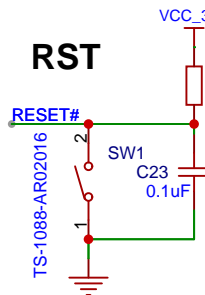


BOOT信号脚上电期间不可为低电平

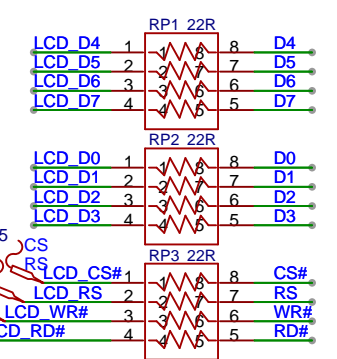
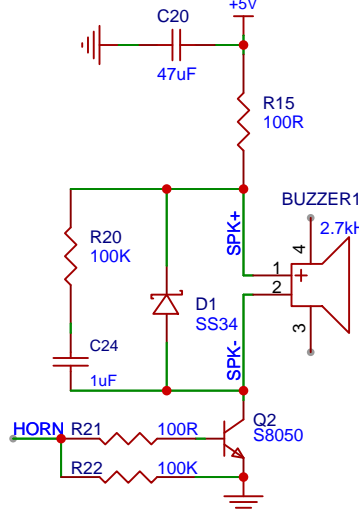
## RTC



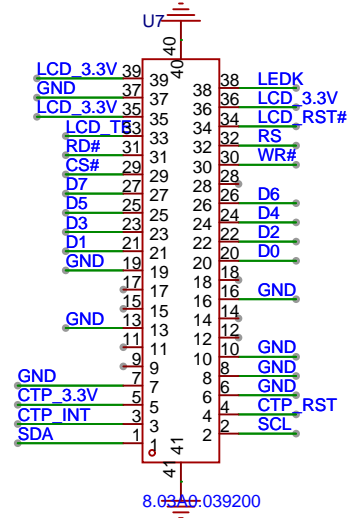
## RST



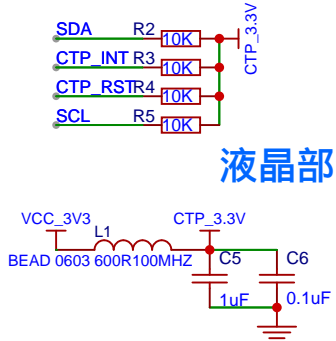
## 蜂鸣器 Beep



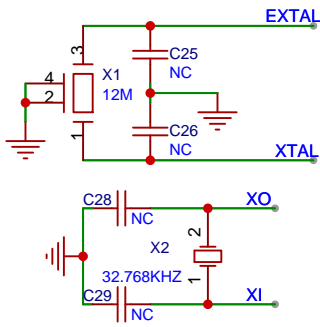
EMC测试可以使用50至100R磁珠  
TFT的FPC需要加屏蔽



## 液晶部分

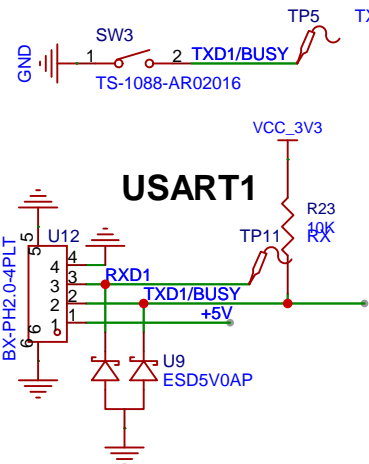


## 晶振



## 主控

## 串口



串口升级使用USART1, BUSY脚与TXD1共用, 为了方便将BUSY脚短接到GND  
此处再单独引出BUSY与GND脚。

原理图	LT165A_24inch24320_V1.3		更新日期	2026-02-09
图页	LT165A_24inch24320_V1.3		创建日期	2023-11-08
绘制			物料编码	
审阅			LT165A_Demo_V1.3_1_20250911	
	版本	尺寸	页	2 共 2
嘉立创EDA		V1.0	A3	嘉立创EDA