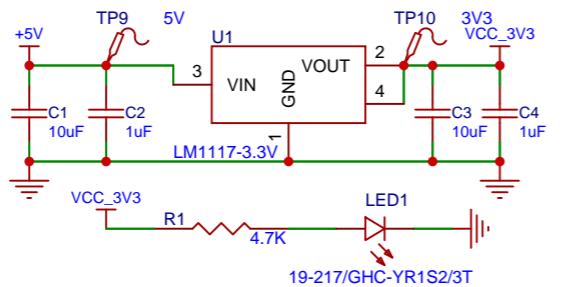


原理图	LT165A_24 inch24320_V1.3			创建日期	2025-12-02
板子				更新日期	2026-02-09
绘制				图页	P3
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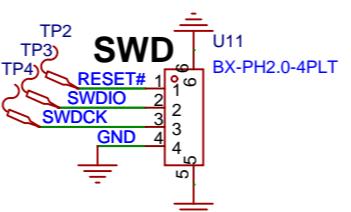
安装孔

5V to 3.3V



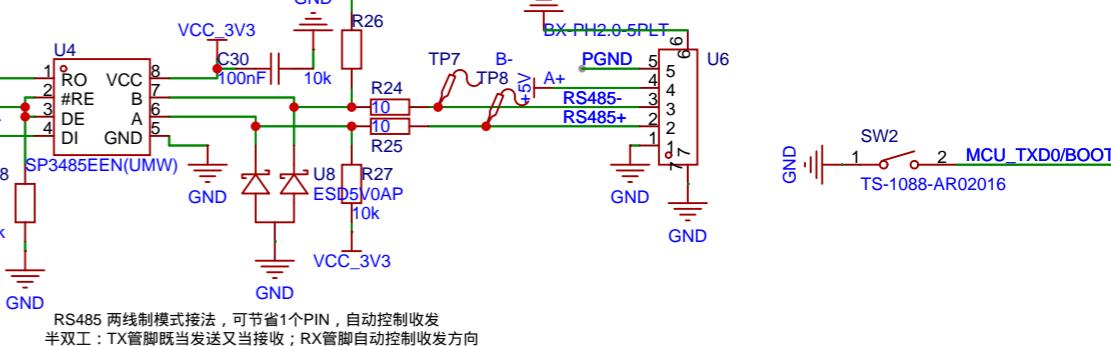
LDO 5->3.3V

烧录



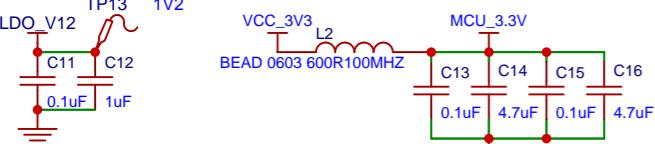
PCB必须预留接口或者测试点

RS485



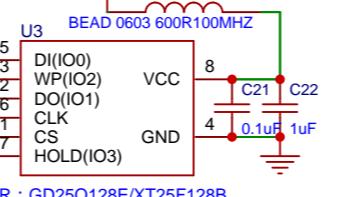
RS485 两线制模式接法，可节省1个PIN，自动控制收发
半双工：TX管脚既当发送又当接收；RX管脚自动控制收发方向

BOOT脚与TXD0共用，
为了方便将BOOT脚短接到GND，此处再单独引出BOOT与GND脚。



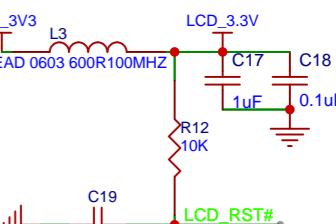
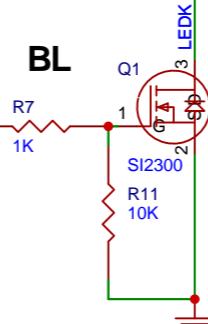
滤波电容

FLASH



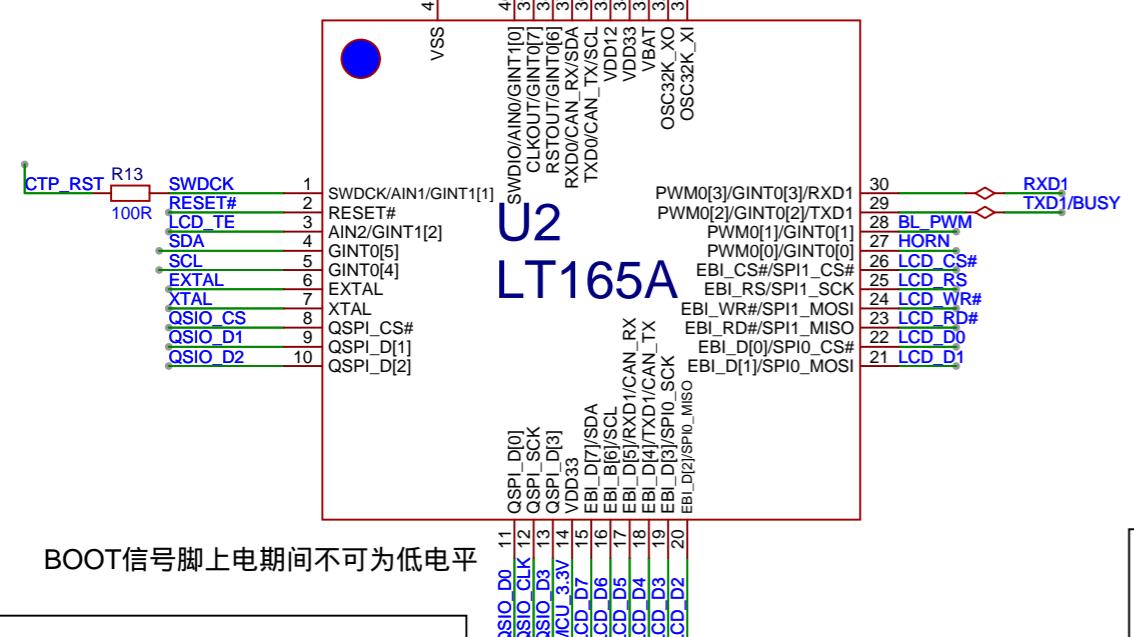
Layout时FLASH应尽量靠近LT165A

BL



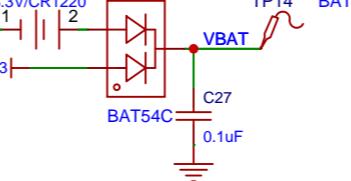
液晶复位

LT165A

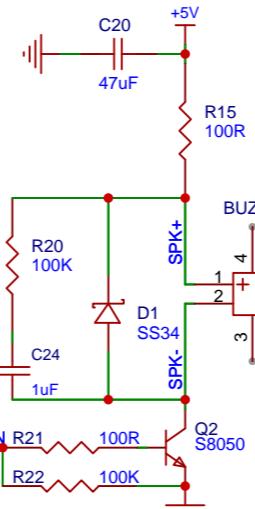


BOOT信号脚上电期间不可为低电平

存储

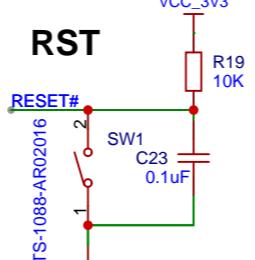


蜂鸣器 Beep

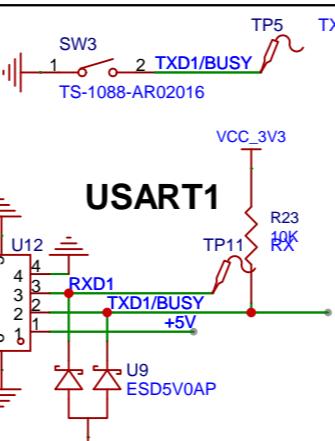


EMC测试可以使用50至100R磁珠
TFT的FPC需要加屏蔽

RST

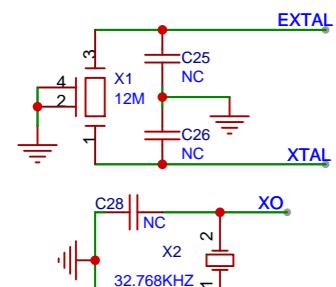


USART1



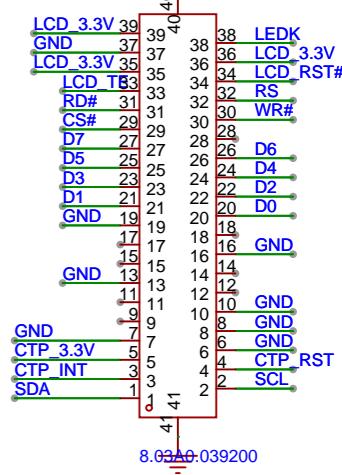
串口升级使用USART1，BUSY脚与TXD1共用，为了方便将BUSY脚短接到GND
此处再单独引出BUSY与GND脚。

晶振



主控

LCD



液晶部分

原理图

LT165A_24inch24320_V1.3

更新日期 2026-02-09
创建日期 2023-11-08

图页

LT165A_24inch24320_V1.3

物料编码

绘制

LT165A_24inch24320_V1.3

审阅

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V1.0

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嘉立创EDA

V1.0

嘉立创EDA