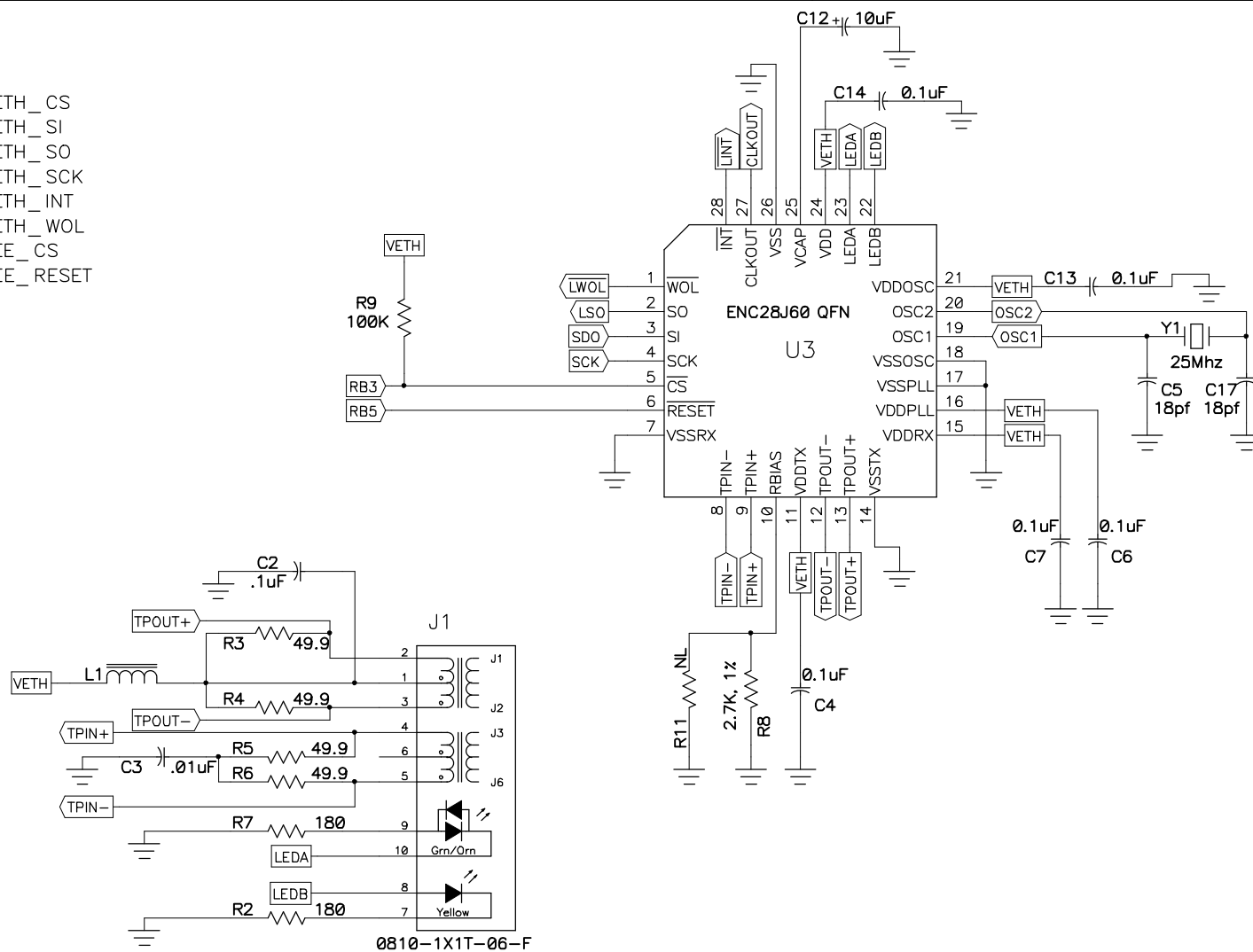
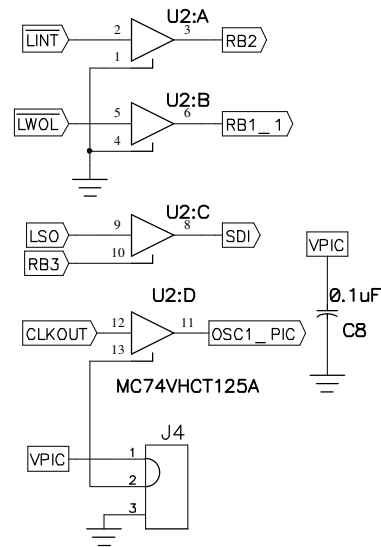



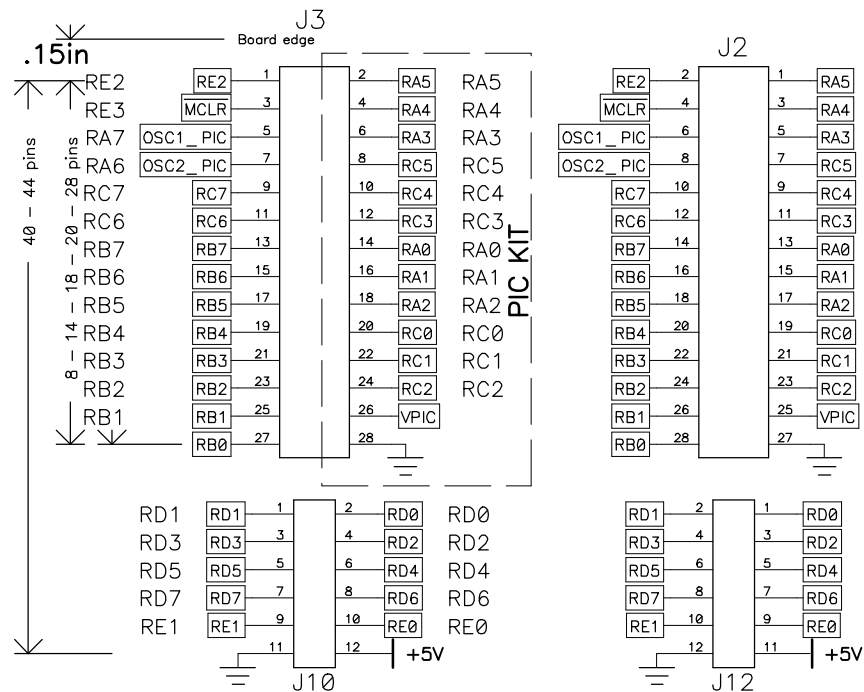
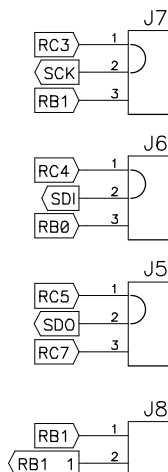
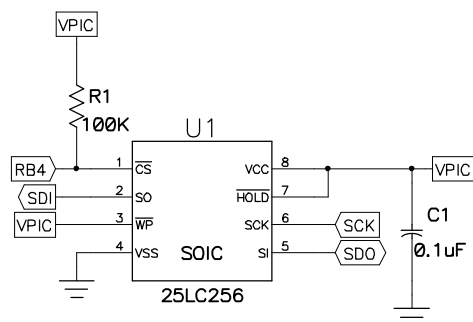
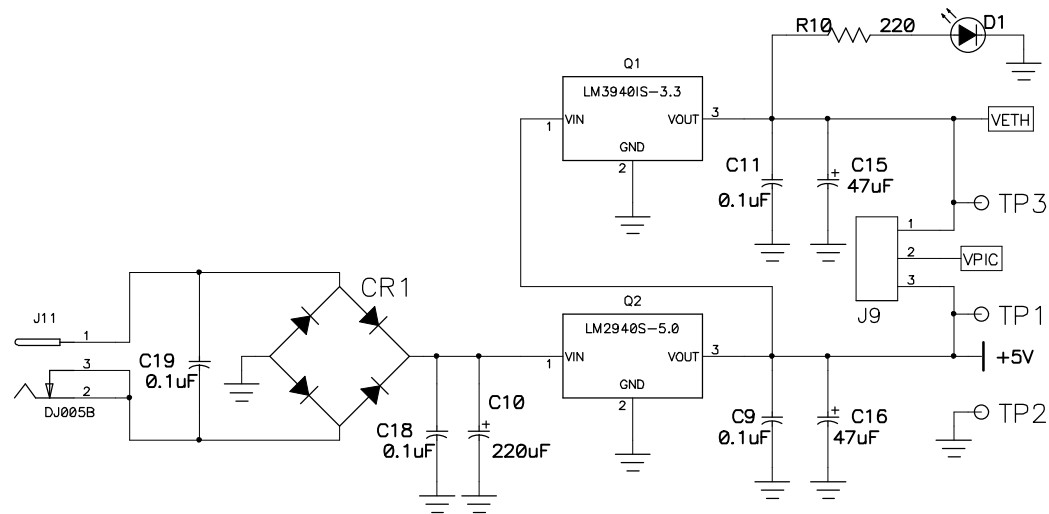
RB3 - ETH_CS
 RC5 - ETH_SI
 RC4 - ETH_SO
 RC3 - ETH_SCK
 RB2 - ETH_INT
 RB1 - ETH_WOL
 RB4 - EE_CS
 RB5 - EE_RESET



LVTTTL TO PIC
 level translator
 (not needed if PIC
 operating at 3.3V)



Title Ethernet PICtail™ Daughter Board			
Size B	 MICROCHIP	03-01841	Rev 4
Date: Fri Jun 24, 2005		Eng: R. Rojvanit	Drawn by: S. Humberd
Filename: 03-01841_Rev4.sch		Sheet 1	of 2



Title Ethernet PICTail Daughter Bo			
Size B	03-01841		Rev 4
Date: Fri Jun 24, 2005		Eng: R. Rojvanit	
Filename: 03-01841_Rev4.sch		Drawn by: S. Humberd	
		Sheet 2 of 2	