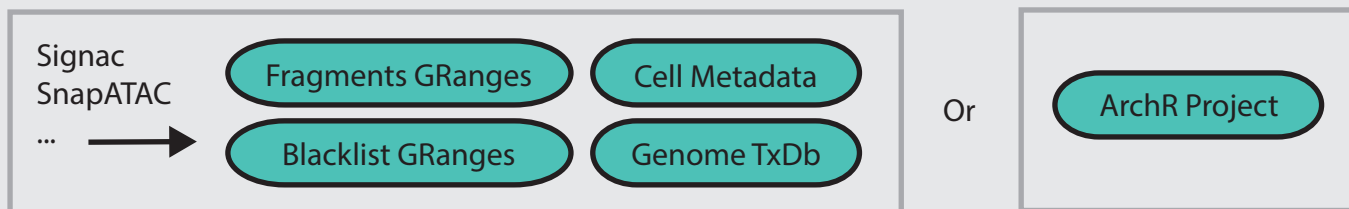
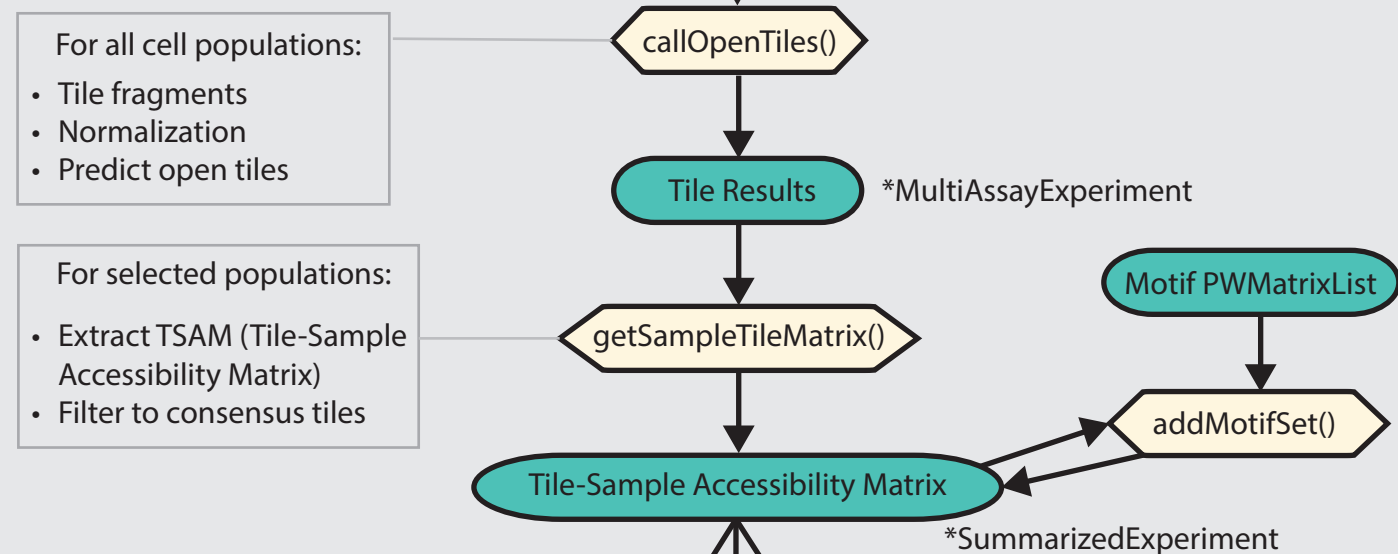


## a Inputs



## b Core Functionality



## c Downstream Analyses

