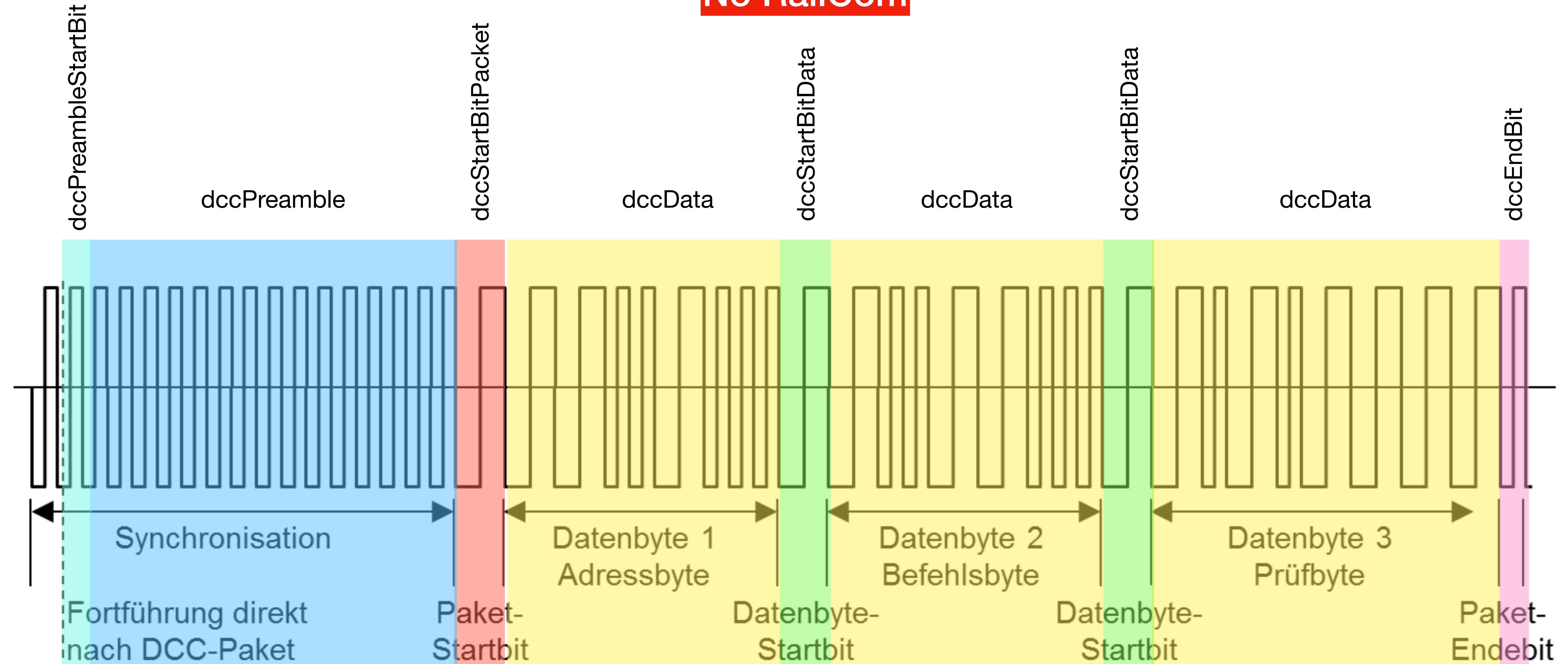

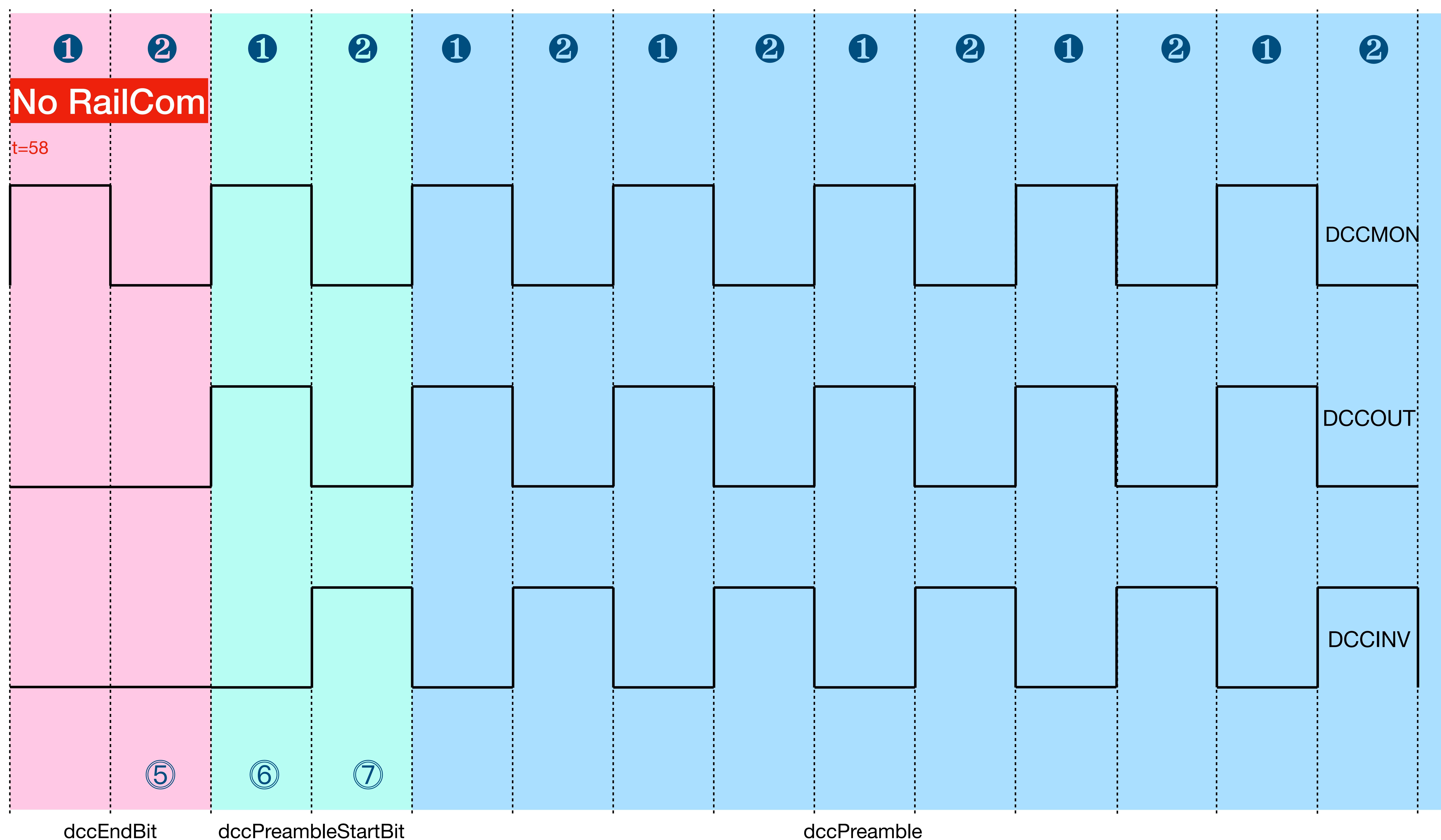


No RailCom



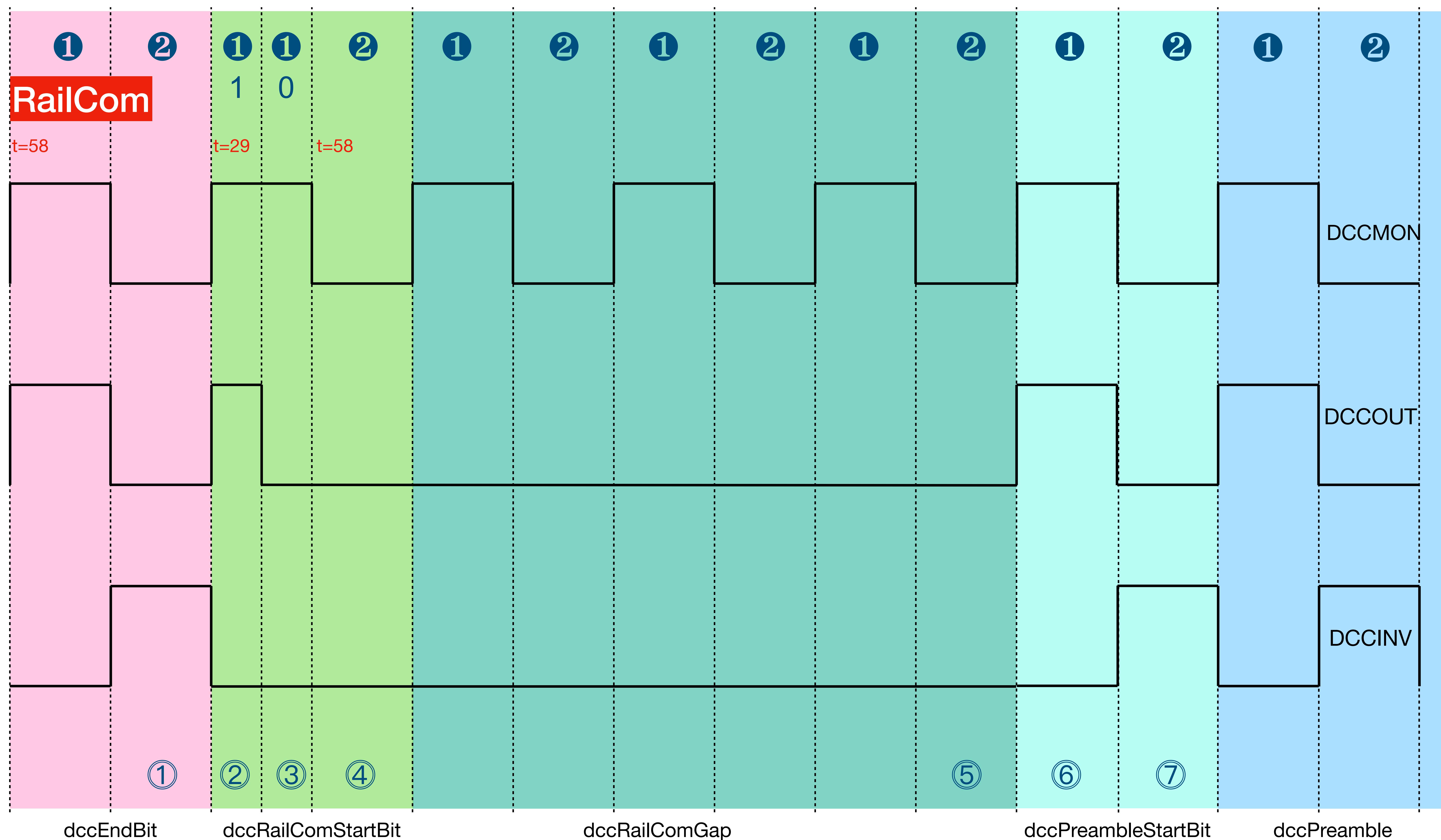




⑤ => dccPreambleStartBit / **ENABLE_DCCOUT**

⑥ **ENABLE_DCCINV**

⑦ => dccPreamble



① => dccRailComStartBit

② $t = 29 / \text{DISABLE_DCCMON \& DISABLE_DCCINV}$

③ $\text{ENABLE_DCCMON \& DISABLE_DCCOUT}$

④ => dccRailComGap / $t = 58$

⑤ => dccPreambleStartBit / ENABLE_DCCOUT

⑥ ENABLE_DCCINV

⑦ => dccPreamble