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1/* USER CODE BEGIN Header */
2/**
3 *****
4 * @file      : main.c
5 * @brief     : Main program body
6 *****
7 * @attention
8 *
9 * Copyright (c) 2023 STMicroelectronics.
10 * All rights reserved.
11 *
12 * This software is licensed under terms that can be found in the LICENSE file
13 * in the root directory of this software component.
14 * If no LICENSE file comes with this software, it is provided AS-IS.
15 *
16 *****
17 */
18/* USER CODE END Header */
19/* Includes -----*/
20#include "main.h"
21/** Github link
22//https://github.com/aimeesimons/NDXDAN019_SMNAIM002_EEE3096S.git
23 */
24/* Private includes -----*/
25/* USER CODE BEGIN Includes */
26#include <stdio.h>
27#include "stm32f0xx.h"
28#include <lcd_stm32f0.c>
29#include<stdbool.h>
30/* USER CODE END Includes */
31
32/* Private typedef -----*/
33/* USER CODE BEGIN PTD */
34
35/* USER CODE END PTD */
36
37/* Private define -----*/
38/* USER CODE BEGIN PD */
39
40/* USER CODE END PD */
41
42/* Private macro -----*/
43/* USER CODE BEGIN PM */
44
45/* USER CODE END PM */
46
47/* Private variables -----*/
48ADC_HandleTypeDef hadc;
49TIM_HandleTypeDef htim3;
50
51/* USER CODE BEGIN PV */
52uint32_t prev_millis = 0;
53uint32_t curr_millis = 0;
54uint32_t delay_t = 500; // Initialise delay to 500ms
55uint32_t adc_val;
56uint32_t start = 0;
57/* USER CODE END PV */

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58
59 /* Private function prototypes -----*/
60 void SystemClock_Config(void);
61 static void MX_GPIO_Init(void);
62 static void MX_ADC_Init(void);
63 static void MX_TIM3_Init(void);
64
65 /* USER CODE BEGIN PFP */
66 void EXTI0_1_IRQHandler(void);
67 void writeLCD(char *char_in);
68 uint32_t pollADC(void);
69 uint32_t ADCToCCR(uint32_t adc_val);
70 uint32_t val = 0;
71
72 /* USER CODE END PFP */
73
74 /* Private user code -----*/
75 /* USER CODE BEGIN 0 */
76
77 /* USER CODE END 0 */
78
79 /**
80  * @brief The application entry point.
81  * @retval int
82  */
83 int main(void)
84 {
85     /* USER CODE BEGIN 1 */
86     /* USER CODE END 1 */
87
88     /* MCU Configuration-----*/
89
90     /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
91     HAL_Init();
92
93     /* USER CODE BEGIN Init */
94     /* USER CODE END Init */
95
96     /* Configure the system clock */
97     SystemClock_Config();
98
99     /* USER CODE BEGIN SysInit */
100    /* USER CODE END SysInit */
101
102    /* Initialize all configured peripherals */
103    MX_GPIO_Init();
104    MX_ADC_Init();
105    MX_TIM3_Init();
106
107    /* USER CODE BEGIN 2 */
108    init_LCD();
109
110    // PWM setup
111    uint32_t CCR = 0;
112
113    HAL_TIM_PWM_Start(&htim3, TIM_CHANNEL_3); // Start PWM on TIM3 Channel 3
114    /* USER CODE END 2 */

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115
116 /* Infinite loop */
117 /* USER CODE BEGIN WHILE */
118 // lcd_putstring("Hello World.");
119
120 while (1)
121 {
122     // Toggle LED0
123     HAL_GPIO_TogglePin(GPIOB, LED7_Pin);
124
125     // ADC to LCD; TODO: Read POT1 value and write to LCD
126     HAL_ADC_Start_IT(&hadc);
127     val = pollADC();
128
129     char char_in[10];
130     sprintf(char_in, "%d", val);
131     writeLCD(char_in);
132
133     // Update PWM value; TODO: Get CRR
134     uint32_t CCR = ADCToCCR(val);
135     __HAL_TIM_SetCompare(&htim3, TIM_CHANNEL_3, CCR);
136
137     // Wait for delay ms
138     HAL_Delay(delay_t);
139     /* USER CODE END WHILE */
140
141     /* USER CODE BEGIN 3 */
142 }
143 /* USER CODE END 3 */
144
145
146 /**
147  * @brief System Clock Configuration
148  * @retval None
149  */
150 void SystemClock_Config(void)
151 {
152     LL_FLASH_SetLatency(LL_FLASH_LATENCY_0);
153     while (LL_FLASH_GetLatency() != LL_FLASH_LATENCY_0)
154     {
155     }
156     LL_RCC_HSI_Enable();
157
158     /* Wait till HSI is ready */
159     while (LL_RCC_HSI_IsReady() != 1)
160     {
161     }
162     LL_RCC_HSI_SetCalibTrimming(16);
163     LL_RCC_HSI14_Enable();
164
165     /* Wait till HSI14 is ready */
166     while (LL_RCC_HSI14_IsReady() != 1)
167     {
168     }
169
170     LL_RCC_HSI14_SetCalibTrimming(16);
171 }
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172 LL_RCC_SetAHBPrescaler(LL_RCC_SYSCLK_DIV_1);
173 LL_RCC_SetAPB1Prescaler(LL_RCC_APB1_DIV_1);
174 LL_RCC_SetSysClkSource(LL_RCC_SYS_CLKSOURCE_HSI);
175
176 /* Wait till System clock is ready */
177 while LL_RCC_GetSysClkSource() != LL_RCC_SYS_CLKSOURCE_STATUS_HSI)
178 {
179
180 }
181 LL_SetSystemCoreClock(8000000);
182
183 /* Update the time base */
184 if (HAL_InitTick (TICK_INT_PRIORITY) != HAL_OK)
185 {
186     Error_Handler();
187 }
188 LL_RCC_HSI14_EnableADCControl();
189
190
191 /**
192  * @brief ADC Initialization Function
193  * @param None
194  * @retval None
195  */
196 static void MX_ADC_Init(void)
197 {
198
199 /* USER CODE BEGIN ADC_Init 0 */
200 /* USER CODE END ADC_Init 0 */
201
202 ADC_ChannelConfTypeDef sConfig = {0};
203
204 /* USER CODE BEGIN ADC_Init 1 */
205
206 /* USER CODE END ADC_Init 1 */
207
208 /** Configure the global features of the ADC (Clock, Resolution, Data Alignment and number
    of conversion)
209 */
210 hadc.Instance = ADC1;
211 hadc.Init.ClockPrescaler = ADC_CLOCK_ASYNC_DIV1;
212 hadc.Init.Resolution = ADC_RESOLUTION_12B;
213 hadc.Init.DataAlign = ADC_DATAALIGN_RIGHT;
214 hadc.Init.ScanConvMode = ADC_SCAN_DIRECTION_FORWARD;
215 hadc.Init.EOCSelection = ADC_EOC_SINGLE_CONV;
216 hadc.Init.LowPowerAutoWait = DISABLE;
217 hadc.Init.LowPowerAutoPowerOff = DISABLE;
218 hadc.Init.ContinuousConvMode = DISABLE;
219 hadc.Init.DiscontinuousConvMode = DISABLE;
220 hadc.Init.ExternalTrigConv = ADC_SOFTWARE_START;
221 hadc.Init.ExternalTrigConvEdge = ADC_EXTERNALTRIGCONVEDGE_NONE;
222 hadc.Init.DMAContinuousRequests = DISABLE;
223 hadc.Init.Overrun = ADC_OVR_DATA_PRESERVED;
224 if (HAL_ADC_Init(&hadc) != HAL_OK)
225 {
226     Error_Handler();
227 }

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228
229 /** Configure for the selected ADC regular channel to be converted.
230 */
231 sConfig.Channel = ADC_CHANNEL_6;
232 sConfig.Rank = ADC_RANK_CHANNEL_NUMBER;
233 sConfig.SamplingTime = ADC_SAMPLETIME_1CYCLE_5;
234 if (HAL_ADC_ConfigChannel(&hadc, &sConfig) != HAL_OK)
235 {
236     Error_Handler();
237 }
238 /* USER CODE BEGIN ADC_Init 2 */
239 ADC1->CR |= ADC_CR_ADSCAL;
240 while (ADC1->CR & ADC_CR_ADSCAL); // Calibrate the ADC
241 ADC1->CR |= (1 << 0); // Enable ADC
242 while (ADC1->ISR & (1 << 0)) == 0; // Wait for ADC ready
243 /* USER CODE END ADC_Init 2 */
244
245
246
247 /**
248  * @brief TIM3 Initialization Function
249  * @param None
250  * @retval None
251  */
252 static void MX_TIM3_Init(void)
253 {
254
255     /* USER CODE BEGIN TIM3_Init 0 */
256
257     /* USER CODE END TIM3_Init 0 */
258
259     TIM_ClockConfigTypeDef sClockSourceConfig = {0};
260     TIM_MasterConfigTypeDef sMasterConfig = {0};
261     TIM_OC_InitTypeDef sConfigOC = {0};
262
263     /* USER CODE BEGIN TIM3_Init 1 */
264
265     /* USER CODE END TIM3_Init 1 */
266     htim3.Instance = TIM3;
267     htim3.Init.Prescaler = 0;
268     htim3.Init.CounterMode = TIM_COUNTERMODE_UP;
269     htim3.Init.Period = 47999;
270     htim3.Init.ClockDivision = TIM_CLOCKDIVISION_DIV1;
271     htim3.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_DISABLE;
272     if (HAL_TIM_Base_Init(&htim3) != HAL_OK)
273     {
274         Error_Handler();
275     }
276     sClockSourceConfig.ClockSource = TIM_CLOCKSOURCE_INTERNAL;
277     if (HAL_TIM_ConfigClockSource(&htim3, &sClockSourceConfig) != HAL_OK)
278     {
279         Error_Handler();
280     }
281     if (HAL_TIM_PWM_Init(&htim3) != HAL_OK)
282     {
283         Error_Handler();
284     }

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285 sMasterConfig.MasterOutputTrigger = TIM_TRGO_RESET;
286 sMasterConfig.MasterSlaveMode = TIM_MASTERSLAVEMODE_DISABLE;
287 if (HAL_TIMEx_MasterConfigSynchronization(&htim3, &sMasterConfig) != HAL_OK)
288 {
289     Error_Handler();
290 }
291 sConfigOC.OCMode = TIM_OCMODE_PWM1;
292 sConfigOC.Pulse = 0;
293 sConfigOC.OCpolarity = TIM_OCPOLARITY_HIGH;
294 sConfigOC.OCFastMode = TIM_OCFAST_DISABLE;
295 if (HAL_TIM_PWM_ConfigChannel(&htim3, &sConfigOC, TIM_CHANNEL_3) != HAL_OK)
296 {
297     Error_Handler();
298 }
299 /* USER CODE BEGIN TIM3_Init 2 */
300
301 /* USER CODE END TIM3_Init 2 */
302 HAL_TIM_MspPostInit(&htim3);
303
304 }
305
306 /**
307  * @brief GPIO Initialization Function
308  * @param None
309  * @retval None
310  */
311 static void MX_GPIO_Init(void)
312 {
313     LL_EXTI_InitTypeDef EXTI_InitStruct = {0};
314     LL_GPIO_InitTypeDef GPIO_InitStruct = {0};
315 /* USER CODE BEGIN MX_GPIO_Init_1 */
316 /* USER CODE END MX_GPIO_Init_1 */
317
318 /* GPIO Ports Clock Enable */
319 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOF);
320 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOA);
321 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOB);
322
323 /**/
324 LL_GPIO_ResetOutputPin(LED7_GPIO_Port, LED7_Pin);
325
326 /**/
327 LL_SYSCFG_SetEXTISource(LL_SYSCFG_EXTI_PORTA, LL_SYSCFG_EXTI_LINE0);
328
329 /**/
330 LL_GPIO_SetPinPull(Button0_GPIO_Port, Button0_Pin, LL_GPIO_PULL_UP);
331
332 /**/
333 LL_GPIO_SetPinMode(Button0_GPIO_Port, Button0_Pin, LL_GPIO_MODE_INPUT);
334
335 /**/
336 EXTI_InitStruct.Line_0_31 = LL_EXTI_LINE_0;
337 EXTI_InitStruct.LineCommand = ENABLE;
338 EXTI_InitStruct.Mode = LL_EXTI_MODE_IT;
339 EXTI_InitStruct.Trigger = LL_EXTI_TRIGGER_RISING;
340 LL_EXTI_Init(&EXTI_InitStruct);
341

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342  /**/
343  GPIO_InitStruct.Pin = LED7_Pin;
344  GPIO_InitStruct.Mode = LL_GPIO_MODE_OUTPUT;
345  GPIO_InitStruct.Speed = LL_GPIO_SPEED_FREQ_LOW;
346  GPIO_InitStruct.OutputType = LL_GPIO_OUTPUT_PUSHPULL;
347  GPIO_InitStruct.Pull = LL_GPIO_PULL_NO;
348  LL_GPIO_Init(LED7_GPIO_Port, &GPIO_InitStruct);
349
350 /* USER CODE BEGIN MX_GPIO_Init_2 */
351 HAL_NVIC_SetPriority(EXTI0_1_IRQn, 0, 0);
352 HAL_NVIC_EnableIRQ(EXTI0_1_IRQn);
353 /* USER CODE END MX_GPIO_Init_2 */
354 }
355
356 /* USER CODE BEGIN 4 */
357 void EXTI0_1_IRQHandler(void)
358 {
359     // TODO: Add code to switch LED7 delay frequency
360
361     if(HAL_GetTick()-start>1000){
362         if(delay_t==1000){
363             delay_t=500;
364         }
365         else{
366             delay_t=1000;
367         }
368     }
369     start = HAL_GetTick();
370 }
371
372
373
374
375     HAL_GPIO_EXTI_IRQHandler(Button0_Pin); // Clear interrupt flags
376
377
378 }
379
380 // TODO: Complete the writeLCD function
381 void writeLCD(char *char_in){
382     lcd_command(CLEAR);
383     lcd_putstr(char_in);
384     delay(3000);
385
386
387 }
388
389 // Get ADC value
390 uint32_t pollADC(void){
391     // TODO: Complete function body to get ADC val
392     //HAL_ADC_PollForConversion(&hadc,5);
393     uint32_t val = HAL_ADC_GetValue(&hadc);
394     return val;
395 }
396
397 // Calculate PWM CCR value
398 uint32_t ADCToCCR(uint32_t adc_val){

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```
399 // TODO: Calculate CCR val using an appropriate equation
400 float val;
401 // ADC = 0-4095
402 float dutyCycle = ((float) adc_val/4095);
403
404 val = (dutyCycle*47999);
405
406 return (int) val;
407 }
408
409 void ADC1_COMP_IRQHandler(void)
410 {
411     adc_val = HAL_ADC_GetValue(&hadc); // read adc value
412     HAL_ADC_IRQHandler(&hadc); //Clear flags
413 }
414 /* USER CODE END 4 */
415
416 /**
417  * @brief This function is executed in case of error occurrence.
418  * @retval None
419  */
420 void Error_Handler(void)
421 {
422     /* USER CODE BEGIN Error_Handler_Debug */
423     /* User can add his own implementation to report the HAL error return state */
424     __disable_irq();
425     while (1)
426     {
427     }
428     /* USER CODE END Error_Handler_Debug */
429 }
430
431 #ifndef USE_FULL_ASSERT
432 /**
433  * @brief Reports the name of the source file and the source line number
434  * where the assert_param error has occurred.
435  * @param file: pointer to the source file name
436  * @param line: assert_param error line source number
437  * @retval None
438  */
439 void assert_failed(uint8_t *file, uint32_t line)
440 {
441     /* USER CODE BEGIN 6 */
442     /* User can add his own implementation to report the file name and line number,
443     ex: printf("Wrong parameters value: file %s on line %d\r\n", file, line) */
444     /* USER CODE END 6 */
445 }
446 #endif /* USE_FULL_ASSERT */
447
```