

```

1/* USER CODE BEGIN Header */
2/**
3 *****
4 * @file      : main.c
5 * @brief     : Main program body
6 *****
7 * @attention
8 *
9 * Copyright (c) 2023 STMicroelectronics.
10 * All rights reserved.
11 *
12 * This software is licensed under terms that can be found in the LICENSE file
13 * in the root directory of this software component.
14 * If no LICENSE file comes with this software, it is provided AS-IS.
15 *
16 *****
17 */
18/* USER CODE END Header */
19/* Includes -----*/
20#include "main.h"
21
22/* Private includes -----*/
23/* USER CODE BEGIN Includes */
24#include <stdio.h>
25#include "stm32f0xx.h"
26#include <lcd_stm32f0.c>
27#include<stdbool.h>
28/* USER CODE END Includes */
29
30/* Private typedef -----*/
31/* USER CODE BEGIN PTD */
32
33/* USER CODE END PTD */
34
35/* Private define -----*/
36/* USER CODE BEGIN PD */
37
38/* USER CODE END PD */
39
40/* Private macro -----*/
41/* USER CODE BEGIN PM */
42
43/* USER CODE END PM */
44
45/* Private variables -----*/
46ADC_HandleTypeDef hadc;
47TIM_HandleTypeDef htim3;
48
49/* USER CODE BEGIN PV */
50uint32_t prev_millis = 0;
51uint32_t curr_millis = 0;
52uint32_t delay_t = 500; // Initialise delay to 500ms
53uint32_t adc_val;
54uint32_t start = 0;
55/* USER CODE END PV */
56
57/* Private function prototypes -----*/

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```

58 void SystemClock_Config(void);
59 static void MX_GPIO_Init(void);
60 static void MX_ADC_Init(void);
61 static void MX_TIM3_Init(void);
62
63 /* USER CODE BEGIN PFP */
64 void EXTI0_1_IRQHandler(void);
65 void writeLCD(char *char_in);
66 uint32_t pollADC(void);
67 uint32_t ADCToCCR(uint32_t adc_val);
68 uint32_t val = 0;
69
70 /* USER CODE END PFP */
71
72 /* Private user code -----*/
73 /* USER CODE BEGIN 0 */
74
75 /* USER CODE END 0 */
76
77 /**
78  * @brief The application entry point.
79  * @retval int
80  */
81 int main(void)
82 {
83     /* USER CODE BEGIN 1 */
84     /* USER CODE END 1 */
85
86     /* MCU Configuration-----*/
87
88     /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
89     HAL_Init();
90
91     /* USER CODE BEGIN Init */
92     /* USER CODE END Init */
93
94     /* Configure the system clock */
95     SystemClock_Config();
96
97     /* USER CODE BEGIN SysInit */
98     /* USER CODE END SysInit */
99
100    /* Initialize all configured peripherals */
101    MX_GPIO_Init();
102    MX_ADC_Init();
103    MX_TIM3_Init();
104
105    /* USER CODE BEGIN 2 */
106    init_LCD();
107
108    // PWM setup
109    uint32_t CCR = 0;
110
111    HAL_TIM_PWM_Start(&htim3, TIM_CHANNEL_3); // Start PWM on TIM3 Channel 3
112    /* USER CODE END 2 */
113
114    /* Infinite loop */

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```
115 /* USER CODE BEGIN WHILE */
116 // lcd_putstring("Hello World.");
117
118 while (1)
119 {
120     // Toggle LED0
121     HAL_GPIO_TogglePin(GPIOB, LED7_Pin);
122
123     // ADC to LCD; TODO: Read POT1 value and write to LCD
124     HAL_ADC_Start_IT(&hadc);
125     val = pollADC();
126
127     char char_in[10];
128     sprintf(char_in, "%d", val);
129     writeLCD(char_in);
130
131     // Update PWM value; TODO: Get CRR
132     uint32_t CCR = ADCToCCR(val);
133     __HAL_TIM_SetCompare(&htim3, TIM_CHANNEL_3, CCR);
134
135     // Wait for delay ms
136     HAL_Delay(delay_t);
137     /* USER CODE END WHILE */
138
139     /* USER CODE BEGIN 3 */
140 }
141 /* USER CODE END 3 */
142
143
144 /**
145  * @brief System Clock Configuration
146  * @retval None
147  */
148 void SystemClock_Config(void)
149 {
150     LL_FLASH_SetLatency(LL_FLASH_LATENCY_0);
151     while (LL_FLASH_GetLatency() != LL_FLASH_LATENCY_0)
152     {
153     }
154     LL_RCC_HSI_Enable();
155
156     /* Wait till HSI is ready */
157     while (LL_RCC_HSI_IsReady() != 1)
158     {
159     }
160
161     LL_RCC_HSI_SetCalibTrimming(16);
162     LL_RCC_HSI14_Enable();
163
164     /* Wait till HSI14 is ready */
165     while (LL_RCC_HSI14_IsReady() != 1)
166     {
167     }
168
169     LL_RCC_HSI14_SetCalibTrimming(16);
170     LL_RCC_SetAHBPrescaler(LL_RCC_SYSCLK_DIV_1);
171     LL_RCC_SetAPB1Prescaler(LL_RCC_APB1_DIV_1);
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172 LL_RCC_SetSysClkSource(LL_RCC_SYS_CLKSOURCE_HSI);
173
174 /* Wait till System clock is ready */
175 while LL_RCC_GetSysClkSource() != LL_RCC_SYS_CLKSOURCE_STATUS_HSI
176 {
177
178 }
179 LL_SetSystemCoreClock(8000000);
180
181 /* Update the time base */
182 if (HAL_InitTick (TICK_INT_PRIORITY) != HAL_OK)
183 {
184     Error_Handler();
185 }
186 LL_RCC_HSI14_EnableADCControl();
187
188
189 /**
190  * @brief ADC Initialization Function
191  * @param None
192  * @retval None
193  */
194 static void MX_ADC_Init(void)
195 {
196
197     /* USER CODE BEGIN ADC_Init 0 */
198     /* USER CODE END ADC_Init 0 */
199
200     ADC_ChannelConfTypeDef sConfig = {0};
201
202     /* USER CODE BEGIN ADC_Init 1 */
203
204     /* USER CODE END ADC_Init 1 */
205
206     /** Configure the global features of the ADC (Clock, Resolution, Data Alignment and number
207     of conversion)
208     */
209     hadc.Instance = ADC1;
210     hadc.Init.ClockPrescaler = ADC_CLOCK_ASYNC_DIV1;
211     hadc.Init.Resolution = ADC_RESOLUTION_12B;
212     hadc.Init.DataAlign = ADC_DATAALIGN_RIGHT;
213     hadc.Init.ScanConvMode = ADC_SCAN_DIRECTION_FORWARD;
214     hadc.Init.EOCSelection = ADC_EOC_SINGLE_CONV;
215     hadc.Init.LowPowerAutoWait = DISABLE;
216     hadc.Init.LowPowerAutoPowerOff = DISABLE;
217     hadc.Init.ContinuousConvMode = DISABLE;
218     hadc.Init.DiscontinuousConvMode = DISABLE;
219     hadc.Init.ExternalTrigConv = ADC_SOFTWARE_START;
220     hadc.Init.ExternalTrigConvEdge = ADC_EXTERNALTRIGCONVEDGE_NONE;
221     hadc.Init.DMAContinuousRequests = DISABLE;
222     hadc.Init.Overrun = ADC_OVR_DATA_PRESERVED;
223     if (HAL_ADC_Init(&hadc) != HAL_OK)
224     {
225         Error_Handler();
226     }
227
228     /** Configure for the selected ADC regular channel to be converted.
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228 */
229 sConfig.Channel = ADC_CHANNEL_6;
230 sConfig.Rank = ADC_RANK_CHANNEL_NUMBER;
231 sConfig.SamplingTime = ADC_SAMPLETIME_1CYCLE_5;
232 if (HAL_ADC_ConfigChannel(&hadc, &sConfig) != HAL_OK)
233 {
234     Error_Handler();
235 }
236 /* USER CODE BEGIN ADC_Init 2 */
237 ADC1->CR |= ADC_CR_ADSCAL;
238 while (ADC1->CR & ADC_CR_ADSCAL);           // Calibrate the ADC
239 ADC1->CR |= (1 << 0);                       // Enable ADC
240 while ((ADC1->ISR & (1 << 0)) == 0);        // Wait for ADC ready
241 /* USER CODE END ADC_Init 2 */
242
243
244
245 /**
246  * @brief TIM3 Initialization Function
247  * @param None
248  * @retval None
249  */
250 static void MX_TIM3_Init(void)
251 {
252
253     /* USER CODE BEGIN TIM3_Init 0 */
254
255     /* USER CODE END TIM3_Init 0 */
256
257     TIM_ClockConfigTypeDef sClockSourceConfig = {0};
258     TIM_MasterConfigTypeDef sMasterConfig = {0};
259     TIM_OC_InitTypeDef sConfigOC = {0};
260
261     /* USER CODE BEGIN TIM3_Init 1 */
262
263     /* USER CODE END TIM3_Init 1 */
264     htim3.Instance = TIM3;
265     htim3.Init.Prescaler = 0;
266     htim3.Init.CounterMode = TIM_COUNTERMODE_UP;
267     htim3.Init.Period = 47999;
268     htim3.Init.ClockDivision = TIM_CLOCKDIVISION_DIV1;
269     htim3.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_DISABLE;
270     if (HAL_TIM_Base_Init(&htim3) != HAL_OK)
271     {
272         Error_Handler();
273     }
274     sClockSourceConfig.ClockSource = TIM_CLOCKSOURCE_INTERNAL;
275     if (HAL_TIM_ConfigClockSource(&htim3, &sClockSourceConfig) != HAL_OK)
276     {
277         Error_Handler();
278     }
279     if (HAL_TIM_PWM_Init(&htim3) != HAL_OK)
280     {
281         Error_Handler();
282     }
283     sMasterConfig.MasterOutputTrigger = TIM_TRGO_RESET;
284     sMasterConfig.MasterSlaveMode = TIM_MASTERSLAVEMODE_DISABLE;

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285 if (HAL_TIMEx_MasterConfigSynchronization(&htim3, &sMasterConfig) != HAL_OK)
286 {
287     Error_Handler();
288 }
289 sConfigOC.OCMode = TIM_OCMODE_PWM1;
290 sConfigOC.Pulse = 0;
291 sConfigOC.OCpolarity = TIM_OCPOLARITY_HIGH;
292 sConfigOC.OCFastMode = TIM_OCFAST_DISABLE;
293 if (HAL_TIM_PWM_ConfigChannel(&htim3, &sConfigOC, TIM_CHANNEL_3) != HAL_OK)
294 {
295     Error_Handler();
296 }
297 /* USER CODE BEGIN TIM3_Init 2 */
298
299 /* USER CODE END TIM3_Init 2 */
300 HAL_TIM_MspPostInit(&htim3);
301
302 }
303
304 /**
305  * @brief GPIO Initialization Function
306  * @param None
307  * @retval None
308  */
309 static void MX_GPIO_Init(void)
310 {
311     LL_EXTI_InitTypeDef EXTI_InitStruct = {0};
312     LL_GPIO_InitTypeDef GPIO_InitStruct = {0};
313 /* USER CODE BEGIN MX_GPIO_Init_1 */
314 /* USER CODE END MX_GPIO_Init_1 */
315
316 /* GPIO Ports Clock Enable */
317 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOF);
318 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOA);
319 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOB);
320
321 /**/
322 LL_GPIO_ResetOutputPin(LED7_GPIO_Port, LED7_Pin);
323
324 /**/
325 LL_SYSCFG_SetEXTISource(LL_SYSCFG_EXTI_PORTA, LL_SYSCFG_EXTI_LINE0);
326
327 /**/
328 LL_GPIO_SetPinPull(Button0_GPIO_Port, Button0_Pin, LL_GPIO_PULL_UP);
329
330 /**/
331 LL_GPIO_SetPinMode(Button0_GPIO_Port, Button0_Pin, LL_GPIO_MODE_INPUT);
332
333 /**/
334 EXTI_InitStruct.Line_0_31 = LL_EXTI_LINE_0;
335 EXTI_InitStruct.LineCommand = ENABLE;
336 EXTI_InitStruct.Mode = LL_EXTI_MODE_IT;
337 EXTI_InitStruct.Trigger = LL_EXTI_TRIGGER_RISING;
338 LL_EXTI_Init(&EXTI_InitStruct);
339
340 /**/
341 GPIO_InitStruct.Pin = LED7_Pin;

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```
342 GPIO_InitStruct.Mode = LL_GPIO_MODE_OUTPUT;
343 GPIO_InitStruct.Speed = LL_GPIO_SPEED_FREQ_LOW;
344 GPIO_InitStruct.OutputType = LL_GPIO_OUTPUT_PUSHPULL;
345 GPIO_InitStruct.Pull = LL_GPIO_PULL_NO;
346 LL_GPIO_Init(LED7_GPIO_Port, &GPIO_InitStruct);
347
348 /* USER CODE BEGIN MX_GPIO_Init_2 */
349 HAL_NVIC_SetPriority(EXTI0_1_IRQn, 0, 0);
350 HAL_NVIC_EnableIRQ(EXTI0_1_IRQn);
351 /* USER CODE END MX_GPIO_Init_2 */
352 }
353
354 /* USER CODE BEGIN 4 */
355 void EXTI0_1_IRQHandler(void)
356 {
357     // TODO: Add code to switch LED7 delay frequency
358
359     if(HAL_GetTick()-start>1000){
360         if(delay_t==1000){
361             delay_t=500;
362         }
363         else{
364             delay_t=1000;
365         }
366     }
367
368     start = HAL_GetTick();
369
370 }
371
372
373 HAL_GPIO_EXTI_IRQHandler(Button0_Pin); // Clear interrupt flags
374
375
376 }
377
378 // TODO: Complete the writeLCD function
379 void writeLCD(char *char_in){
380     lcd_command(CLEAR);
381     lcd_putstr(char_in);
382     delay(3000);
383
384
385 }
386
387 // Get ADC value
388 uint32_t pollADC(void) {
389     // TODO: Complete function body to get ADC val
390     //HAL_ADC_PollForConversion(&hadc,5);
391     uint32_t val = HAL_ADC_GetValue(&hadc);
392     return val;
393 }
394
395 // Calculate PWM CCR value
396 uint32_t ADCToCCR(uint32_t adc_val){
397     // TODO: Calculate CCR val using an appropriate equation
398     float val;
```

```
399 // ADC = 0-4095
400 float dutyCycle = ((float adc_val/4095);
401
402 val = (dutyCycle*47999);
403
404 return (int)val;
405 }
406
407 void ADC1_COMP_IRQHandler(void)
408 {
409     adc_val = HAL_ADC_GetValue(&hadc); // read adc value
410     HAL_ADC_IRQHandler(&hadc); //Clear flags
411 }
412 /* USER CODE END 4 */
413
414 /**
415  * @brief This function is executed in case of error occurrence.
416  * @retval None
417  */
418 void Error_Handler(void)
419 {
420     /* USER CODE BEGIN Error_Handler_Debug */
421     /* User can add his own implementation to report the HAL error return state */
422     __disable_irq();
423     while (1)
424     {
425     }
426     /* USER CODE END Error_Handler_Debug */
427 }
428
429 #ifndef USE_FULL_ASSERT
430 /**
431  * @brief Reports the name of the source file and the source line number
432  * where the assert_param error has occurred.
433  * @param file: pointer to the source file name
434  * @param line: assert_param error line source number
435  * @retval None
436  */
437 void assert_failed(uint8_t *file, uint32_t line)
438 {
439     /* USER CODE BEGIN 6 */
440     /* User can add his own implementation to report the file name and line number,
441     ex: printf("Wrong parameters value: file %s on line %d\r\n", file, line) */
442     /* USER CODE END 6 */
443 }
444 #endif /* USE_FULL_ASSERT */
445
```