

AlMer v2.1 and Beyond

2025 KMS Spring Meeting

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²KAIST

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MPC-in-the-Head (MPCitH)

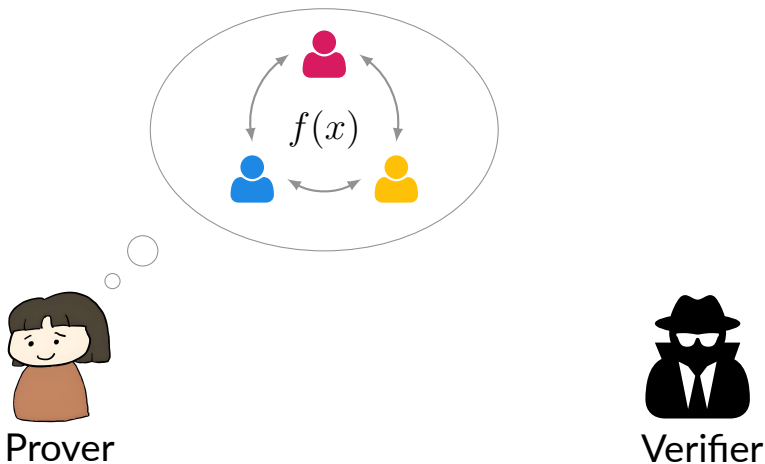


Prover

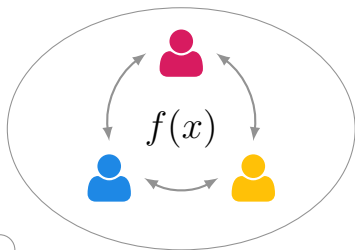


Verifier

MPC-in-the-Head (MPCitH)



MPC-in-the-Head (MPCitH)



Which MPC?
Garbled circuit
GMW
Beaver triple

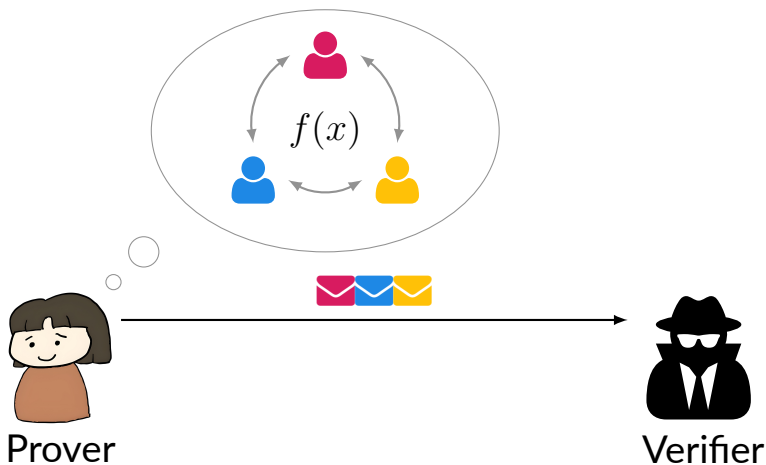


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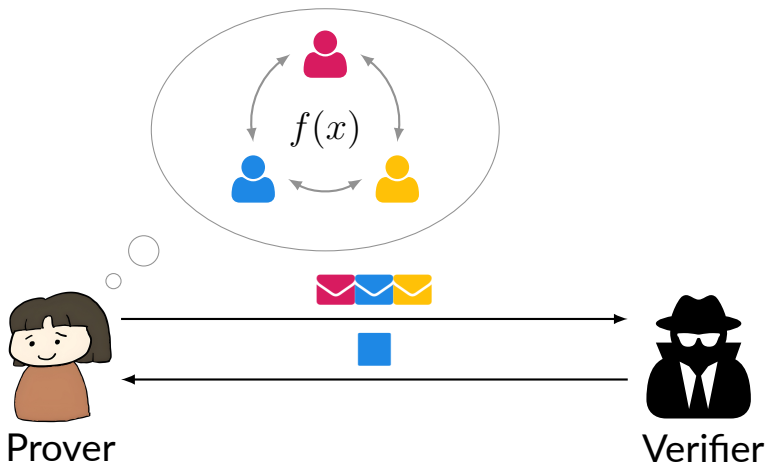


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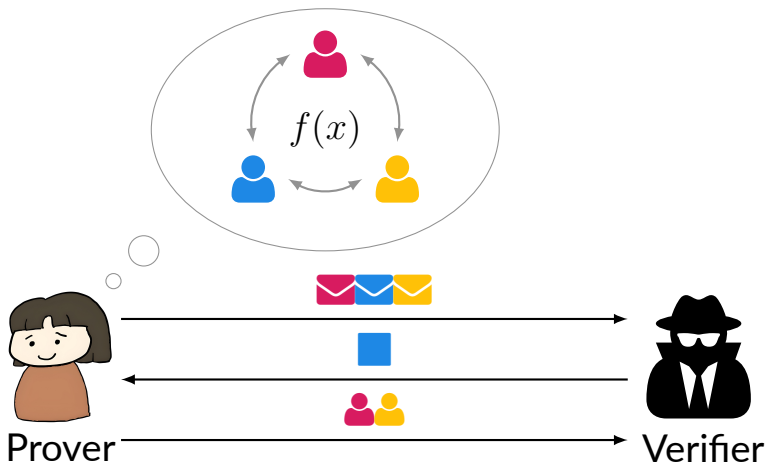
MPC-in-the-Head (MPCitH)



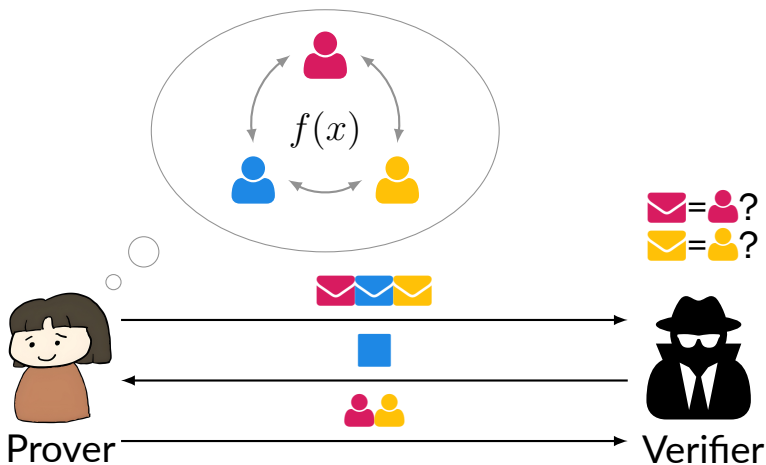
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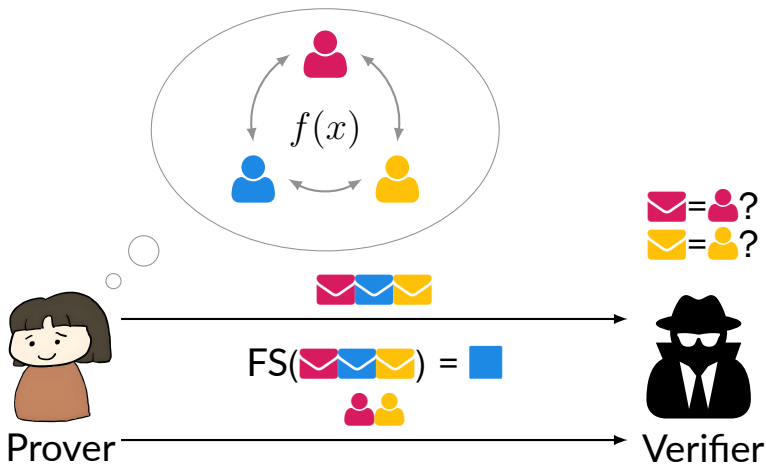
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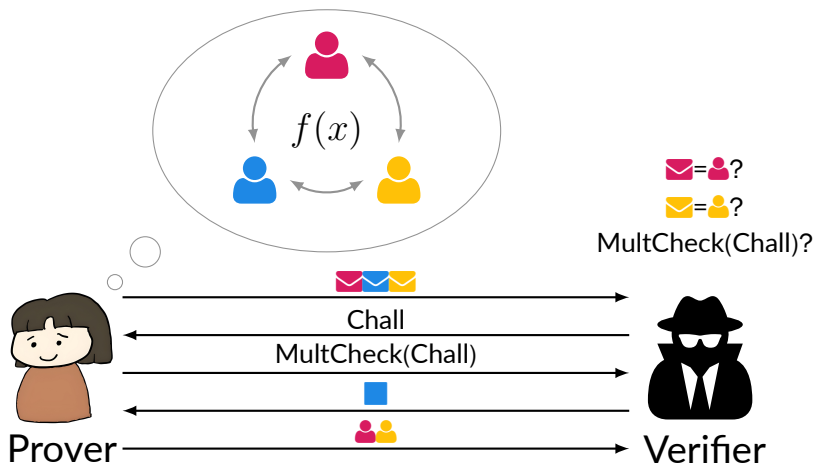
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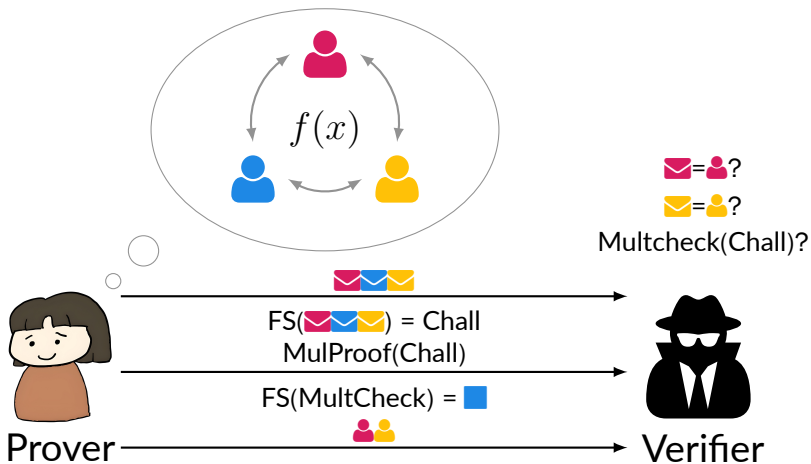
MPCitH-based Signature



Recent MPCitH

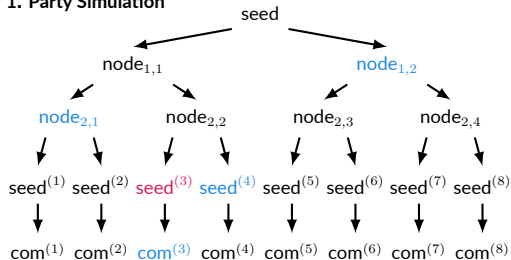


Recent MPCitH-based Signature



Detailed MPCitH

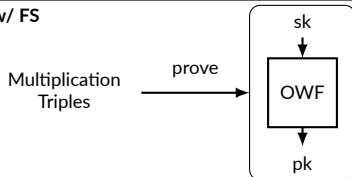
1. Party Simulation



2. Multiplication triple generation

$$\begin{aligned} \text{PRG}(\text{seed}^{(1)}) &= (w_1^{(1)}, \dots, w_C^{(1)}, a_1^{(1)}, \dots, a_C^{(1)}, b_1^{(1)}, \dots, b_C^{(1)}, c^{(1)}) \\ &\vdots \\ \text{PRG}(\text{seed}^{(N)}) &= (w_1^{(N)}, \dots, w_C^{(N)}, a_1^{(N)}, \dots, a_C^{(N)}, b_1^{(N)}, \dots, b_C^{(N)}, c^{(N)}) \end{aligned}$$

3. Proof w/ FS

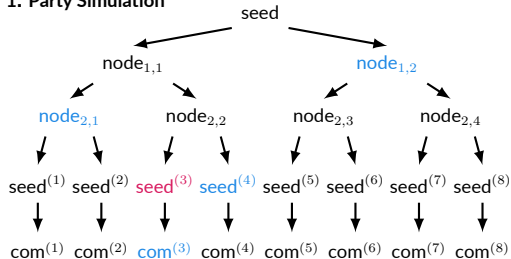


4. Party Opening

Choose i using FS!

Detailed MPCitH

1. Party Simulation



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3. Proof w/ FS

Proving $x \cdot y = z$

$$\alpha^{(i)} = \epsilon \cdot x^{(i)} + a^{(i)}$$

$$\beta^{(i)} = y^{(i)} + b^{(i)}$$

Broadcast α and β

$$\text{Check } \sum_i (\epsilon z^{(i)} - c^{(i)} + \alpha b^{(i)} + \beta a^{(i)} - \alpha \beta) = 0$$

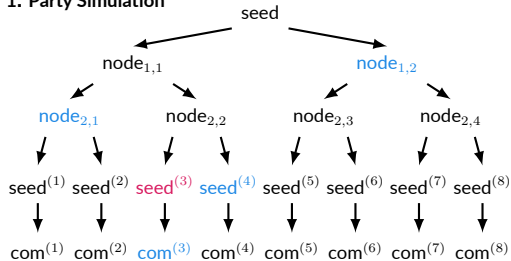
where $ab = c$

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3. Proof w/ FS

Proving $x_j \cdot y_j = z_j$

$$\alpha_j^{(i)} = \epsilon_j \cdot x_j^{(i)} + a_j^{(i)}$$

$$\beta_j^{(i)} = y_j^{(i)} + b_j^{(i)}$$

Broadcast α_j and β_j

$$\text{Check } \sum_i (\sum_j (\epsilon_j z_j^{(i)} + \alpha_j b_j^{(i)} + \beta_j a_j^{(i)} - \alpha_j \beta_j) - c^{(i)}) = 0$$

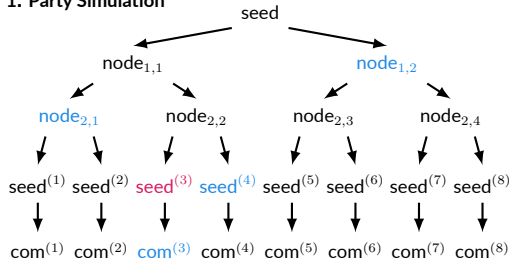
where $\sum_j a_j b_j = c$

4. Party Opening

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AlMer v1.0

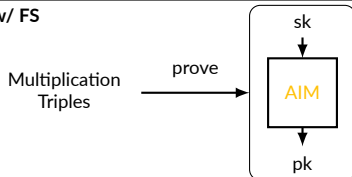
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$$\begin{aligned} \text{PRG}(\text{seed}^{(1)}) &= (w_1^{(1)}, \dots, w_C^{(1)}, a_1^{(1)}, \dots, a_C^{(1)}, b_1^{(1)}, \dots, b_C^{(1)}, c^{(1)}) \\ &\vdots \\ \text{PRG}(\text{seed}^{(N)}) &= (w_1^{(N)}, \dots, w_C^{(N)}, a_1^{(N)}, \dots, a_C^{(N)}, b_1^{(N)}, \dots, b_C^{(N)}, c^{(N)}) \end{aligned}$$

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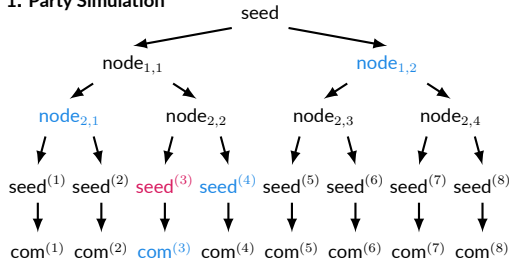


4. Party Opening

Choose i using FS!

AIMer v2.0

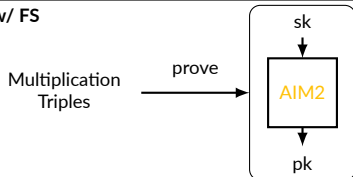
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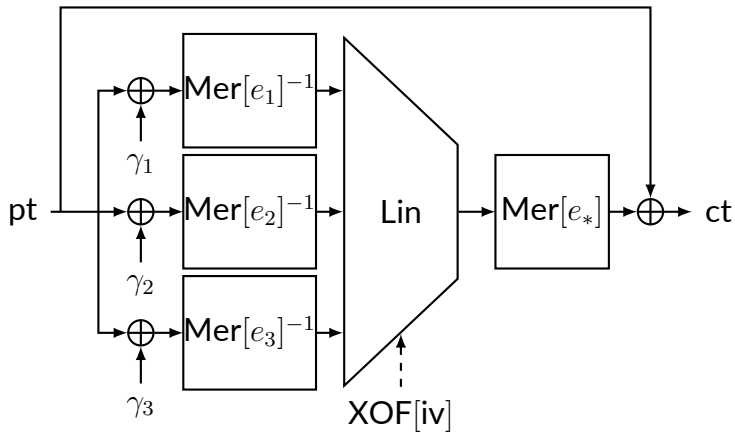
3. Proof w/ FS



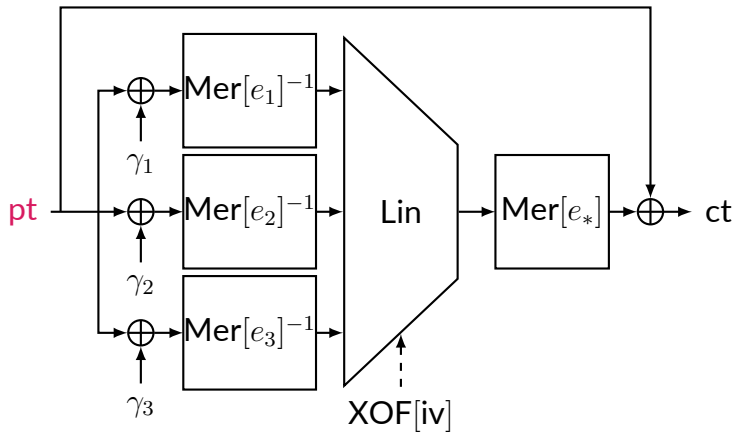
4. Party Opening

Choose i using FS!

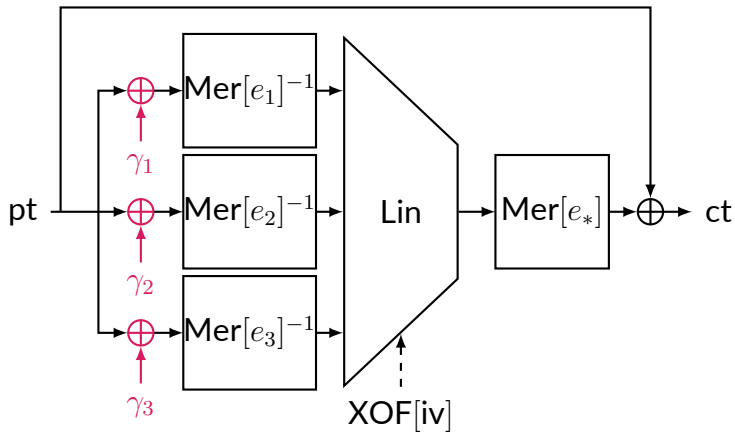
AIM2



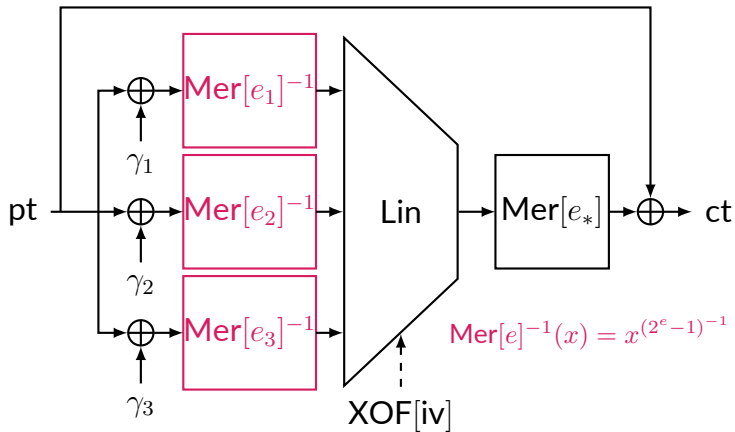
AIM2



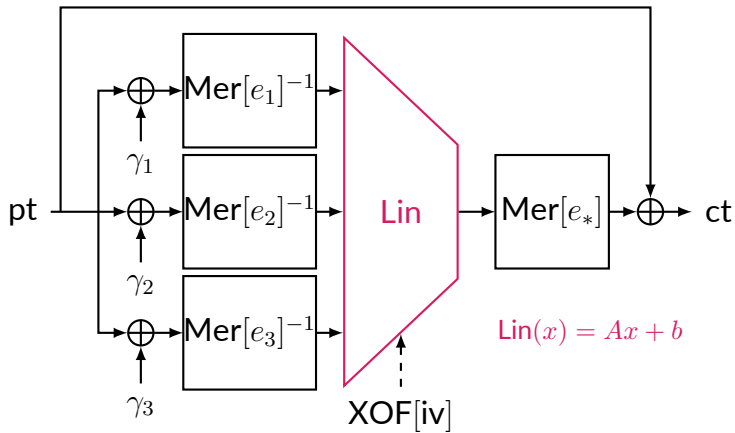
AIM2



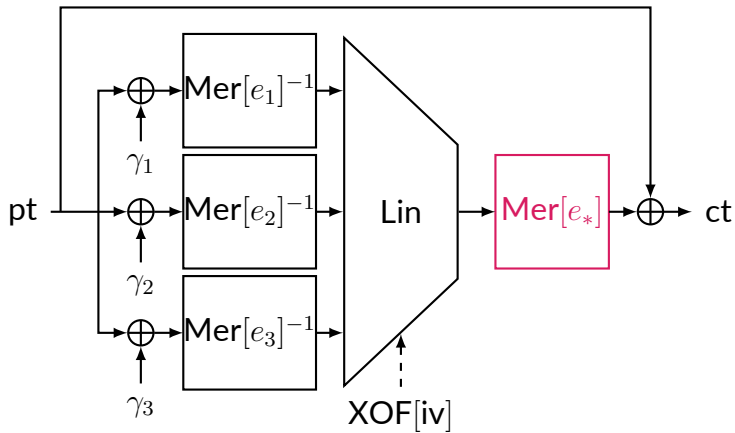
AIM2



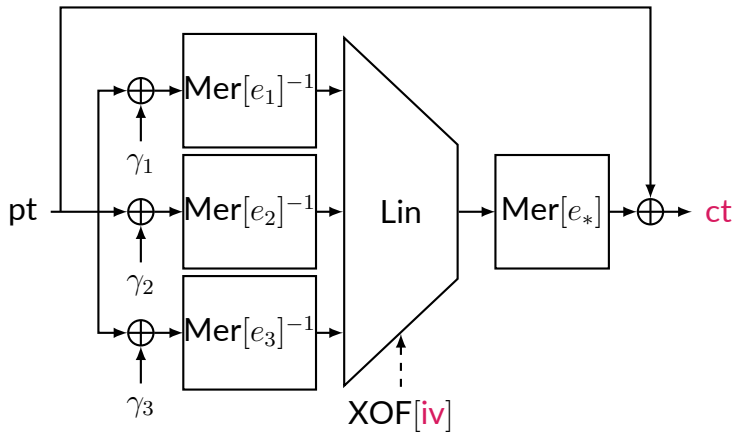
AIM2



AIM2



AIM2



Advantage & Limitation

Advantage & Limitation

- Advantages
 1. Short key size
 2. Security only relies on symmetric primitives
 3. Most efficient among schemes relying only on symmetric primitives
- Limitations
 1. Modest performance
 2. Relatively new primitive
 - * But multiple cryptanalysts have admitted that AIM2 is secure against state-of-the-art cryptanalytic techniques.

Security

- Security of AImEr is reduced to preimage resistance of AIM2
- Conventional symmetric key cryptanalysis cannot be applied to AIM2
 - Single input-output assumption
- We prevent algebraic attacks with the utmost effort
 - Sufficient security margin despite of radical assumption
 - We brute-forced all the derivable quadratic system of AIM2
 - All the attacks done for symmetric primitives with large S-boxes are considered

Security

Scheme	Type	#Var	Variables	(#Eq, Deg)	Complexity		
					k	d_{reg}	Time (bits)
AIM2-I	S_1	n	t_1	$(n, 60)$	-	-	-
	S_2	$2n$	t_1, t_2	$(3n, 2)$	62	15	207.9
	S_{quad}	$3n$	x, t_1, t_2	$(12n, 2)$	0	16	185.3
AIM2-III	S_1	n	x	$(2n, 114)$	-	-	-
	S_2	$2n$	t_1, t_2	$(3n, 2)$	100	20	301.9
	S_{quad}	$3n$	x, t_1, t_2	$(12n, 2)$	0	22	262.4
AIM2-V	S_1	n	x	$(2n, 172)$	-	-	-
	S_2	$2n$	t_2, z	$(n, 2) + (2n, 38)$	253	30	513.5
	S_3	$3n$	t_1, t_2, t_3	$(6n, 2)$	2	47	503.7
	S_{quad}	$4n$	x, t_1, t_2, t_3	$(18n, 2)$	9	32	411.4

Performance

AlMer enjoys balanced performance (all-rounder).

Scheme	Size (B)			Time (cycle)		
	sk	pk	sig	KeyGen	Sign	Verify
Dilithium	2,528	1,312	2,420			
Falcon	1,281	897	666			
SPHINCS+-f	64	32	17.1K			
HAETA	1,408	992	1,474			
NCC-Sign-tri	2,400	1,760	2,912			
MQ-Sign-LR	161K	328K	134			
AlMer-f	48	32	5,888			

SUPERCOP result (Zen 4), Category 1 or 2, median speed

Performance

AImer enjoys balanced performance (all-rounder).

Scheme	Size (B)			Time (cycle)		
	sk	pk	sig	KeyGen	Sign	Verify
Dilithium	2,528	1,312	2,420	62K	149K	70K
Falcon	1,281	897	666	15.6M*	331K*	63K*
SPHINCS+-f	64	32	17.1K	1.23M*	5.65M*	6.26M*
HAETAE	1,408	992	1,474	437K	1.13M	100K
NCC-Sign-tri	2,400	1,760	2,912	197K	295K	196K
MQ-Sign-LR	161K	328K	134	5.60M*	67K*	35K*
AImer-f	48	32	5,888	40K	889K	898K

* Not intend to be constant-time

SUPERCOP result (Zen 4), Category 1 or 2, median speed

History: AlMer v0.9 (Oct. 2022)

History: AlMer v0.9 (Oct. 2022)

Algorithm		Implementation	Security
Symmetric	Protocol		
AIM	BN++	C standalone	Birthday-bound

History: AlMer v1.0 (Jun. 2023)

Algorithm		Implementation	Security
Symmetric	Protocol		
AIM	BN++ Merge hash Domain sep.	C standalone AVX2	Birthday-bound

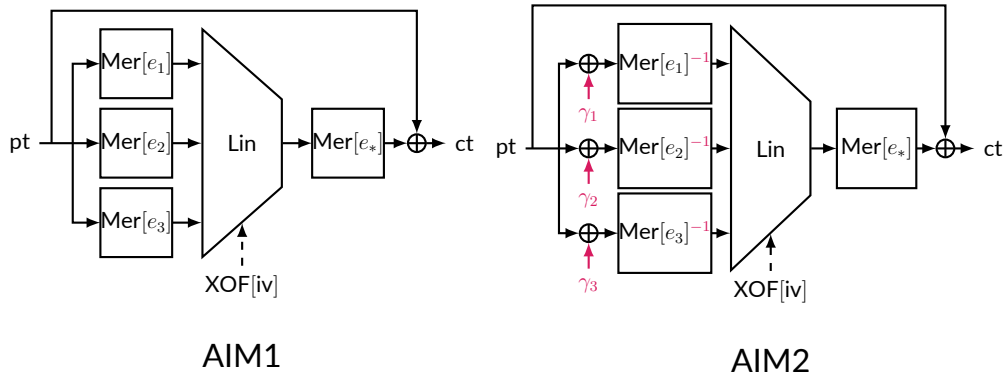
History: AlMer v1.0 (Sep. 2023)

Algorithm		Implementation	Security
Symmetric	Protocol		
AIM Attack AIM2	BN++ Merge hash Domain sep.	C standalone AVX2	Birthday-bound

History: AlMer v2.0 (Feb. 2024)

Algorithm		Implementation	Security
Symmetric	Protocol		
AIM Attack AIM2	BN++ Merge hash Domain sep. Half salt Prehashing	C standalone AVX2 ARM64	Birthday-bound Full-bound

History: AIMer v2.0 (Feb. 2024)

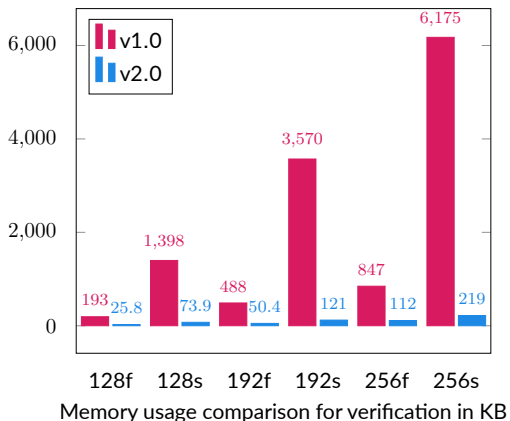
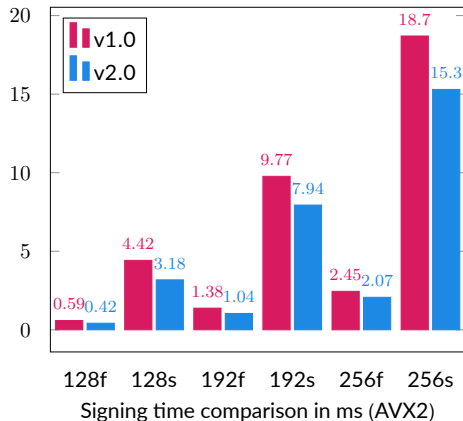


History: AIMer v2.0 (Feb. 2024)

Scheme	λ	n	ℓ	e_1	e_2	e_3	e_*
AIM-I	128	128	2	3	27	-	5
AIM-III	192	192	2	5	29	-	7
AIM-V	256	256	3	3	53	7	5

Scheme	λ	n	ℓ	e_1	e_2	e_3	e_*
AIM2-I	128	128	2	49	91	-	3
AIM2-III	192	192	2	17	47	-	5
AIM2-V	256	256	3	11	141	7	3

History: AImer v2.0 (Feb. 2024)



History: AlMer v2.1 (Aug. 2024)

Algorithm		Implementation	Security
Symmetric	Protocol		
AIM Attack AIM2	BN++ Merge hash Domain sep. Half salt Prehashing	C standalone AVX2 ARM64 + SHA3 ARM Cortex-M4 PQClean Constrained mem. TIMECOP	Birthday-bound Full-bound

Lesson Learned from Standardization

- Conservative security first
 - Old security assumption preferred
 - Simple security proof preferred

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- Conservative security first
 - Old security assumption preferred
 - Simple security proof preferred
- So many people are needed than expected
 - Algorithm makers, cryptanalysts, (quantum) provable security experts, side-channel analysts, implementation experts on many different platforms, languages, and protocols, ...

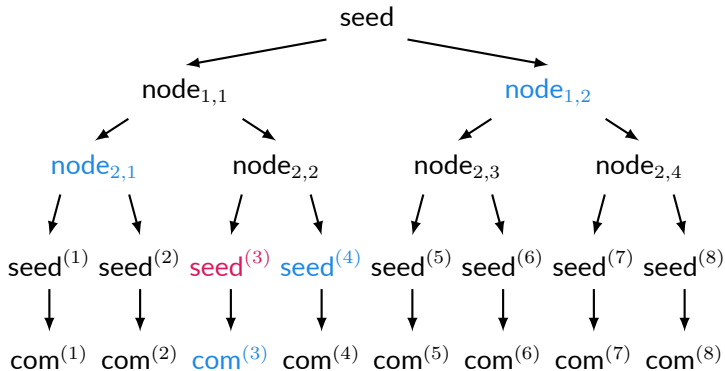
Lesson Learned from Standardization

- Conservative security first
 - Old security assumption preferred
 - Simple security proof preferred
- So many people are needed than expected
 - Algorithm makers, cryptanalysts, (quantum) provable security experts, side-channel analysts, implementation experts on many different platforms, languages, and protocols, ...
- Proper marketing required
 - If security, efficiency, and simplicity of my scheme is the best, then anything does not matter
 - Otherwise, where can my scheme fit into?
 - Protocol (TLS, IPSec, SSH, DNSSEC), security assumption (lattice, isogeny, MQ, code), constrained resources, ...

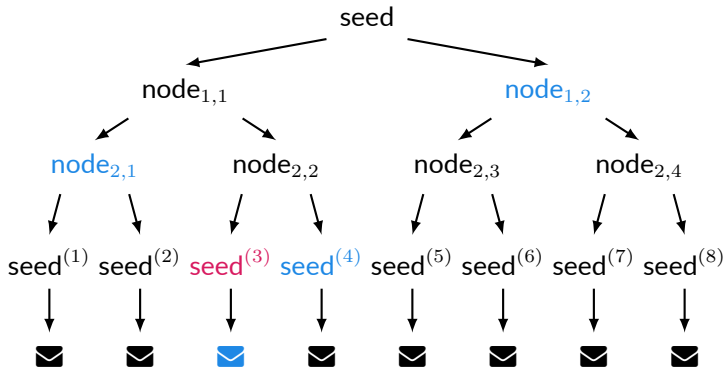
Relaxed Vector Commitment for Shorter Signatures (Eurocrypt 2025)

Vector Commitment

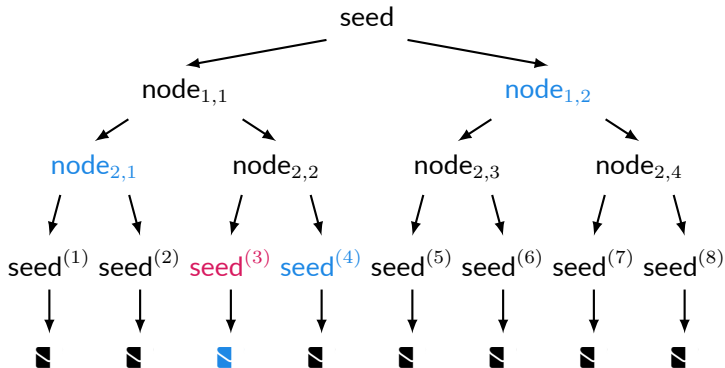
Vector Commitment



Vector Commitment



Vector Semi-Commitment

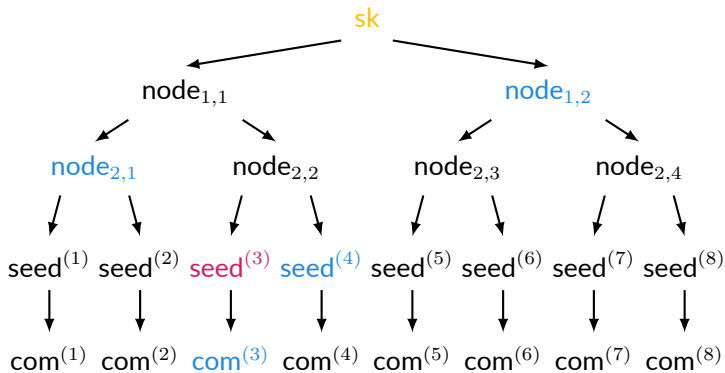


Application of VSC (rMPCitH)

1. Halved commitment size
2. GGM tree \rightarrow correlated GGM tree

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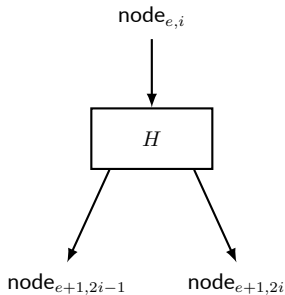


Application of VSC (rMPCitH)

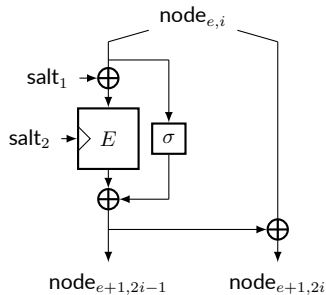
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3. Random oracle model \rightarrow ideal cipher model

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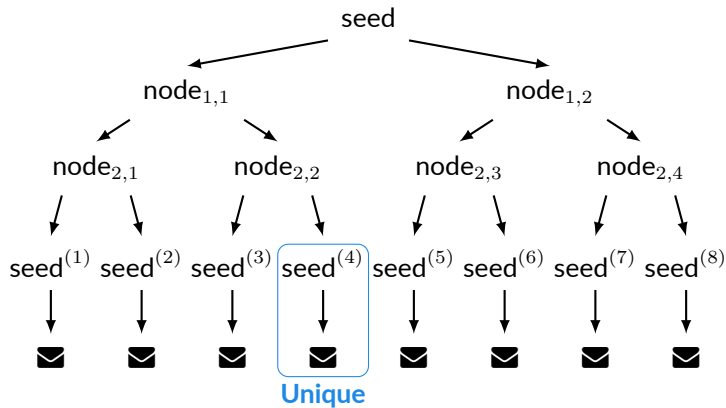


Double-length PRG

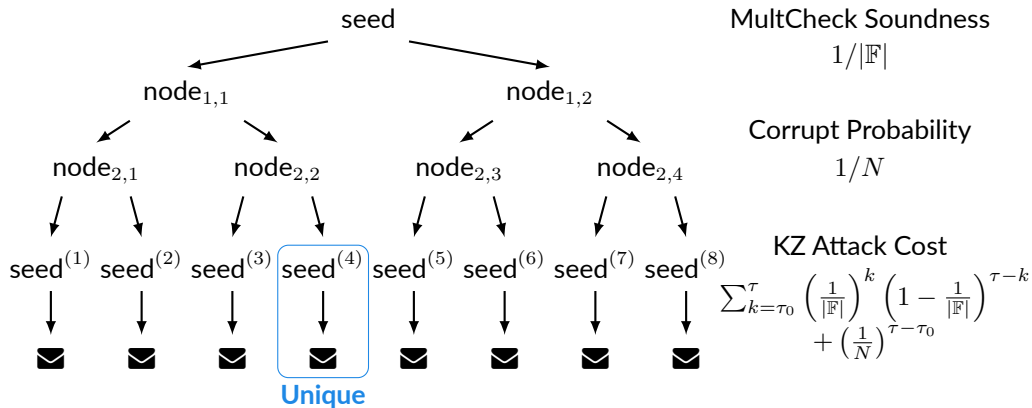


IC-VSC

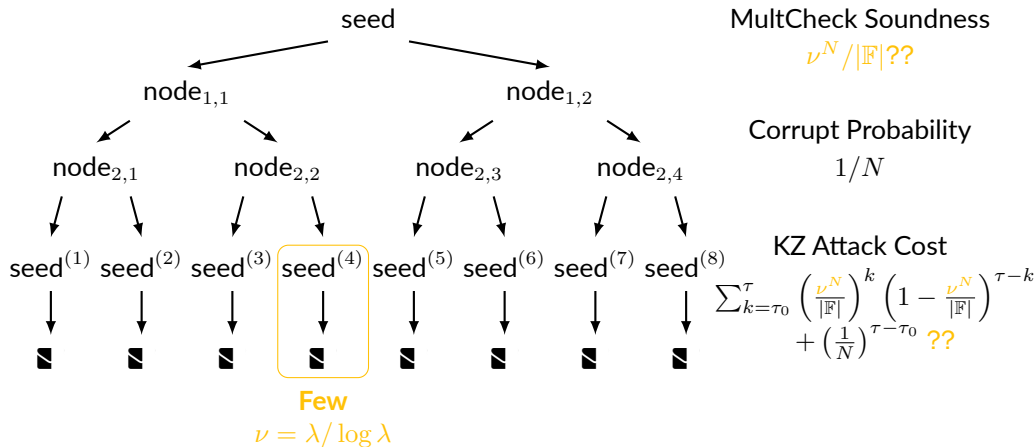
Difference of Security Proof



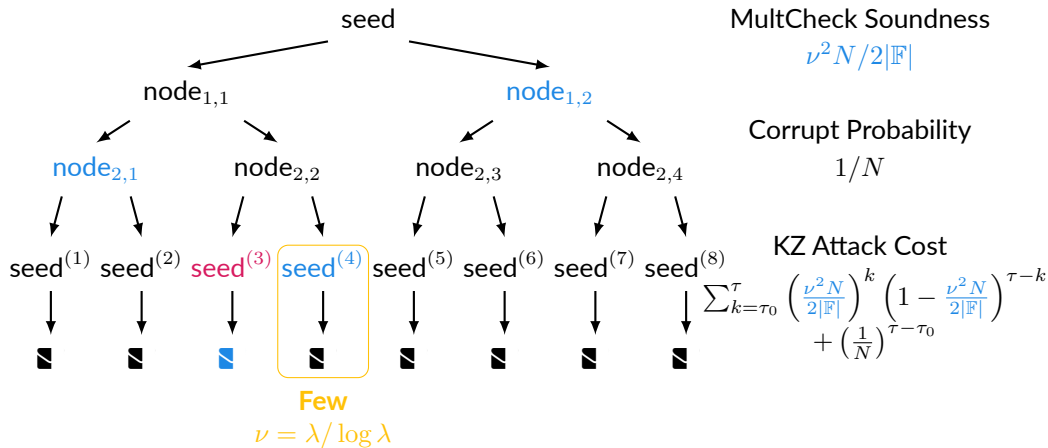
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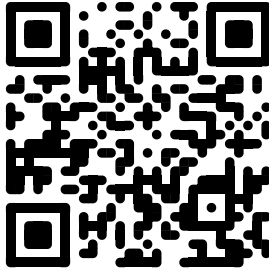


Performance

Scheme	$ pk $ (B)	$ sig $ (B)	Sign (Kc)	Verify (Kc)
Dilithium2	1,312	2,420	162	57
SPHINCS ⁺ -128f*	32	17,088	38,216	2,158
SPHINCS ⁺ -128s*	32	7,856	748,053	799
SDitH-Hypercube-gf256	132	8,496	20,820	10,935
FAEST-128f	32	6,336	2,387	2,344
FAEST-128s	32	5,006	20,926	20,936
AlMer-v2.0-128f	32	5,888	788	752
AlMer-v2.0-128s	32	4,160	5,926	5,812
rAlMer-128f	32	4,848	421	395
rAlMer-128s	32	3,632	2,826	2,730

*: -SHAKE256-simple

Thank you!
Check out our website!



Attribution

- Illustrations at the very beginning was created using fontawesome latex package (<https://github.com/xdanaux/fontawesome-latex>).
- SUPERCOP result can be found in <https://bench.cr.yp.to/results-sign/amd64-hertz.html>.