

Software Tools

User Manual



Revision: 1.5
June 29, 2011

Important Information

Safety and Handling

The circuit boards are delicate and require care in handling and installation. Do not remove circuit boards from their protective plastic coverings or from the shipping box until you are ready to install the boards.

If a board is removed from the chassis for any reason, be sure to store it in its original shipping box. Do not store boards on top of workbenches or other areas where they might be susceptible to damage or exposure to strong electromagnetic or electrostatic fields. Store circuit boards in protective anti-electrostatic wrapping and away from electromagnetic fields.

Disclaimer

In no event shall Test Evolution be liable for any consequential damages whatsoever (including unlimited damages for loss of business profits, business interruption, loss of business information, or any other losses) arising out of the use of or inability to use this product, even if Test Evolution has been advised of the possibility for such damages.

Copyright

Copyright ©2009-2011 by Test Evolution Corp. All rights reserved.

Under the copyright laws, no part of this document may be reproduced or transmitted in any form, electronic or mechanical, including photocopying, recording, storing in an information retrieval system, or translating, in whole or in part, without the prior written consent of Test Evolution Inc.

Test Evolution respects the intellectual property of others and we ask our users to do the same. Test Evolution software is protected by copyright and other intellectual property laws.

Trademarks

Product and company names listed are trademarks or trade names of their respective companies.

PICMG, the AdvancedTCA name and logos, the ATCA name and logo, and the PICMG logo are registered trademarks of the PCI Industrial Computers Manufacturers Group.

Table of Contents

TABLE OF CONTENTS	3
1 ABOUT THIS MANUAL	4
2 INSTALLATION	5
3 INSTRUMENT DEBUG TOOL	6
4 PATTERN DEBUG TOOL	10
5 PIN MARGIN TOOL	23
6 TRACE TOOL	27
7 DATALOG VIEWER.....	28
8 DIAGNOSTIC TOOL	29

1 About This Manual

Manual Scope and Organization

This manual is intended to describe the capabilities and operation of the Test Evolution software tools under **version 1.5.xxxx** of the Tev AXI software.

Conventions

<code>monospace</code>	examples of syntax and programming examples
bold	denotes items that you must enter or select. Also denotes default conditions of API parameters.
<i>italic</i>	specialized terms, ...

Related Documentation

Test Evolution General System Manuals
Test Evolution DPS12 User Manual
Test Evolution DD48 User Manual

2 Installation

The Test Evolution software tools are installed as part of the normal installation of the TEV software.

Software tools can be accessed from the standard Window menus at:
Start/AllPrograms/TEV/AXI

By default, the software tool executables are located at:
... \Program Files\TEV\AXI\bin

and are Application file types (.exe).

3 Instrument Debug Tool

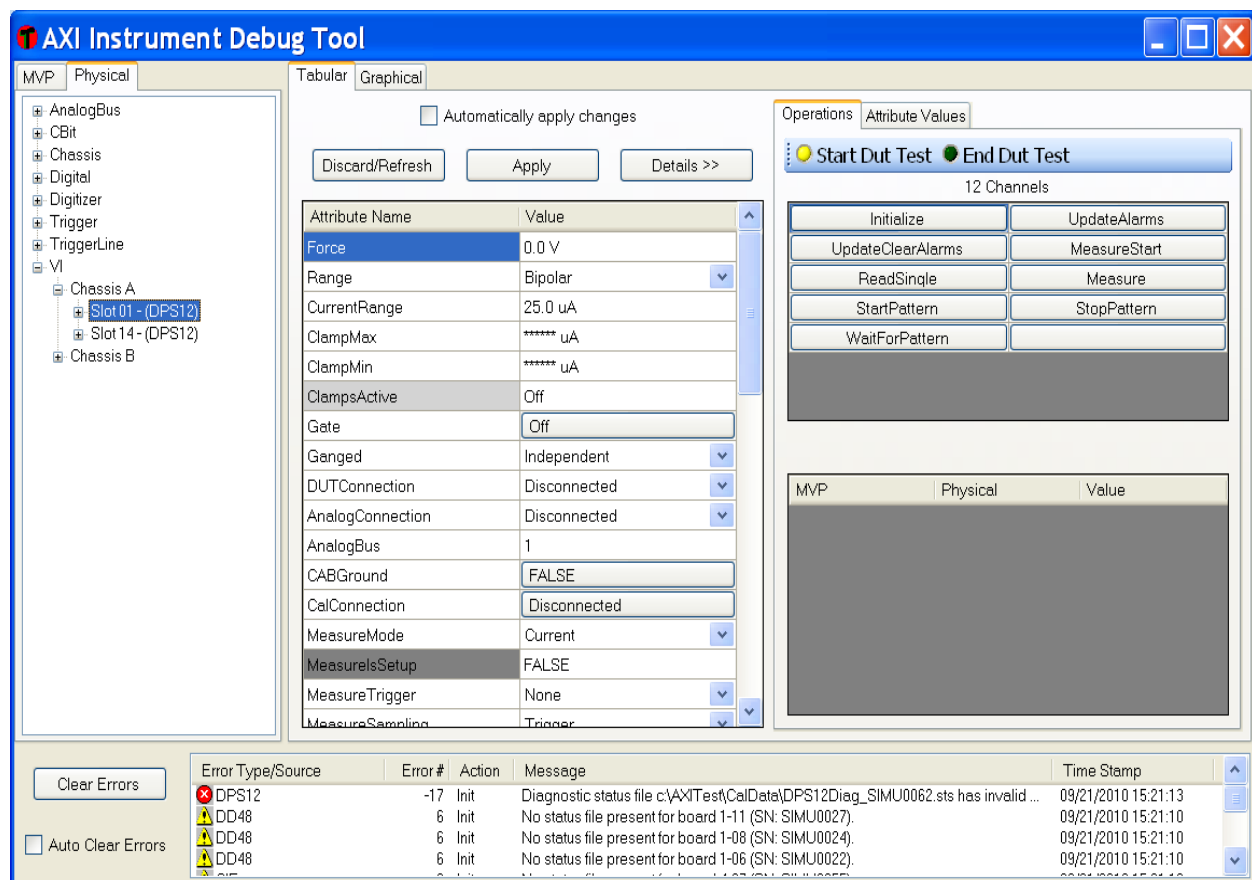
The Instrument Debug Tool displays state of the programmable parameters for all instruments.

It also provides a means to interactively set/modify instrument parameters and actions.

Philosophy of shared memory space used by both drivers and tools.

Each TEV Instrument driver is designed to publish all the attributes as well as the set of operations.

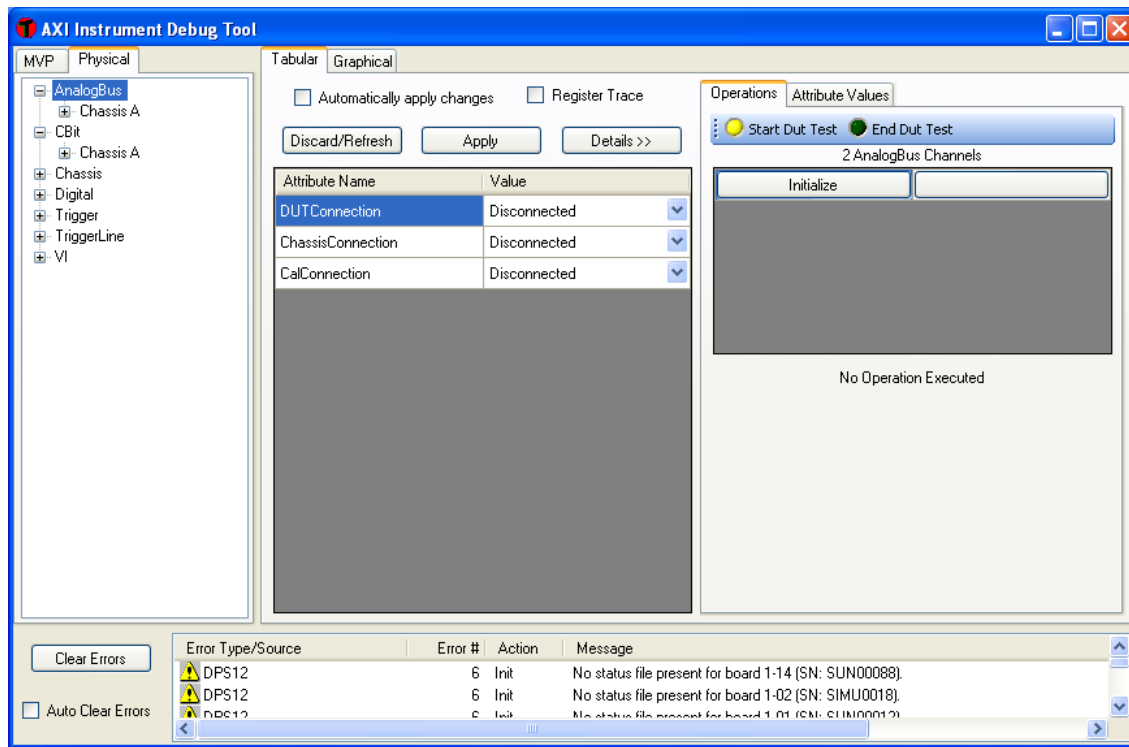
This information is automatically picked up by the IDT. This includes the valid range of numerical parameters, etc.



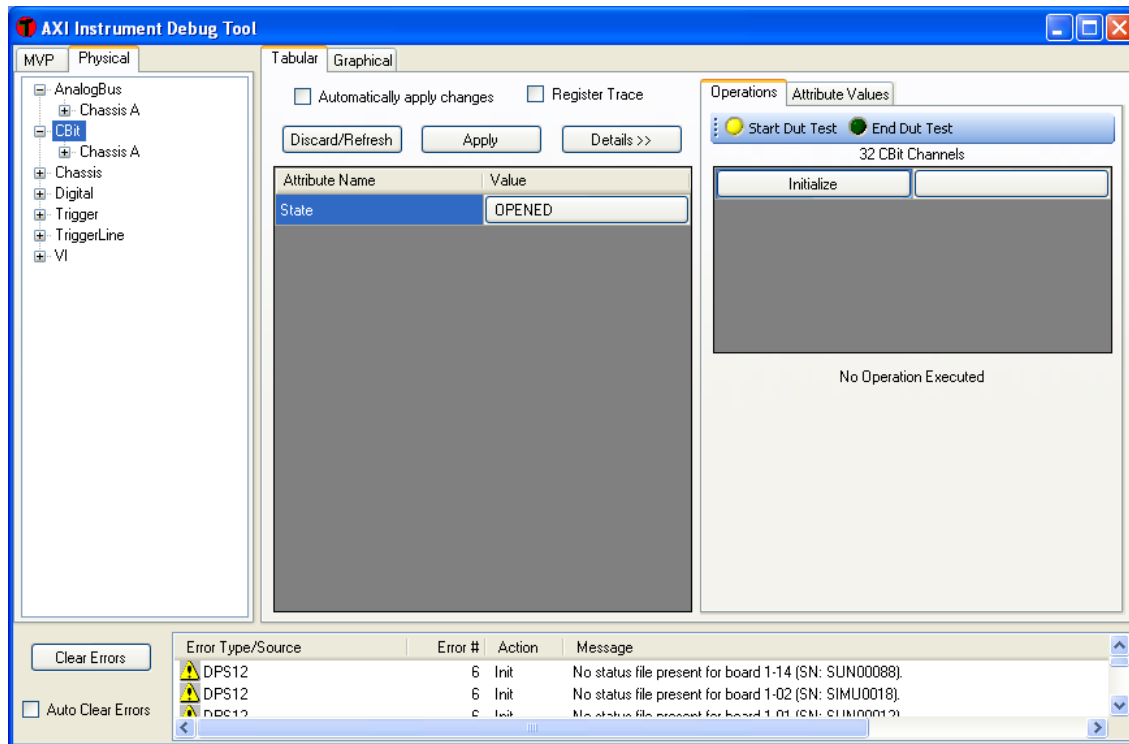
Basic organization – 4 panes

- Left** Instrument inventory
- Center** Instrument parameters
- Right** Instrument Operations /??? Attributes
- Bottom** Error messages

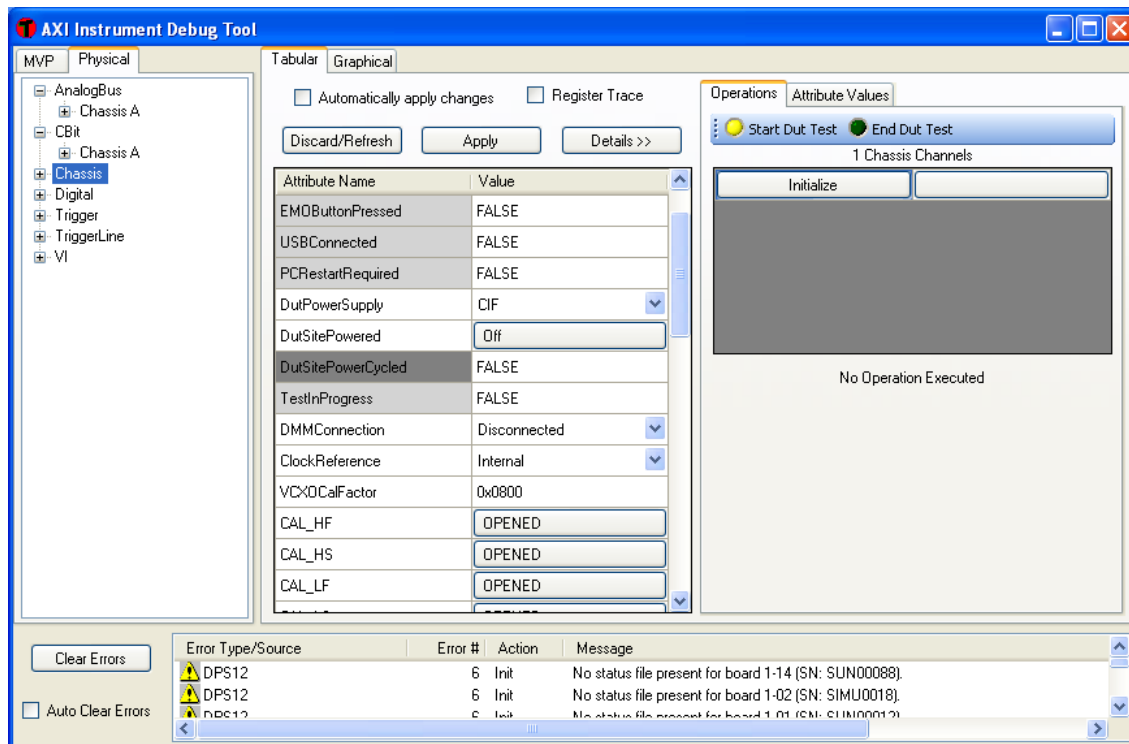
Analog Bus



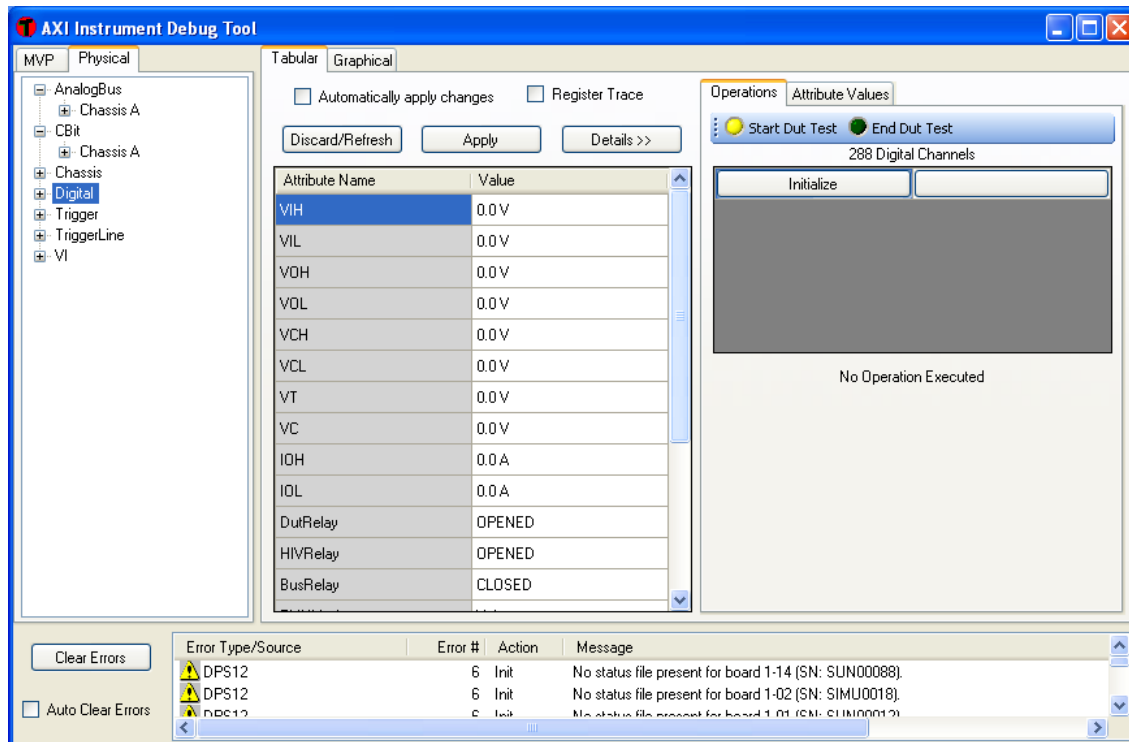
CBits



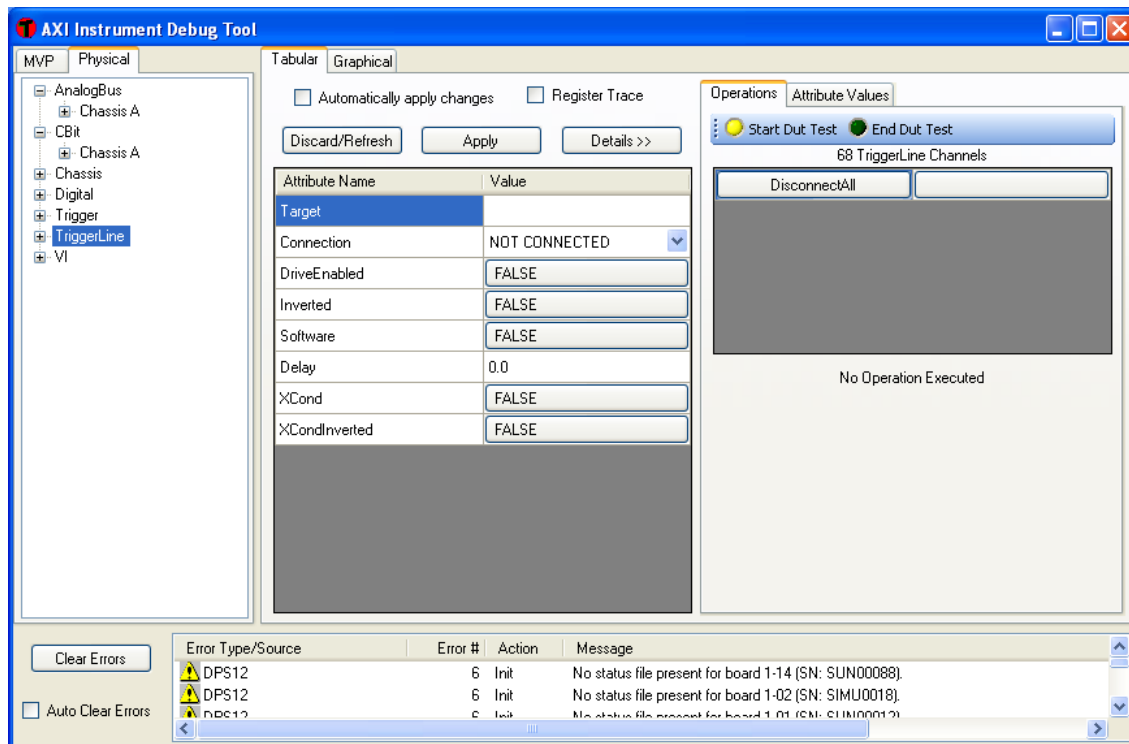
Chassis



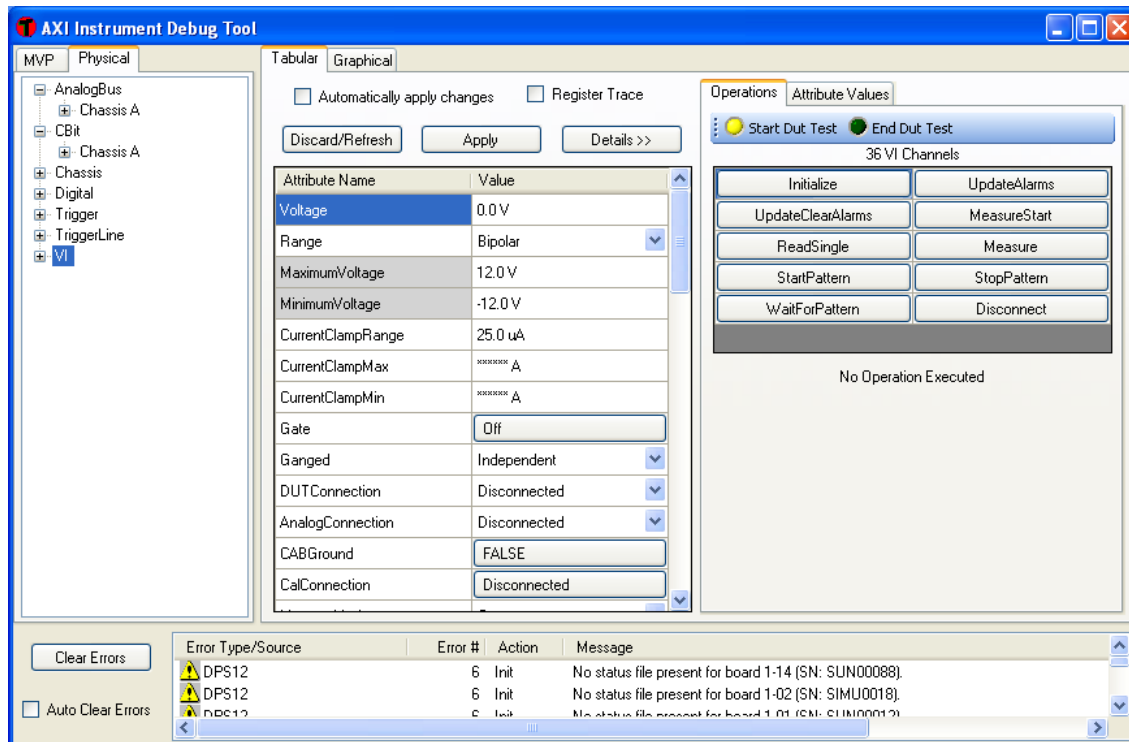
Digital



Trigger Line



VI



4 Pattern Debug Tool

The Pattern Debug Tool provides interactive operation of the Dynamic Digital Subsystem (DD48)
Works with hardware data directly.

Changes made in the tool do not affect pattern files or the API calls in the test program...

Capabilities include:

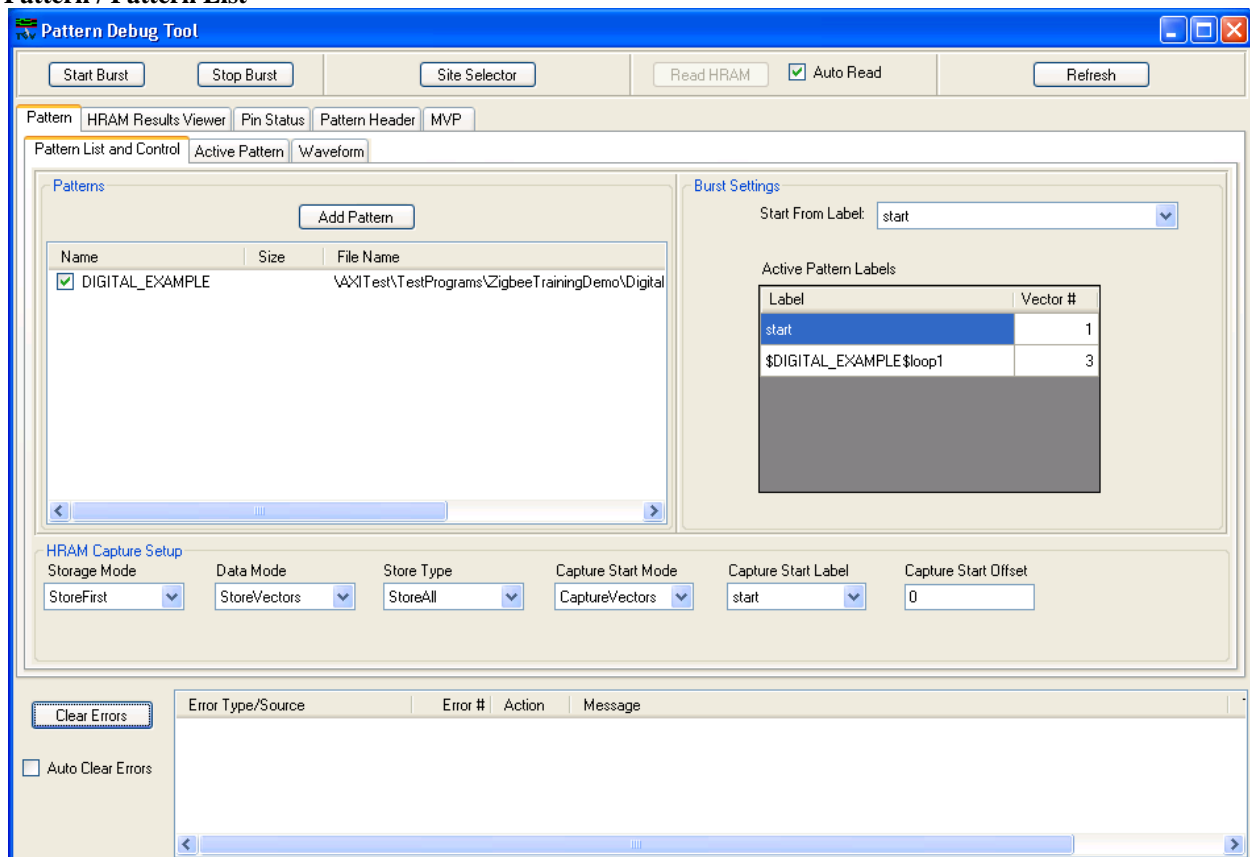
- Load MVP data and patterns.
- Manage HRAM capture.
- Modify vector and pin pattern data.
- Burst pattern and view results.
- Change pin setup data (levels, edges, relays, etc.).
- Works with running test program

Top level navigation is provided by a list of functional areas

- Pattern
- HRAM
- Pin Status
- Pattern Header
- MVP

Selecting one of these options displays a secondary list of capabilities specific to that option

Pattern / Pattern List



Pattern / Active Pattern

Pattern Debug Tool

Start Burst Stop Burst Site Selector Read HRAM ☒ Auto Read Refresh

Pattern HRAM Results Viewer Pin Status Pattern Header MVP

Pattern List and Control Active Pattern Waveform

Vector Display Range Start 1 start Next Fail Prev Fail View Mode ☒ Show Pins ☐ Show Groups Group Radix ☒ Hex ☐ Octal ☐ Binary

Vector #	Label	OpCode	G P I O 1	G P I O 2	G P I O 3	G P I O 4	G P I O 5	G P I O 6	G P I O 7	G P I O 8	C E	R S T	R X T X E N	A T T N	I R Q	A T T N	TSet
0000001	start	lcnk 100	1	L	1	1	1	0	0	0	11	HH	11	11	1111	HHHH	TsetDrCmp
0000002		nop	0	L	0	0	0	1	0	0	00	LL	00	00	0000	LLLL	TsetDrCmp
0000003	loop1	nop	1	H	1	1	0	0	1	0	11	HH	11	11	1111	HHHH	TsetDrCmp
0000004		rep 6	0	L	0	0	0	0	0	1	00	LL	00	00	0000	LLLL	TsetDrCmp
0000005		nop	1	H	1	1	0	0	0	0	11	HH	11	11	1111	HHHH	TsetDrCmp
0000006		nop	0	L	0	1	0	0	0	0	00	LL	00	00	0000	LLLL	TsetDrCmp
0000007		nop	0	H	1	1	0	0	0	0	11	HH	01	01	1111	HHHH	TsetDrCmp
0000008		nop	0	L	0	0	0	0	0	0	00	LL	00	00	0000	LLLL	TsetDrCmp
0000009		endl loop1	1	H	1	1	0	0	0	0	11	HH	11	11	1111	HHHH	TsetDrCmp
0000010		halt	0	L	1	0	0	0	0	0	00	LL	00	00	0000	LLLL	TsetDrCmp
Pin Fail Totals		N/A	0	100	0	0	0	0	0	0	0	200	0	0	0	400	

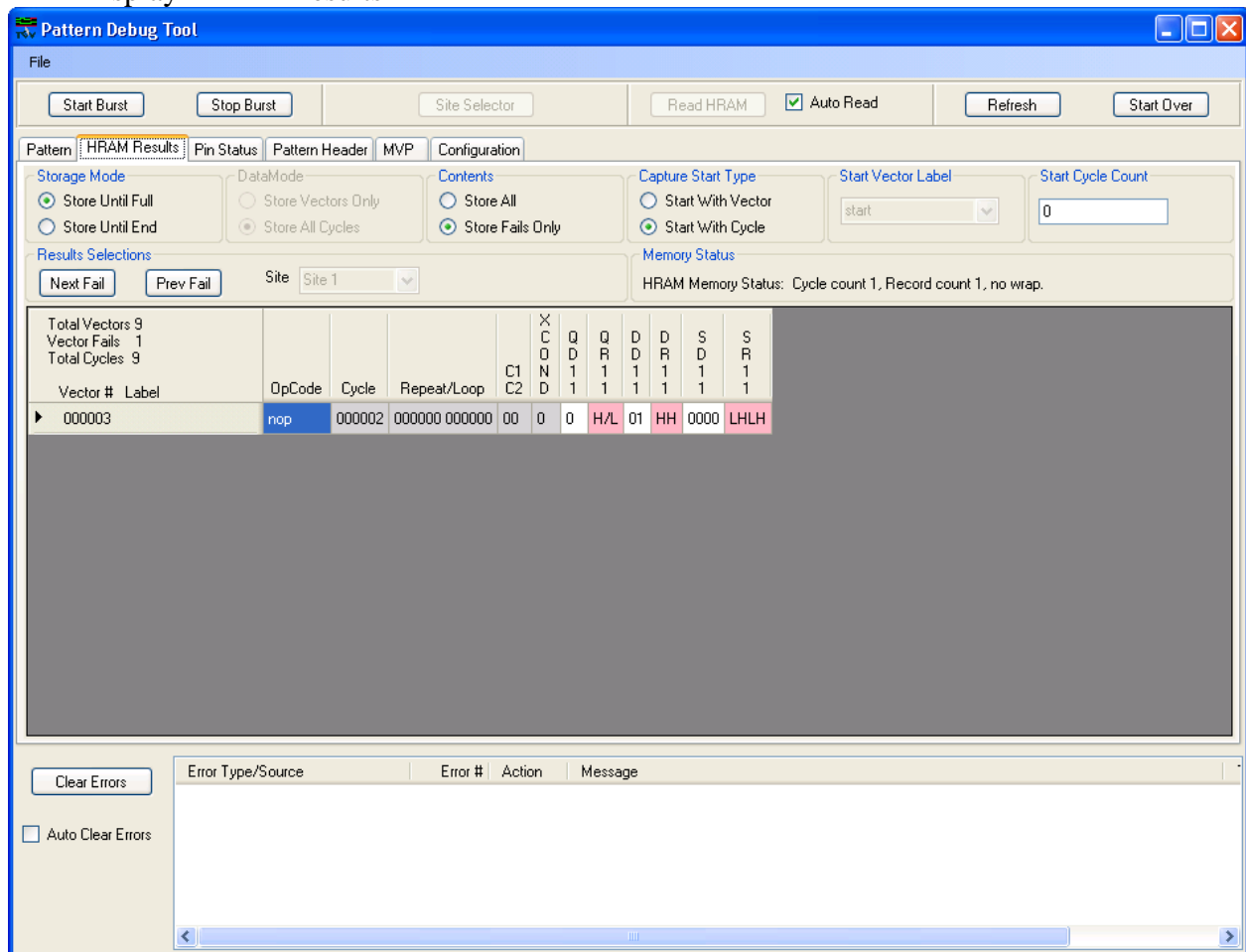
Clear Errors

☐ Auto Clear Errors

Error Type/Source	Error #	Action	Message
-------------------	---------	--------	---------

History Ram (HRA) –

Setup HRA capture conditions.
Display HRA Results



Pin Status / Levels

Pattern Debug Tool

Start Burst Stop Burst Site Selector Read HRAM ☒ Auto Read Refresh

Pattern HRAM Results Viewer **Pin Status** Pattern Header MVP

Levels Pin Mode / Clock Format Edges Relays

Site: Site 1

Pin Name	C-S:c@s	VIH	VIL	VOH	VOL	VCH	VCL	VTT	VC	IOH	IOL
CE_B	A-12:06@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
RST_B	A-12:14@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
RXTXEN	A-12:15@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
ATTN_B	A-12:16@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
ATTN_B1	A-12:21@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
IRQ	A-12:17@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
GPIO1	A-12:13@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
GPIO2	A-12:12@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
GPIO3	A-12:11@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
GPIO4	A-12:10@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
GPIO5	A-12:09@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
GPIO6	A-12:08@01	3	0	2.5	66666	6.5	-2.5	-2.5	2	0.001	0.001
GPIO7	A-12:07@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001
GPIO8	A-12:20@01	3	0	2.5	1.5	6.5	-2.5	-2.5	2	0.001	0.001

Clear Errors

☐ Auto Clear Errors

Error Type/Source	Error #	Action	Message
-------------------	---------	--------	---------

Pin Status / Pin Mode & Clock Format

Pattern Debug Tool

Start Burst Stop Burst Site Selector Read HRAM ☒ Auto Read Refresh

Pattern HRAM Results Viewer **Pin Status** Pattern Header MVP

Levels **Pin Mode / Clock Format** Edges Relays

Site: Site 1

Pin Name	C-S:c@s	Static Drive	Driver Mode	Data Mode	Clock	Clock Edge	Fail Mask
CE_B	A-12:06@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
RST_B	A-12:14@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
RXTXEN	A-12:15@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
ATTN_B	A-12:16@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
ATTN_B1	A-12:21@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
IRQ	A-12:17@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO1	A-12:13@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO2	A-12:12@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO3	A-12:11@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO4	A-12:10@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO5	A-12:09@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO6	A-12:08@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO7	A-12:07@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable
GPIO8	A-12:20@01	Z	▼ DrvSense	▼ Dynamic	▼ T0	▼ Enable	Disable

Clear Errors

☐ Auto Clear Errors

Error Type/Source	Error #	Action	Message
-------------------	---------	--------	---------

Pin Status / Edges

Pattern Debug Tool

Start Burst Stop Burst Site Selector Read HRAM ☒ Auto Read Refresh

Pattern HRAM Results Viewer **Pin Status** Pattern Header MVP

Levels Pin Mode / Clock Format **Edges** Relays

Site: Site 1

Pin Name	C-S:c@s	Marker Mode	TSet Name	PSet Name	T0	C0	ESet Name	Drv0	D0	D1	D2	D3	Cmp0	R0	R1
CE_B	A-12:06@01	Dual													
RST_B	A-12:14@01	Dual	TsetDrCmp	PsetDrCmp	4.0 uHz	1	EsetEasyDrCmpDual	NRZ	0.0 s	800.0 ns	0.0 s	0.0 s	STROBE	0.0 s	1.2 us
RXTXEN	A-12:15@01	Dual	TsetDrCmpExtra	PsetDrCmp	4.0 uHz	1	DEF_EDGESET	X	0.0 s	0.0 s	0.0 s	0.0 s	X	0.0 s	0.0 s
ATTN_B	A-12:16@01	Single Compare													
ATTN_B1	A-12:21@01	Dual													
IRQ	A-12:17@01	Single Drive													
GPIO1	A-12:13@01	Quad													
GPIO2	A-12:12@01	Quad													
GPIO3	A-12:11@01	Quad													
GPIO4	A-12:10@01	Quad													
GPIO5	A-12:09@01	Quad													
GPIO6	A-12:08@01	Quad													
GPIO7	A-12:07@01	Quad													
GPIO8	A-12:20@01	Quad													

Clear Errors

☐ Auto Clear Errors

Error Type/Source	Error #	Action	Message
-------------------	---------	--------	---------

Pin Status / Relays

Pattern Debug Tool

Start Burst Stop Burst Site Selector Read HRAM ☒ Auto Read Refresh

Pattern HRAM Results Viewer **Pin Status** Pattern Header MVP

Levels Pin Mode / Clock Format Edges **Relays**

Site: Site 1

Pin Name	C-S:c@s	DUT	Bus	PPmu	Hiv	Cal Bus FH	Cal Bus FL	Cal Bus
CE_B	A-12:06@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
RST_B	A-12:14@01	CLOSED	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED
RXTXEN	A-12:15@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
ATTN_B	A-12:16@01	CLOSED	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED
ATTN_B1	A-12:21@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
IRQ	A-12:17@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO1	A-12:13@01	CLOSED	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO2	A-12:12@01	CLOSED	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO3	A-12:11@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO4	A-12:10@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO5	A-12:09@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO6	A-12:08@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO7	A-12:07@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED
GPIO8	A-12:20@01	CLOSED	OPENED	OPENED	OPENED	OPENED	OPENED	OPENED

Clear Errors

☐ Auto Clear Errors

Error Type/Source	Error #	Action	Message
-------------------	---------	--------	---------

Pattern Header / Level Sets

Pattern Debug Tool

File

Start Burst Stop Burst Site Selector Read HARAM ☒ Auto Read Refresh

Pattern HARAM Results Viewer Pin Status **Pattern Header** MVP

Level Set Timing / Period Set Edge Set PMU Set

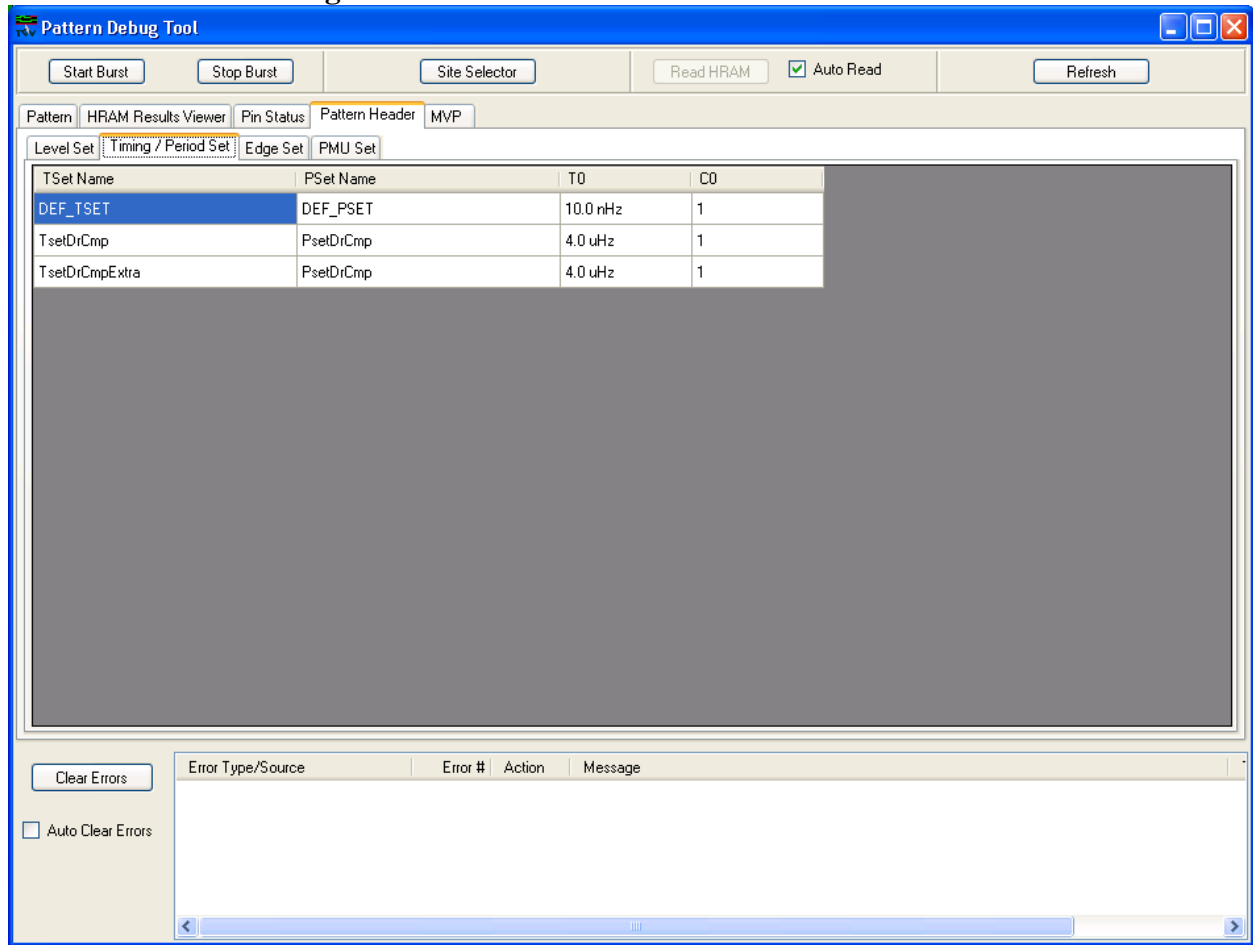
Level Set Name	VIH	VIL	VOH	VOL	VCH	VCL	VTT	VC	IOH	IOL
DEF_LEVELSET	0.0 V	0.0 V	0.0 V	0.0 V	0.0 V	0.0 V	0.0 V	0.0 V	0.0 A	0.0 A
AllPins	3.0 V	0.0 V	2.5 V	1.5 V	6.5 V	-2.5 V	2.0 V	2.0 V	1.0 mA	1.0 mA
GPIO1	3.0 V	0.0 V	2.5 V	1.5 V	6.5 V	-2.5 V	2.0 V	2.0 V	1.0 mA	1.0 mA

Clear Errors

☐ Auto Clear Errors

Error Type/Source	Error #	Action	Message	Time Stamp
DPS12	-17	Init	Diagnostic status file c:\AXITest\CalData\DPS12Diag_SIMU0030.sts has invalid ...	01/11/2011 13:49:00
DPS12	-17	Init	Diagnostic status file c:\AXITest\CalData\DPS12Diag_SIMU0017.sts has invalid ...	01/11/2011 13:48:59
DD48	6	Init	No status file present for board 1-06 (SN: SIMU0022).	01/11/2011 13:48:58
DD48	6	Init	No status file present for board 1-03 (SN: SIMU0019).	01/11/2011 13:48:58
CIF	-17	Init	Diagnostic status file c:\AXITest\CalData\CIFDiag_SIMU0023.sts had invalid or c...	01/11/2011 13:48:58
Inventory	1	Init	This tool is operating with out a running test executive.	01/11/2011 13:48:58
Inventory	-2	Init	IPMI communications to chassis 1 timed out with IP address 10.10.10.2.	01/11/2011 13:48:58

Pattern Header / Timing & Period Sets

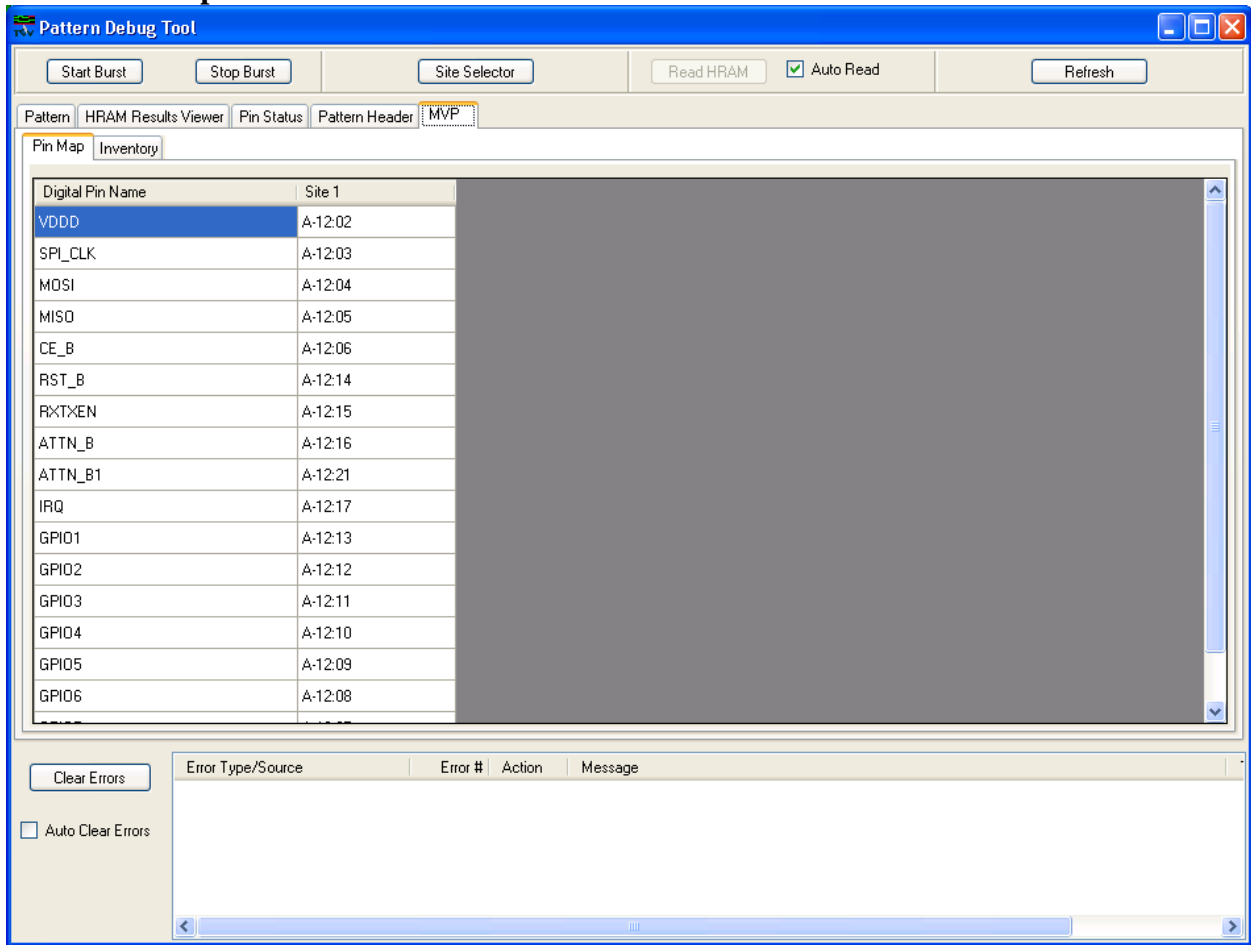


Pattern Header / Edge Sets

The screenshot shows the 'Pattern Debug Tool' window. The 'Pattern Header' tab is selected, and the 'Edge Set' sub-tab is active. The main area displays a table of edge sets with columns for ESet Name, Div0, D0, D1, D2, D3, Cmp0, R0, R1, R2, and R3. The first row, 'DEF_EDGESET', is highlighted. Below the table is a large gray rectangular area. At the bottom, there is an 'Error' section with a 'Clear Errors' button and a checkbox for 'Auto Clear Errors'. The error table has columns for Error Type/Source, Error #, Action, and Message.

ESet Name	Div0	D0	D1	D2	D3	Cmp0	R0	R1	R2	R3
DEF_EDGESET	OFF	0.0 s	0.0 s	0.0 s	0.0 s	OFF	0.0 s	0.0 s	0.0 s	0.0 s
EsetEasyDrCmp	OFF	0.0 s	0.0 s	0.0 s	0.0 s	OFF	0.0 s	0.0 s	0.0 s	0.0 s
EsetEasyDrCmpDual	OFF	0.0 s	0.0 s	0.0 s	0.0 s	OFF	0.0 s	0.0 s	0.0 s	0.0 s
EsetEasyDrSingle	OFF	0.0 s	0.0 s	0.0 s	0.0 s	OFF	0.0 s	0.0 s	0.0 s	0.0 s
EsetEasyCmpSingle	OFF	0.0 s	0.0 s	0.0 s	0.0 s	OFF	0.0 s	0.0 s	0.0 s	0.0 s

MVP / Pin Map



MVP / Inventory

Pattern Debug Tool

File

Start Burst Stop Burst Site Selector Read HRAM ☒ Auto Read Refresh Start Over

Pattern HRAM Results Pin Status Pattern Header MVP Configuration

Pin Map Inventory

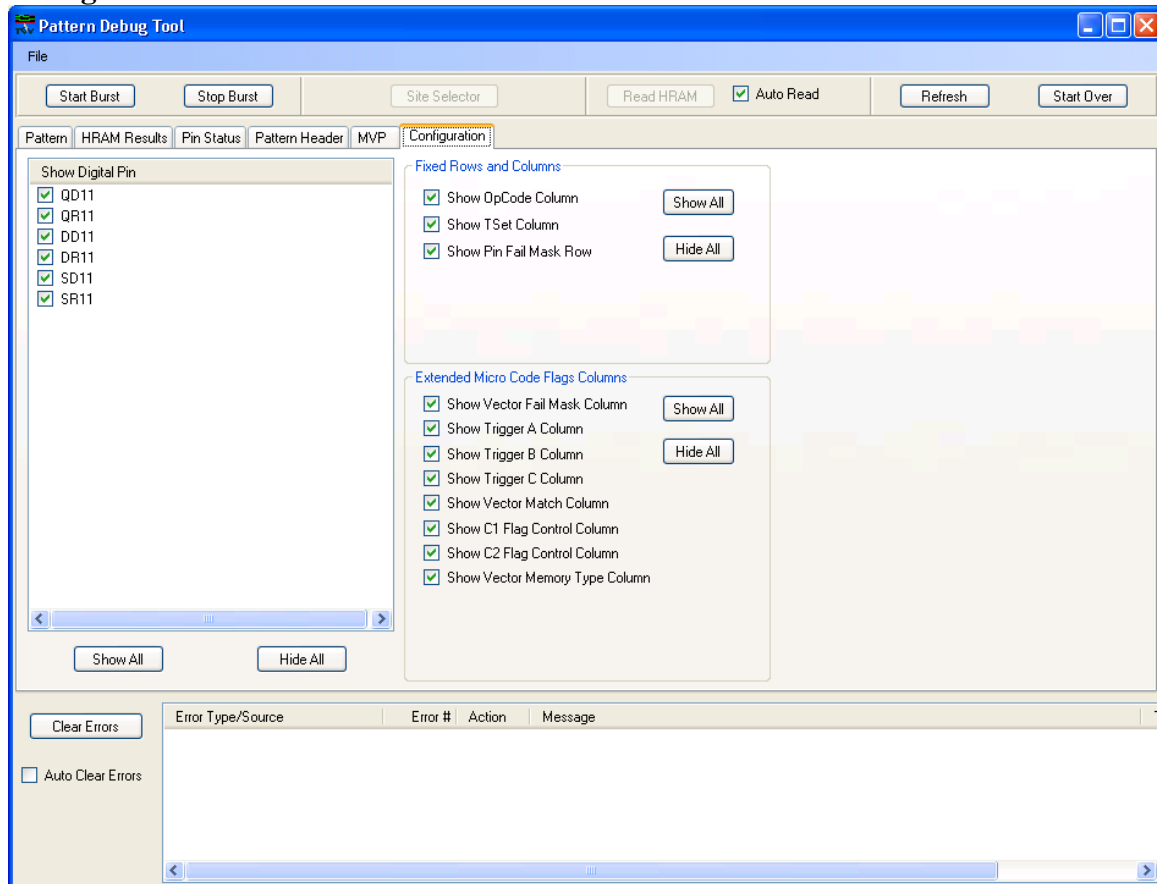
Slot Number	Chassis A (AXI)	Chassis 3 (PXI)
Chassis Model Name	AXI_14_7	PXI-1042
Slot 1	DPS12	
Slot 2		
Slot 3		
Slot 4		
Slot 5		
Slot 6		
Slot 7	CIF	
Slot 8		
Slot 9		
Slot 10		
Slot 11		
Slot 12		
Slot 13		
Slot 14	DD48	
Slot 15		

Clear Errors

☐ Auto Clear Errors

Error Type/Source	Error #	Action	Message
-------------------	---------	--------	---------

Configuration



5 Pin Margin Tool

The Pin Margin Tool provides a convenient way to set up and save margin tests that provide insight into the behavior/timing of device signals. Typically these test are based on running a pattern multiple times while varying one or more timing/voltage parameters of the device.

Basic organization – 3 panes

Left Marginable resources
Right Parameter Setup / Results Display
Bottom Error messages

Pin Margin Setup

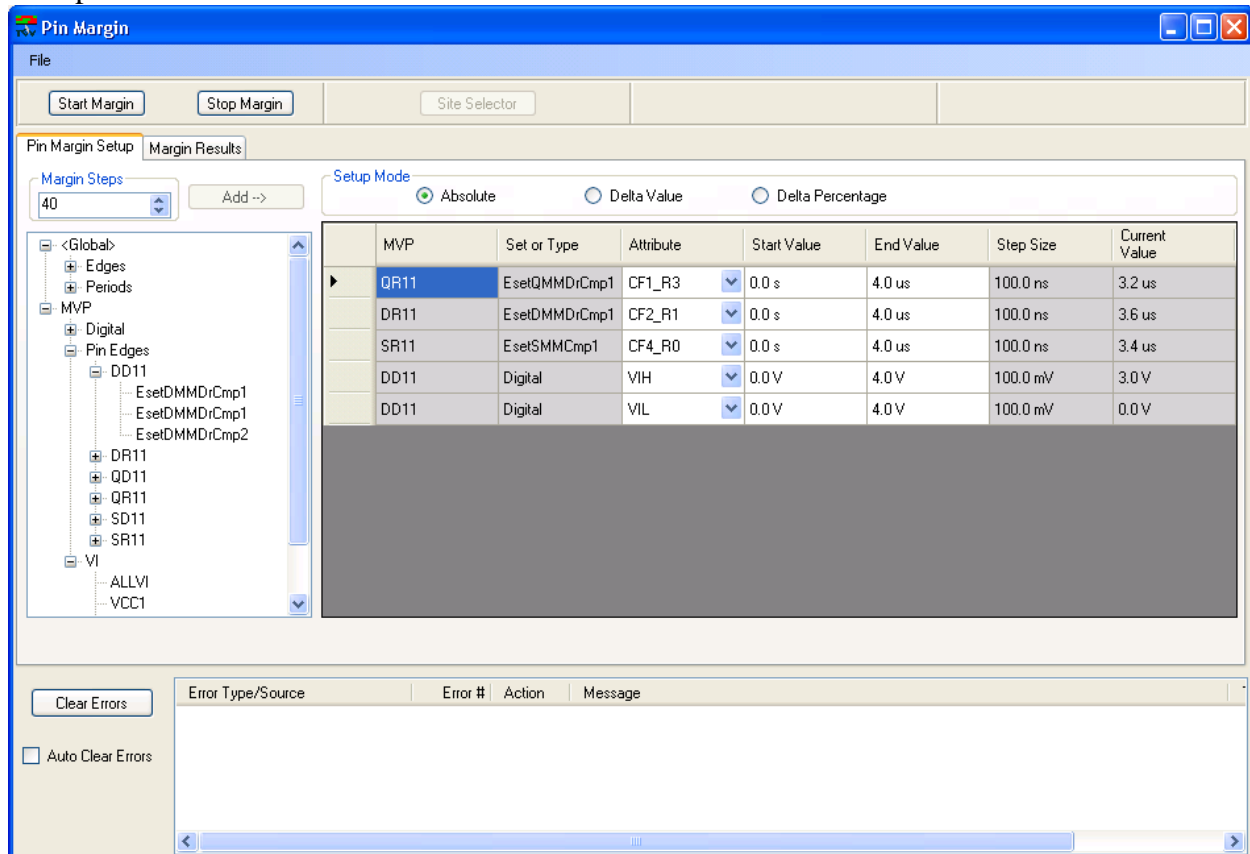
Margin parameters are selected and added to the parameter pane.

MVP attribute (Digital pin level, VI level) | Pin timing edge | Global timing edge | TO
Sweep Parameter values are set

Absolute (Start, Stop) | Delta (Plus, Minus) | Percentage (Plus, Minus)

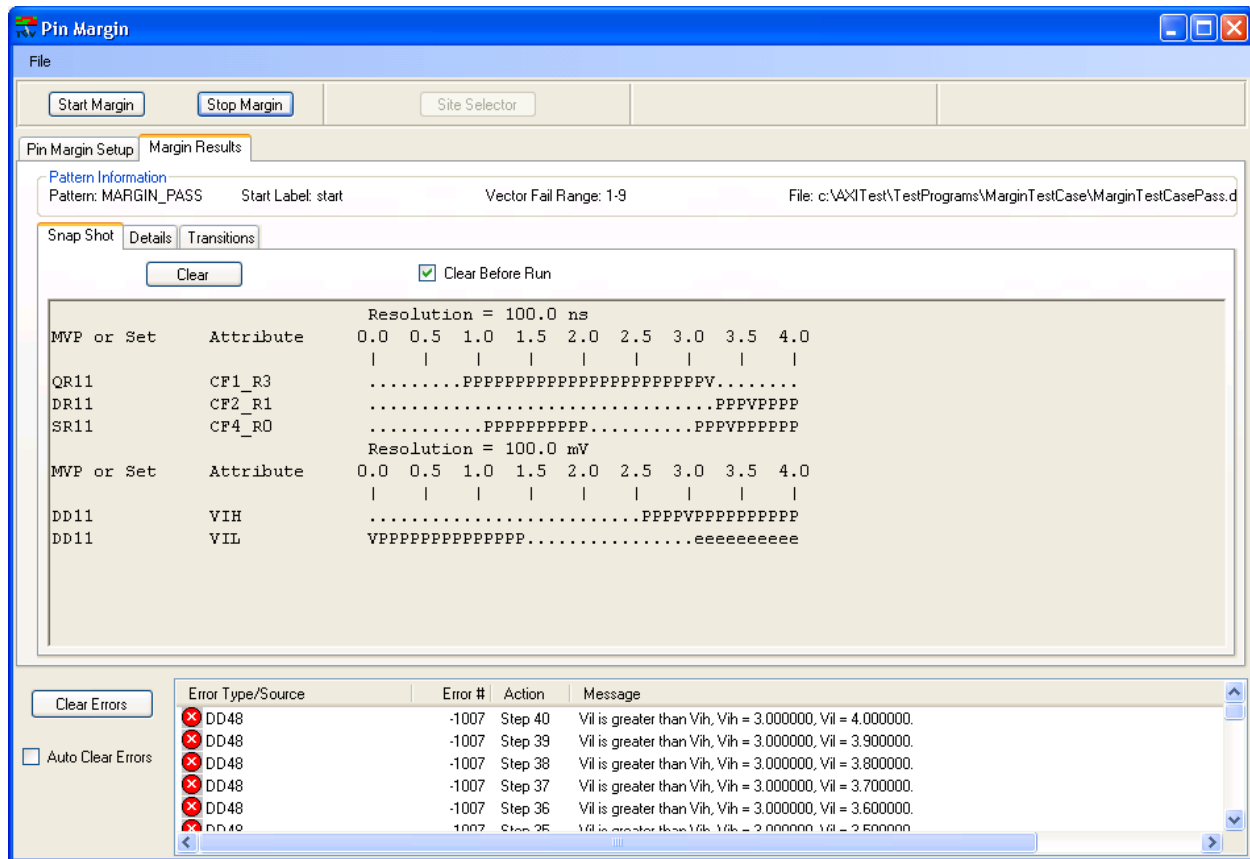
Margin step size is set

Multiple tests can be defined



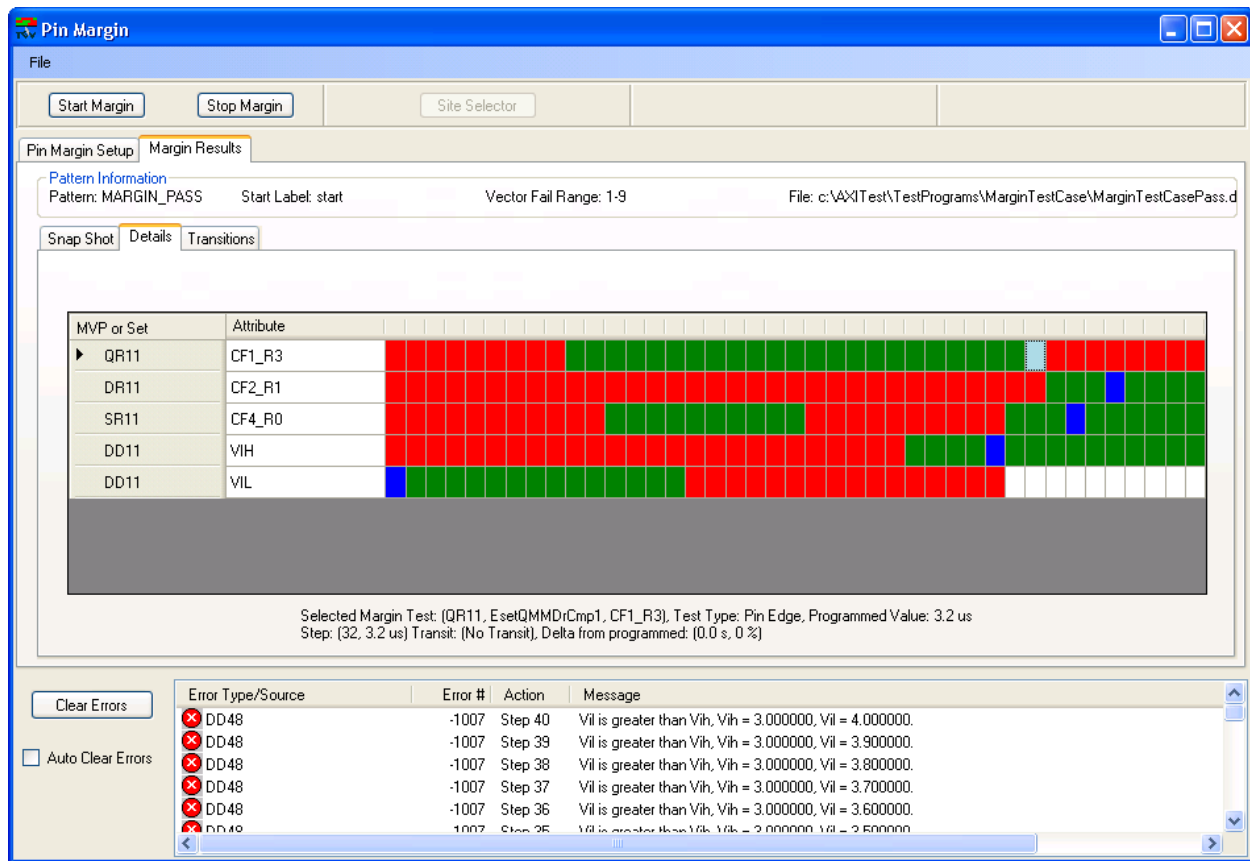
Pin Margin Results / Snap Shot

Text-based view provides a copy/pasteable view of margin results.



Pin Margin Results / Details

More detailed, graphical view of the margin results



Pin Margin Results / Transitions

Numerical details of the edge transitions.

The screenshot displays the 'Pin Margin' application window. The 'Margin Results' tab is active, showing a table of transition data. Below the table, an error log lists several 'DD48' errors with their respective step numbers and messages.

Pattern Information
Pattern: MARGIN_PASS Start Label: start Vector Fail Range: 1-9 File: c:\AXI\Test\TestPrograms\MarginTestCase\MarginTestCasePass.d

Transitions

MVP or Set	Attribute	Transit 1	Transit 2	Transit 3
QR11	CF1_R3	900.0 ns	3.3 us	
DR11	CF2_R1	3.3 us		
SR11	CF4_R0	1.1 us	2.1 us	3.1 us
DD11	VIH	2.6 V		
DD11	VIL	1.5 V		

Error Log

Error Type/Source	Error #	Action	Message
DD48	-1007	Step 40	Vil is greater than Vih, Vih = 3.000000, Vil = 4.000000.
DD48	-1007	Step 39	Vil is greater than Vih, Vih = 3.000000, Vil = 3.900000.
DD48	-1007	Step 38	Vil is greater than Vih, Vih = 3.000000, Vil = 3.800000.
DD48	-1007	Step 37	Vil is greater than Vih, Vih = 3.000000, Vil = 3.700000.
DD48	-1007	Step 36	Vil is greater than Vih, Vih = 3.000000, Vil = 3.600000.
DD48	-1007	Step 35	Vil is greater than Vih, Vih = 3.000000, Vil = 3.500000.

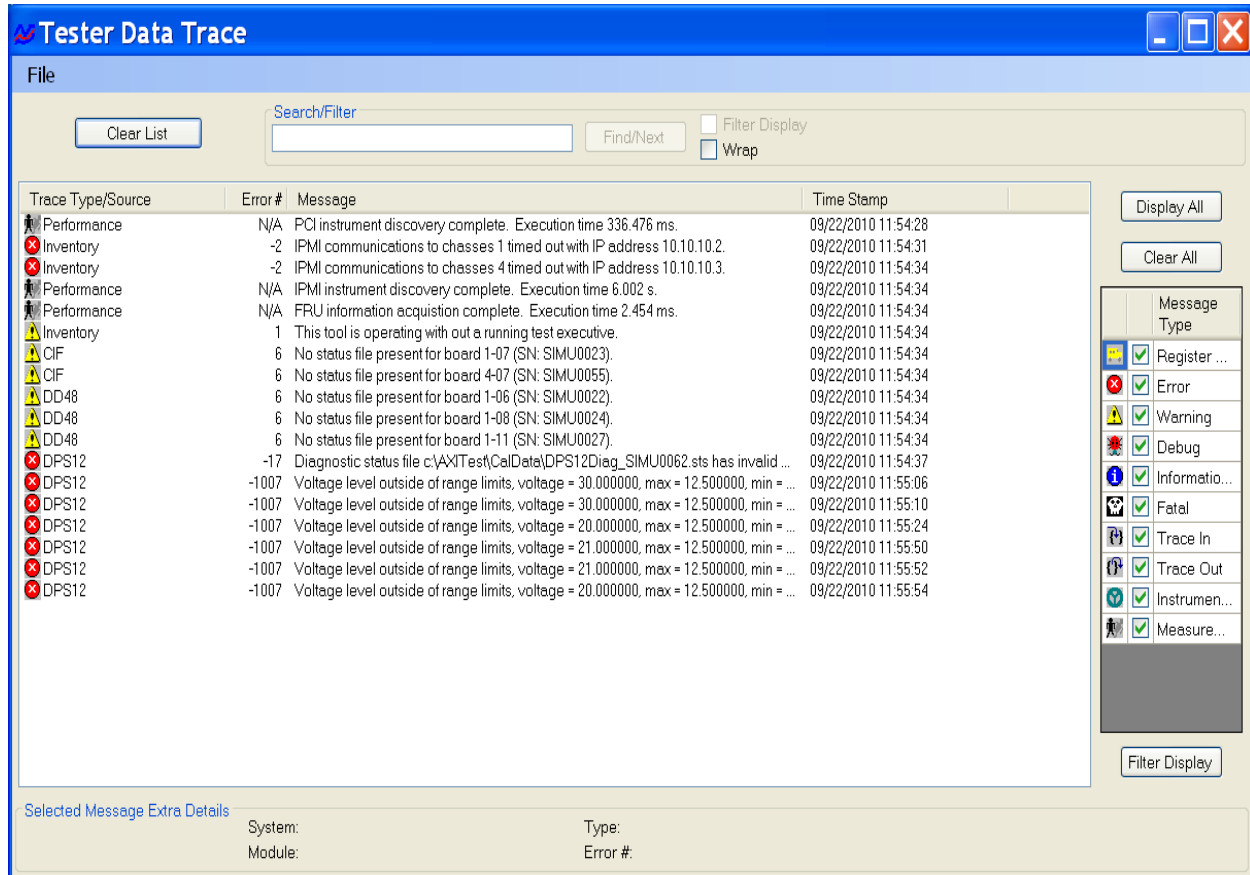
6 Trace Tool

The Trace Tool provide a central point to monitor the various error and warning messages generated as a test program is being debugged.

Describe the message types

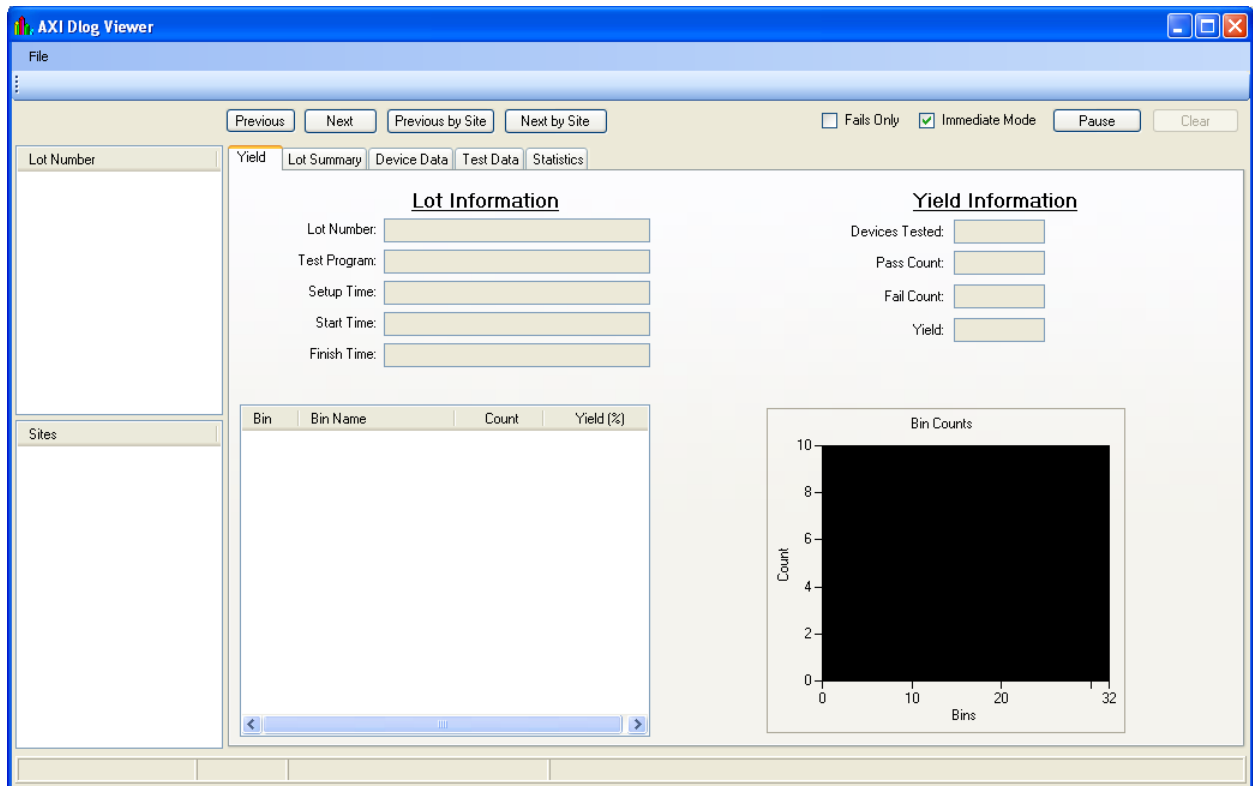
Filter display functionality

Test program APIs / output to



7 Datalog Viewer

The Datalog Viewer provides the ability to view the results of a test program both during execution and after one or more devices have been tested. The results of Tev diagnostic programs can also be viewed with this tool.

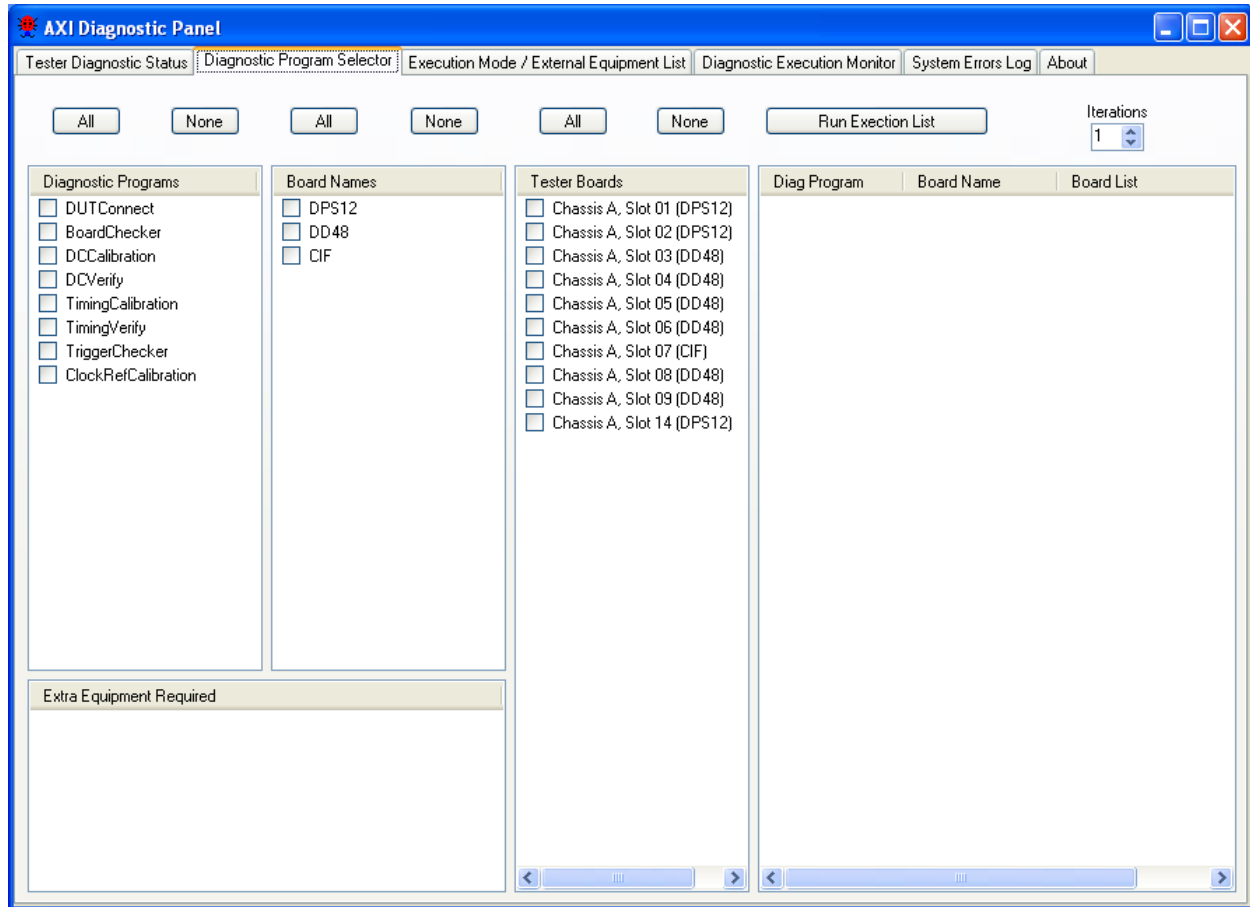


8 Diagnostic Tool

The Diagnostic Tool is the control center for running the various diagnostic programs.

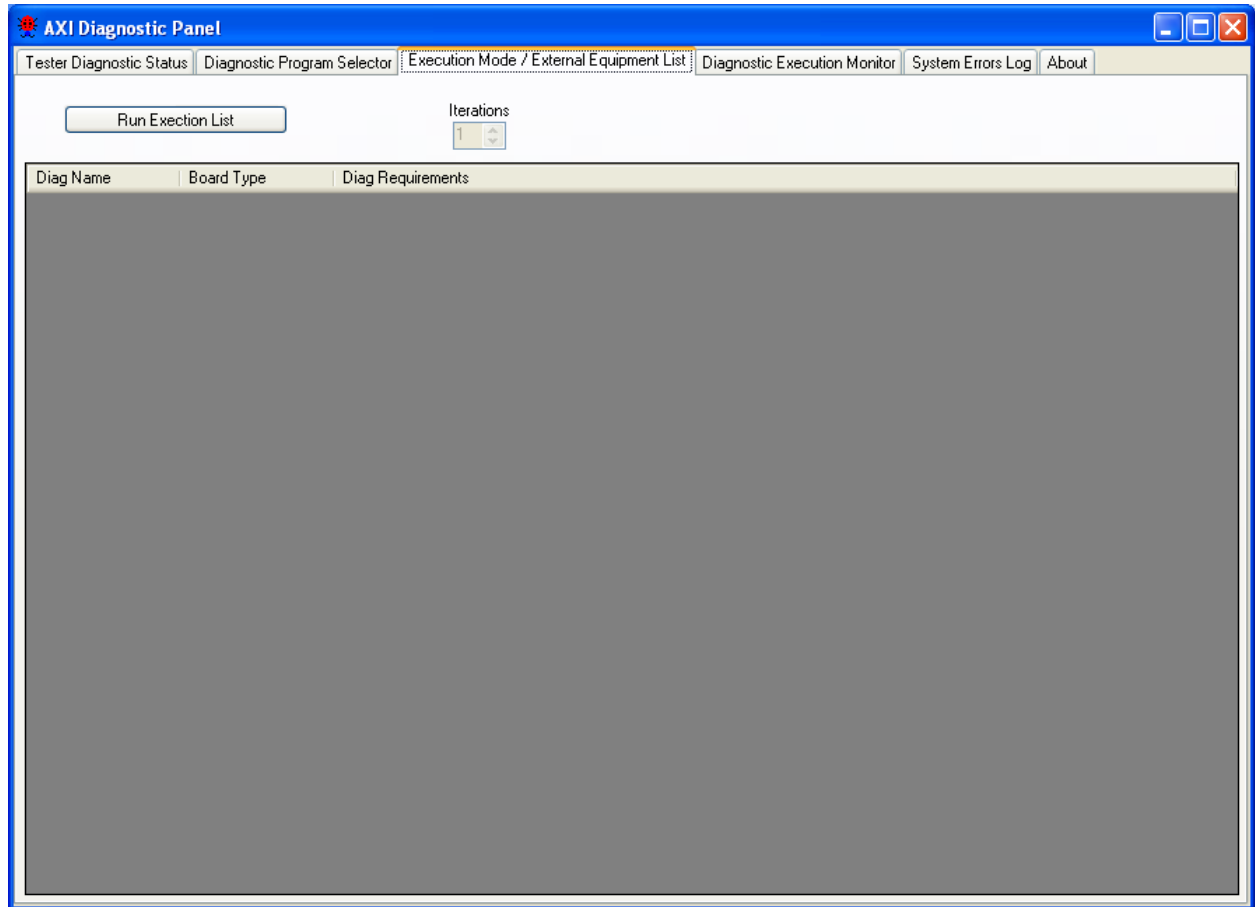
Organization

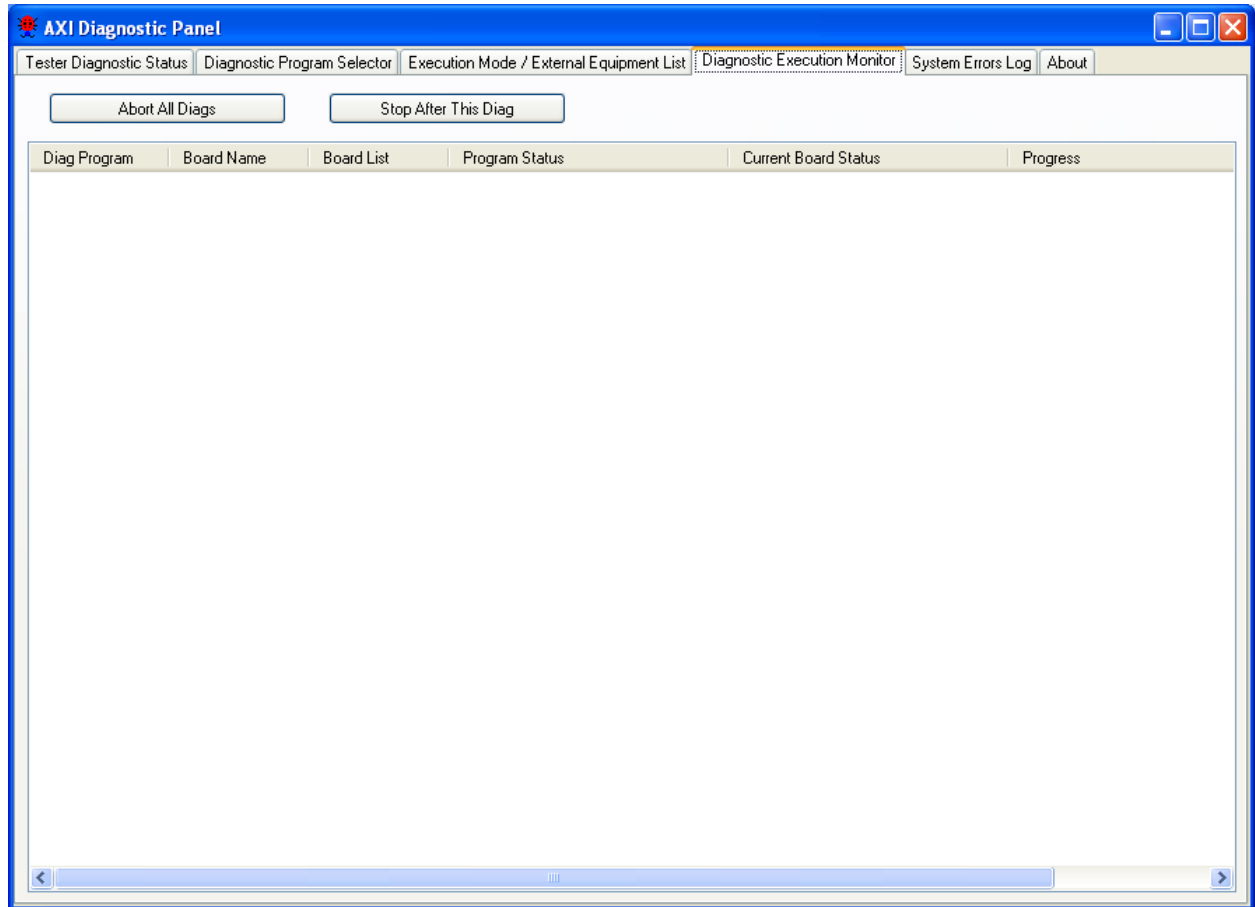
AXI Diagnostic Panel			
Tester Diagnostic Status Diagnostic Program Selector Execution Mode / External Equipment List Diagnostic Execution Monitor System Errors Log About			
Board/Diagnostic	Diagnostic Description	Next Run	Diagnostic Status
Tester	METRIKDS_3-5	Now	Tester is not fully qualified.
Chassis A	AXI	Now	Chassis is not fully tested.
Slot 1	DPS12	Now	At least one diagnostic has not yet been run.
DUTConnect	LoadBoard Connection Checker	Now	DUTConnect has not been run
BoardChecker	Internal Board Checker	Now	BoardChecker has not been run
DCCalibration	Calibration	Now	DCCalibration has not been run
DCVerify	Verification	Now	DCVerify has not been run
Slot 2	DPS12	Now	At least one diagnostic has not yet been run.
DUTConnect	LoadBoard Connection Checker	Now	DUTConnect has not been run
BoardChecker	Internal Board Checker	Now	BoardChecker has not been run
DCCalibration	Calibration	Now	DCCalibration has not been run
DCVerify	Verification	Now	DCVerify has not been run
Slot 3	DD48	Now	At least one diagnostic has not yet been run.
DUTConnect	LoadBoard Connection Checker	Now	DUTConnect has not been run
BoardChecker	Internal Board Checker	Now	BoardChecker has not been run
DCCalibration	Calibration	Now	DCCalibration has not been run
DCVerify	Verification	Now	DCVerify has not been run
TimingCalibration	Calibration	Now	TimingCalibration has not been run
TimingVerify	Verification	Now	TimingVerify has not been run
Slot 4	DD48	Now	At least one diagnostic has not yet been run.
DUTConnect	LoadBoard Connection Checker	Now	DUTConnect has not been run
BoardChecker	Internal Board Checker	Now	BoardChecker has not been run
DCCalibration	Calibration	Now	DCCalibration has not been run
DCVerify	Verification	Now	DCVerify has not been run
TimingCalibration	Calibration	Now	TimingCalibration has not been run
TimingVerify	Verification	Now	TimingVerify has not been run
Slot 5	DD48	Now	At least one diagnostic has not yet been run.
DUTConnect	LoadBoard Connection Checker	Now	DUTConnect has not been run
BoardChecker	Internal Board Checker	Now	BoardChecker has not been run
DCCalibration	Calibration	Now	DCCalibration has not been run
DCVerify	Verification	Now	DCVerify has not been run
TimingCalibration	Calibration	Now	TimingCalibration has not been run
TimingVerify	Verification	Now	TimingVerify has not been run

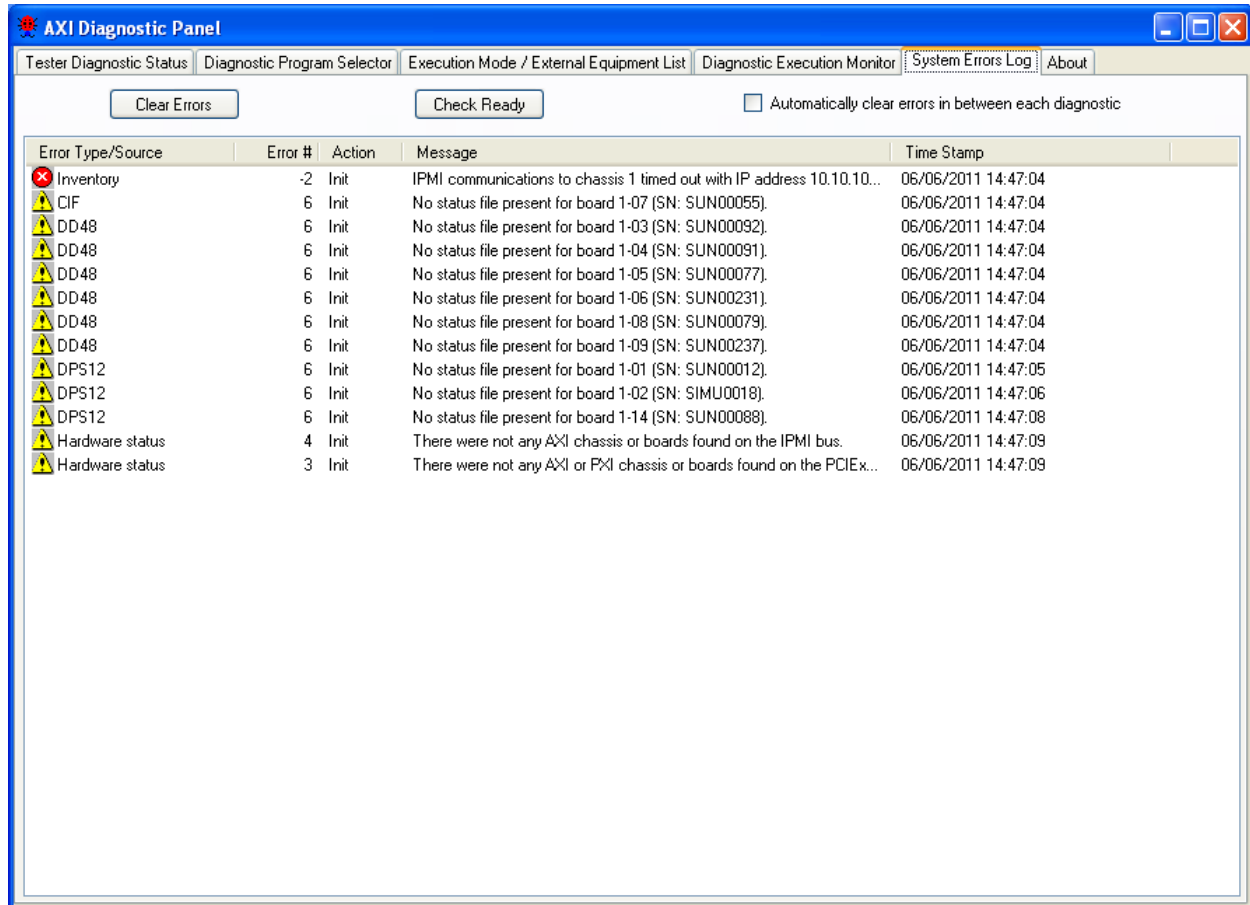


This tab provides the ability to select which diagnostics will be run

- | | |
|------------------|---|
| Left | Diagnostic Programs |
| | Board Names (which <u>type</u> of instrument boards) |
| | Tester Boards (which <u>physical</u> boards) |
| Right
clicked | Resulting sequence of diagnostic programs to be run when Run Execution List is |







AXI Diagnostic Panel

Tester Diagnostic Status | Diagnostic Program Selector | Execution Mode / External Equipment List | Diagnostic Execution Monitor | System Errors Log | About

About Tester Hardware

Component	Name	Serial #	Part #	Revision	Manufacturer	PCI ID	H8 Rev	FPGA 1 Name	FF
Tester	METRIKOS_3-5								
Chassis A	AXI_ATCA_14_7	MMMXXXXX	PCA0036-00	0000000X	TEV CORP				
Slot 1	DPS12	SUN00012	PCA0033-00	00000000	TEV CORP	0x1b71d012		DPS	2.0
Slot 2	DPS12	SIMU0018	PCA0033-00			0x1b71d012		DPS	2.0
Slot 3	DD48	SUN00092	PCA0034-00	00000004	TEV CORP	0x1b71dd48		Sequencer	
Slot 4	DD48	SUN00091	PCA0034-00	00000004	TEV CORP	0x1b71dd48		Sequencer	
Slot 5	DD48	SUN00077	PCA0034-00	00000004	TEV CORP	0x1b71dd48		Sequencer	
Slot 6	DD48	SUN00231	PCA0034-00	00000004	TEV CORP	0x1b71dd48		Sequencer	
Slot 7	CIF	SUN00055	PCA0030-00	00000001	TEV CORP	0x1b71c1f0		ATM	
Slot 8	DD48	SUN00079	PCA0034-00	00000004	TEV CORP	0x1b71dd48		Sequencer	
Slot 9	DD48	SUN00237	PCA0034-00	00000004	TEV CORP	0x1b71dd48		Sequencer	
Slot 14	DPS12	SUN00088	PCA0033-00	00000001	TEV CORP	0x1b71d012		DPS	2.0
Chassis 3	GX7300				Geotest - MTS Inc				

About Tester Software

Component Name	Component Type	Version	Comment
AXIDiagnostic Tool	Tool Application	1.4.0.3364 Release build	
MVP	Environment	1.4.0.3364 Release build	
CIF	Instrument Driver	1.4.0.3364 Release build	Supports instrument classes Analog ...
DD48	Instrument Driver	1.4.0.3364 Release build	Supports instrument classes Digital, ...
DPS12	Instrument Driver	1.4.0.3364 Release build	Supports instrument classes VI, Digi...
Starter	Instrument Driver	1.4.0.3364 Release build	User extendable.
DMM	Instrument Driver	1.4.0.3364 Release build	Supports VISA DMM Agilent34405.