# Power Transistor (50V, 3A) **2SD1760 / 2SD1864**

### Features

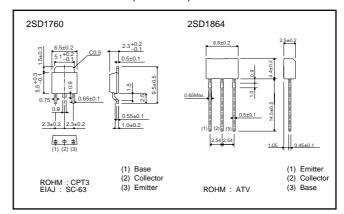
1) Low VCE(sat). VCE(sat) = 0.5V (Typ.) (Ic/IB = 2A / 0.2A)

2) Complements the 2SB1184 / 2SB1243.

# Structure

Epitaxial planar type NPN silicon transistor

# ●External dimensions (Units : mm)



# ● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	60	V
Collector-emitter voltage		Vceo	50	V
Emitter-base volt	age	Vево	5	V
Collector current		Ic	3	A (DC)
Collector current		IC IC	4.5	A (Pulse) *1
Collector power	2SD1760	Pc	15	W (Tc =25°C)*2
dissipation	2SD1864	PC	1	W
Junction temperature		Tj	150	°C
Storage temperature		Tstg	-55~+150	°C

<sup>\*1</sup> Single pulse, Pw = 100ms

<sup>\*2</sup> Printed circuit board, 1.7mm thick, collector copper plating 100mm² or larger.

## ● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	60	-	-	V	Ic = 50μA
Collector-emitter breakdown voltage	BVceo	50	-	-	V	Ic = 1mA
Emitter-base breakdown voltage	ВУЕВО	5	-	-	V	Iε = 50μA
Collector cutoff current	Ісво	-	-	1	μΑ	Vcb = 40V
Emitter cutoff current	ІЕВО	-	-	1	μΑ	V <sub>EB</sub> = 4V
Collector-emitter saturation voltage	VCE (sat)	-	0.5	1	V	Ic/I <sub>B</sub> = 2A/0.2A *
DC current transfer ratio	hfe	82	-	390	-	Vce = 3V, Ic = 0.5A *
Transition frequency	f⊤	-	90	-	MHz	VcE = 5V, IE = -500mA, f = 30MHz *
Output capacitance	Cob	-	40	-	pF	Vcb = 10V, IE = 0A, f = 1MHz

<sup>\*</sup> Measured using pulse current.

# ● Packaging specifications and hFE

		Package	Тар	oing
		Code	TL	TV2
Туре	hfe	Basic ordering unit (pieces)	2500	2500
2SD1760	PQR		0	-
2SD1864	PQR		-	0

hee values are classified as follows:

Item	Р	Q	R
hfe	82~180	120~270	180~390

### ●Electrical characteristic curves

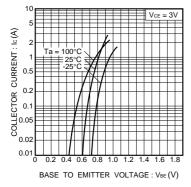


Fig.1 Grounded emitter propagation characteristics

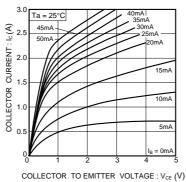


Fig.2 Grounded emitter output characteristics ( I )

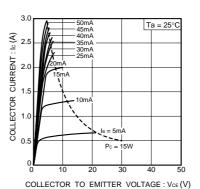


Fig.3 Grounded-emitter output characteristics( II )

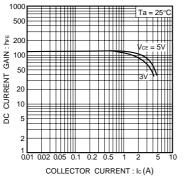


Fig.4 DC current gain vs. collector current( I )

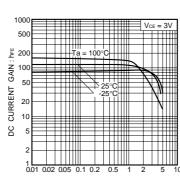


Fig.5 DC current gain vs. collector curren( II )

COLLECTOR CURRENT: Ic (A)

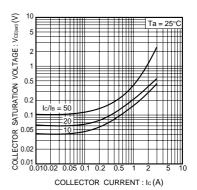


Fig.6 Collector-emitter saturation voltage vs. collector current



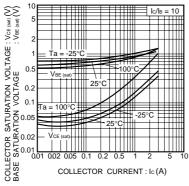


Fig.7 Collector-emitter saturation voltage vs. collector current Base-emitter saturation voltage vs. collector current

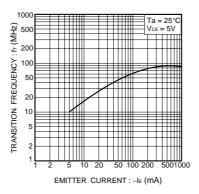


Fig.8 Gain bandwidth product vs. emitter current

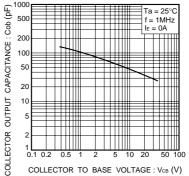


Fig.9 Collector output capacitance vs. collector-base voltage

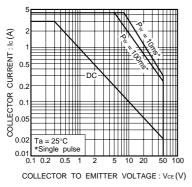


Fig.10 Safe operating area (2SD1760)

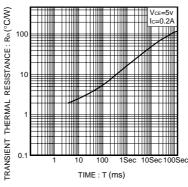


Fig.11 Transient thermal resistance (2SD1760)

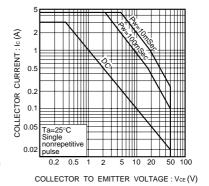


Fig.12 Safe operating area (2SD1864)

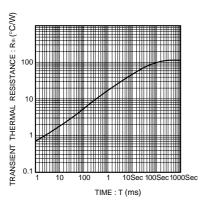


Fig.13 Transient thermal resistance (2SD1864)