COMP1521 WK07

number representations

Course Updates

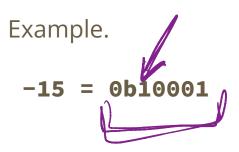
- Assignment 1 was due this Monday! Congrats for making it through the first half of the course
 - 5 days to submit with the late penalty (before 0 is awarded)
- Assignment 2 releasing some time soon
 - C Assignment

Contents

- Negative numbers Two's Complement
 - o Q1, Q2, Q3
- Floating point IEEE-754 representation
 - o Q4, Q5

Two's Complement

 Negative numbers are determined by whether or not the m.s.b (most significant bit) is set to 1



Representing Two's Complement Numbers in Binary

- 1. Represent the number in binary
- 2. Flip all the bits (set 0s to 1s and 1s to 0s)
- 3. Add one to the number

```
Example. \hat{n}um = -127
```

```
1. 127 = -0b0111 1111
2. = 0b1000 0000
3. = 0b1000 0001
```

Assume that the following hexadecimal values are 16-bit twos-complement. Convert each to the corresponding decimal value.

a.
$$0 \times 00013$$
 0000 0000 0001 0011 \rightarrow 19

b. 0×1234 >0001 0010 0011 0100 \rightarrow

c. 0×6666

d. 0×8000

1000 0000 0000 0000 \rightarrow \rightarrow -1

Give a representation for each of the following decimal values in 16-bit two's-complement bit-strings. Show the value in binary, octal and hexadecimal.

- a. 1000
- c. -5
- d. -100

0×03C8

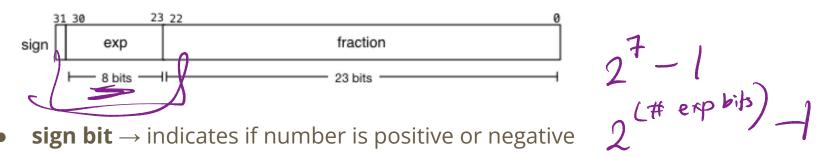
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Floating Point Numbers

- We need a way to represent base 10 Real numbers in Binary
- Easy to represent Integers in base2 but Fractional numbers can be tough to get accurate (can cause floating point errors)
- Base10 has scientific notation
 - o 6.022 * 10^23
- Base2 also has scientific notation
- IEEE-754 is the standard for floating point arithmetic

Representing Floating Point Numbers in Binary



- **exponent bits** \rightarrow unsigned 8 bit value which is interpreted from -127 to 128 by subtracting the "bias" 127
- **fraction** (mantissa) bits \rightarrow 23 bits whose values are in the range 0 to 1. We add 1 to the fractional component to convert the values to be between 1 to 2

```
sign * (1 + fraction) * 2^(exponent - bias)
```

- sign bit $\rightarrow 1$ bit $2^{3-1} = 37$
- **exponent bits** \rightarrow 3 bits (bias = 3)
- fraction (mantissa) bits \rightarrow 4 bits

$$\left(1+\frac{3}{8}\right)\times2^{4-3}=\left(1+\frac{3}{8}\right)\times2$$

1/2 /4 /8 /110

$$e \times p = 4$$

$$frac = \frac{1}{4}t\frac{1}{8} = \frac{3}{8}$$

What decimal numbers do the following single-precision IEEE 754-encoded bit-strings represent?

			c-strings represent:	0 = qx9
a.	0	00000000	000000000000000000000000	Grac=D
(b.)	1	0000000	0000000000000000000000	F. 40 - D
(c.)	0	01111111	1000000000000000000000	$(1+0) \times 2^{0-127}$
d.	0	01111110	000000000000000000000000000	C1407 x D
e.	0	01111110	111111111111111111111111111	-127 × D
f.	0	10000000	01100000000000000000000	2'''
g.	0	10010100	10000000000000000000000	
h.	0	01101110	10100000101000001010000	b U

Each of the above is a single 32-bit bit-string, but partitioned to show the sign, exponent and fraction parts.

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$$Sign = 0 \rightarrow pos$$

 $exp = 126$
 $Rac = \frac{1}{2} + \frac{1}{4} + \frac{1}{5} + \frac{1}{16} + \frac{1}{2}$
 $2 + \frac{1}{5} + \frac{1}{16} + \frac{1}{2} + \frac{1}{2}$

Each of the above is a single 32-bit bit-string, but partitioned to show the sign, exponent and fraction parts.

$$= 1 - 991 \times 2^{-1} = 1 - 991 \times$$

$$(1 + frac) \times 2$$
 exp-bias.

Convert the following decimal numbers into IEEE 754-encoded bit-strings:

a.
$$2.5 = (1 \pm 0.25) \times 2$$

b. 0.375

c. 27.0

frac = $\frac{128}{4}$
 010000000000

d. 100.0

Tutorial Q5
$$(1+frac) \times 2$$

Convert the following decimal numbers into IEEE 754-encoded bit-strings:

$$(1+0.5) \times 2 = -2+127$$

$$= 125$$

Special Floating Point Numbers

 $\mathbf{0} \rightarrow \text{exponent bits are 0}$

- 000000000 11.00110.
- **Infinity**→ exponent bits are all 1's, fraction is 0 つるつつつ
- **NaN** \rightarrow exponent bits are all 1's, fraction is not 0



