



## FINISHED AMPLIFIER:

This circuit verifies the gain, match and noise figure of the finished amplifier (still using ideal components).

The next step is to replace the ideal components with "real world" parts from the SMT Passive Component Library and the Packaged BJT Library. See Amplitude.dsn for the resulting schematic and layout.

## SIMULATIONS

S-PARAMETERS	OPTIONS
S_Param	Options1
SP2	Temp=16.85
SweepVar="freq"	Tnom=25
Start=1 GHz	TopologyCheck=yes
Stop=4 GHz	V_RelTol=1e-6 V
Step=1 MHz	I_RelTol=1e-6 A
CalcS=yes	GiveAllWarnings=yes
CalcNoise=yes	MaxWarnings=10
BandwidthForNoise=1.0 Hz	