



FINISHED AMPLIFIER:

This circuit verifies the gain, match and noise figure of the finished amplifier (still using ideal components).

The next step is to replace the ideal components with "real world" parts from the SMT Passive Component Library and the Packaged BJT Library. See AmpLayout.dsn for the resulting schematic and layout.

SIMULATIONS



S-PARAMETERS

S_Param
SP2

SweepVar="freq"
Start=1 GHz
Stop=4 GHz
Step=1 MHz
CalcS=yes
CalcNoise=yes
BandwidthForNoise=1.0 Hz



OPTIONS

Options
Options1
Temp=16.85
Tnom=25
TopologyCheck=yes
V_RelTol=1e-6 V
I_RelTol=1e-6 A
GiveAllWarnings=yes
MaxWarnings=10