



FACULTY OF INFORMATION TECHNOLOGY AND ELECTRICAL ENGINEERING
MASTER'S PROGRAMME IN WIRELESS COMMUNICATIONS ENGINEERING

521327S Radio Engineering 2 Design Exercise Report

Group 06

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LIST of SYMBOLS and ABBREVIATIONS

α	roll-off factor
S	Scattering parameter
ACPR	Adjacent Channel Power Ratio
ADS	Advanced Design System
BPF	Band Pass Filter
BW	Band Width
dB	Decibels (relative to a 1 Watt)
dBm	Decibels (relative to a 1 mWatt)
DQPSK	Differential Quadrature Phase Shift Keying
EVM	Error Vector Magnitude
IF	Intermediate Frequency
IIP	Input Intercept Point
IMD	Intermodulation Distortion
LNA	Low Noise Amplifier
LO	Local Oscillator
MDS	Minimum Detectable Signal
NF	Noise Figure
OIP	Output Intercept Point
PA	Power Amplifier
RF	Radio Frequency
SAW	Surface Acoustic Wave
SNR	Signal to Noise Ratio
TOI	Third Order Intercept

1 Receiver Design

1.1 Receiver specifications

To begin, the different system specifications of the receiver should be determined.

The main specifications of the receiver were given as:

- Center frequency of the system tuning range 10 GHz.
- Bandwidth of the main lobe between first nulls of the pulse shaped signal 20 MHz.
- The system has 15 channels.
- Sensitivity at least -87 dBm.
- SNR requirement at the output of the IF-stage at least +9 dB.
- Signal level at the output of the IF-stage at least +4 dBm.
- At the output of the IF-stage the 1 dB compression point greater than +17 dBm.
- The IF-frequency must be 618 MHz.

The MDS (P_{sens}) is -87 dBm and the minimum output power level ($P_{out,min}$) is +4 dBm. Therefore, the gain of the receiver is

$$G_{RX} \geq \frac{P_{out,min}}{P_{sens}} \implies G_{RX} \geq 91dB$$

The total bandwidth of the receiver is

$$BW = \text{Number of channels} \times BW_{\text{Single Channel}} \implies BW = 300MHz$$

Hence, since most of [L3 Narda Miteq](#) mixers are lossy ones, we partition the gain such that the RF stage takes $\frac{1}{3}$ of the total gain (around 20 or 30 dB) reserve $\frac{2}{3}$ for the IF stage and add some margin to tolerate the losses of the filters and mixer.

The minimum SNR at the output (SNR_{min}) should be at least +9 dB, therefore

$$P_{sens} = N_{in} + SNR_{min} = KTBW + NF + SNR_{min} \implies NF = P_{sens} - KTBW + SNR_{min} = 4.97dB$$

This means that to reach the SNR requirement, we should have $NF \leq 4.97dB$.

1.2 Components selection

1.2.1 Filters

1.2.1.1 RF filters

In selecting the RF filter, the attenuation of filter in the passband and insertion loss is taken into account. RF filter attenuation is equal to NF of the filter and since the first component in

the chain has the most effect on the total NF, it should be selected carefully. The filter should also ensure the rejection of spurious frequencies out of RF frequency range. The passband of the filter should be equal to the total system bandwidth of 300 MHz and centered around RF frequency (10 GHz). The filter with part number 4MP10-10000/T300-Z/Z was selected as it meets the required specifications, as shown in Figure 1.1.

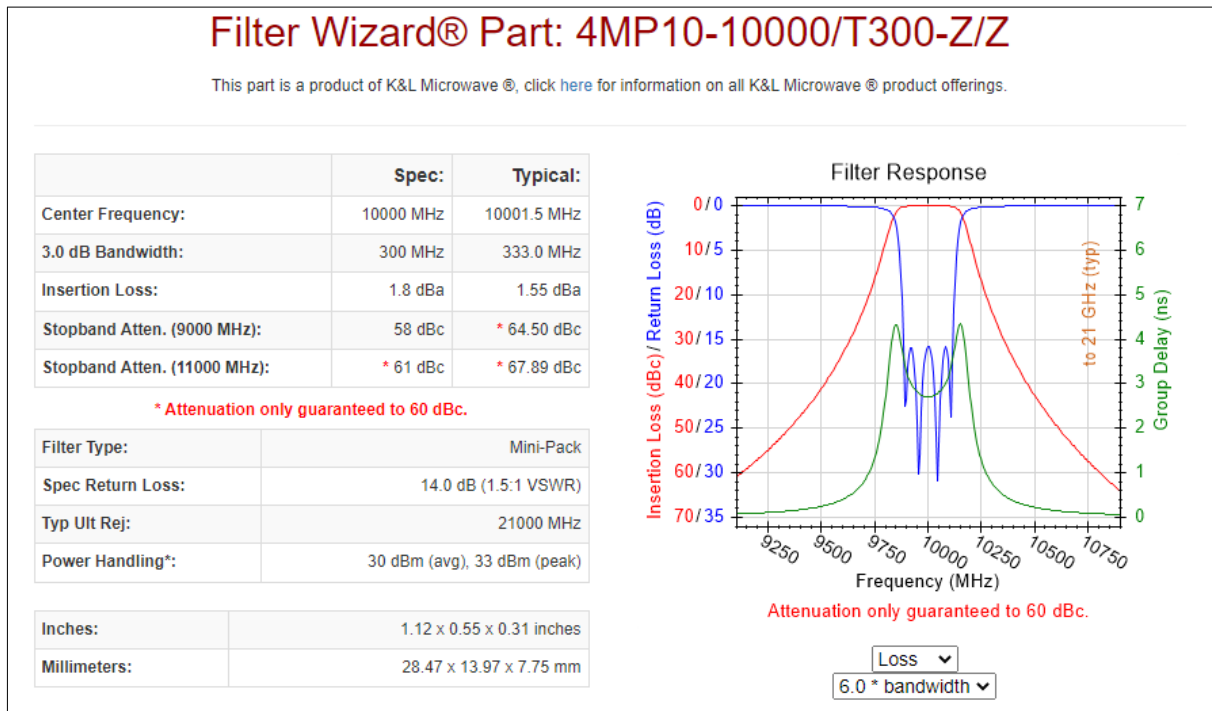


Figure 1.1: RF filter manufacturer's specifications

The RF filter has been modeled in ADS using its typical values and the Chebyshev filter model, and tested against the s-parameters provided by the manufacturer, as shown in Figure 1.2. With some trial and error to determine the order of the Chebyshev filter, the modeled filter resembles the operation of the real filter, as shown in Figure 1.3.

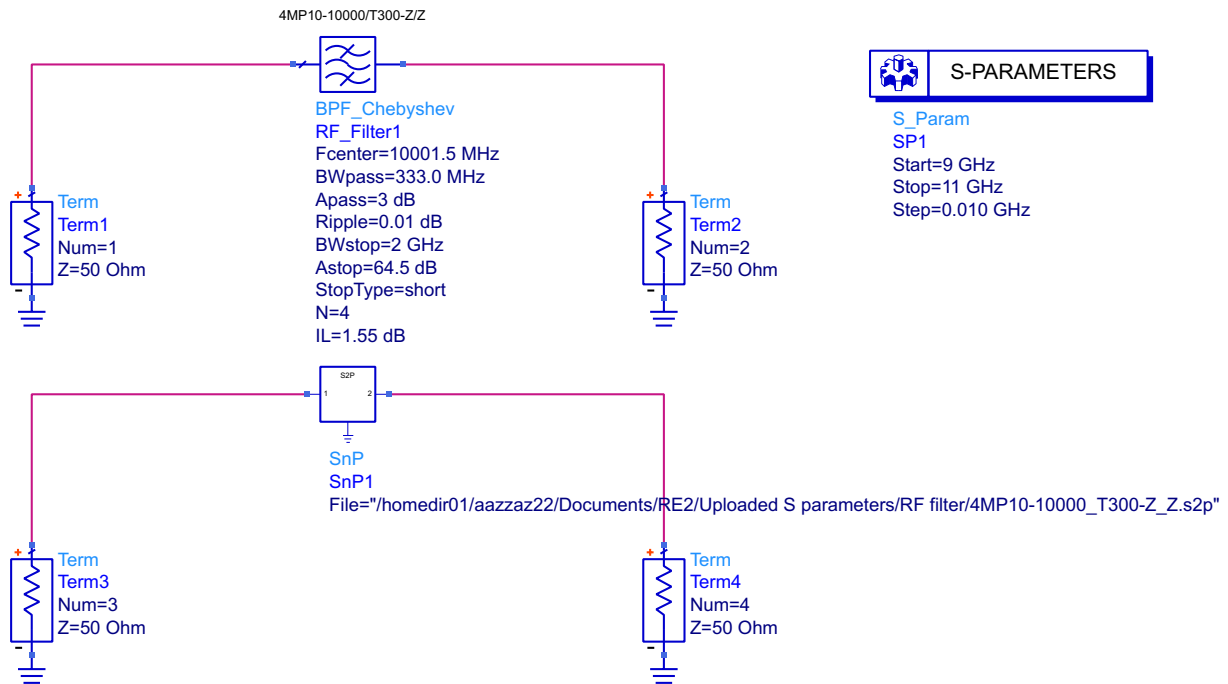


Figure 1.2: RF filter comparison test bench

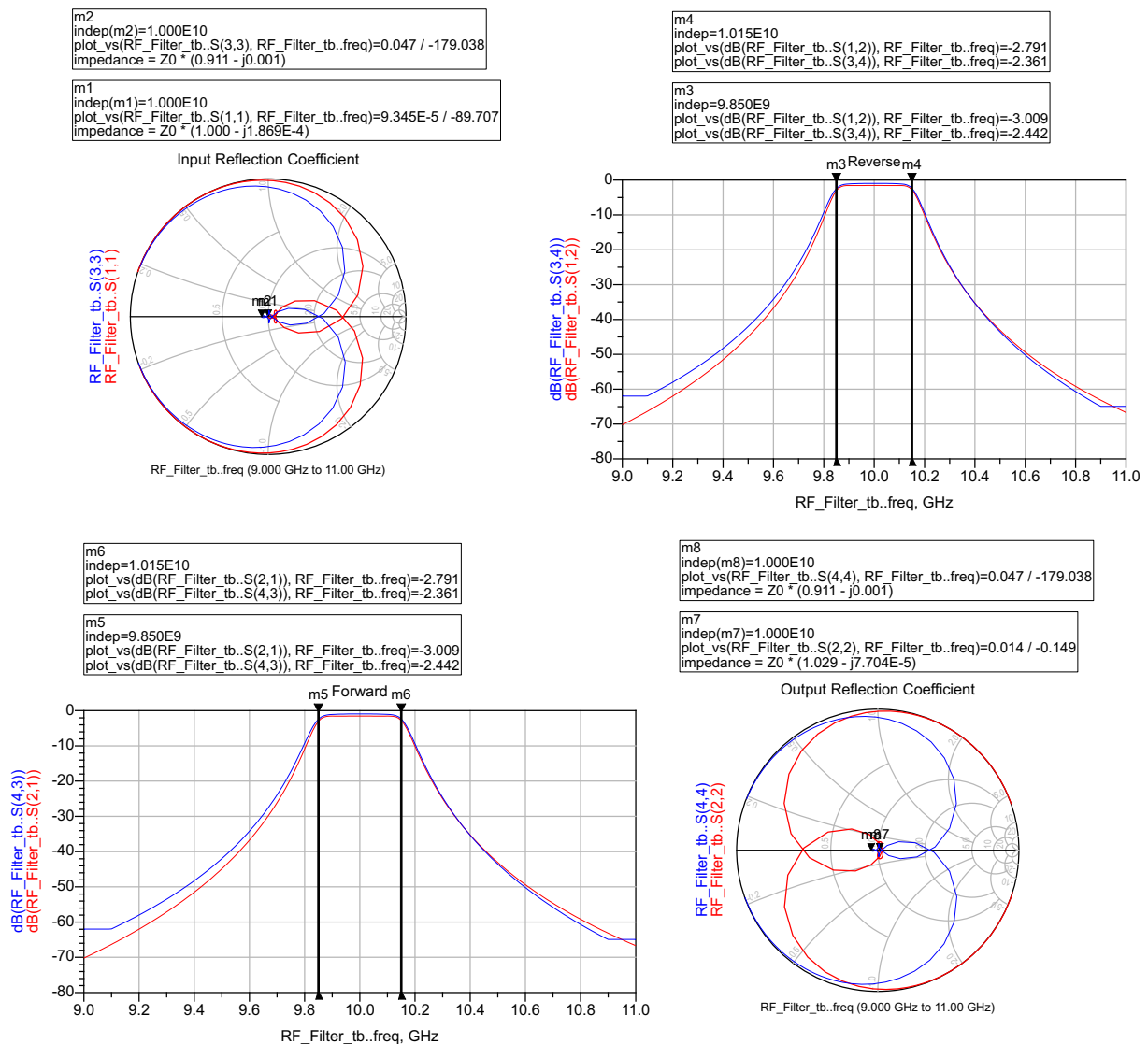


Figure 1.3: RF filter comparison results

1.2.1.2 IF filters

Similarly, the If filter has been selected to have a bandwidth of one channel (20 MHz) centered around the IF frequency (618 MHz) and a reasonable insertion loss. The filter with the part number 5DH35-618/T20-1.9 has been selected as it meets the specifications, as shown in Figure 1.4. The IF filter has been modeled in ADS using the Chebyshev filter model and tested against the s-parameters provided by the manufacturer, as shown in Figure 1.5, and its operation resembles the operation of the real filter, as shown in Figure 1.6.

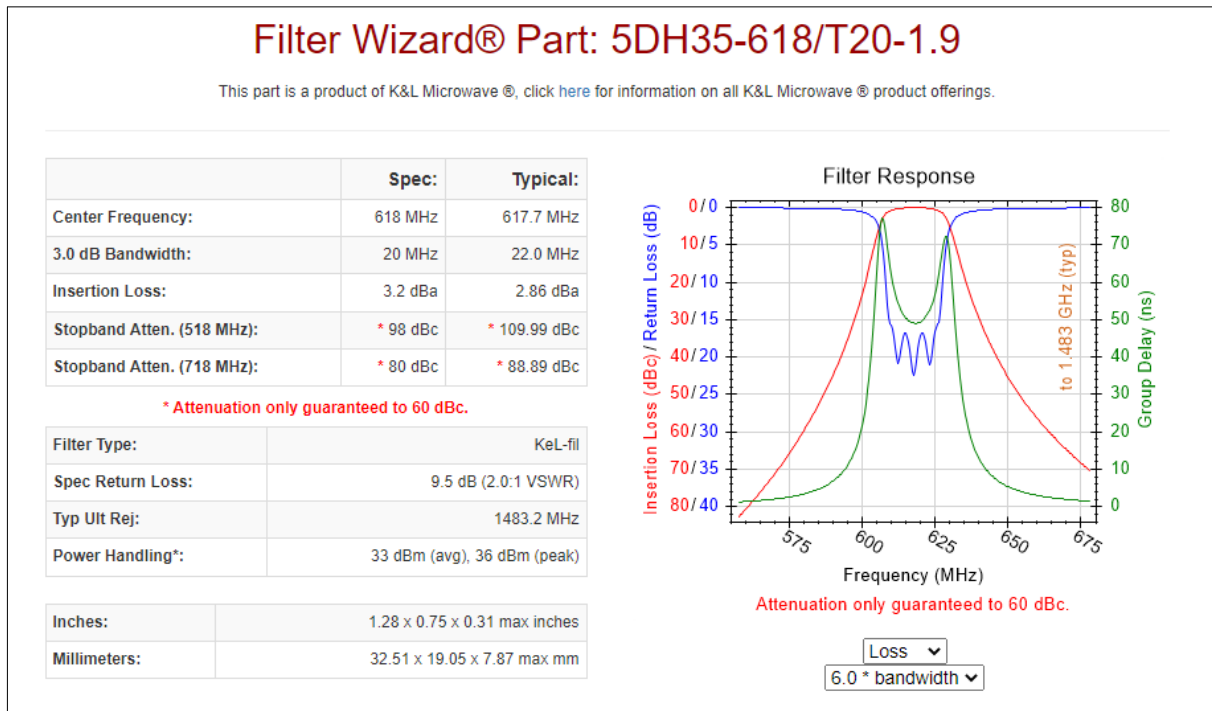


Figure 1.4: IF filter manufacturer's specifications

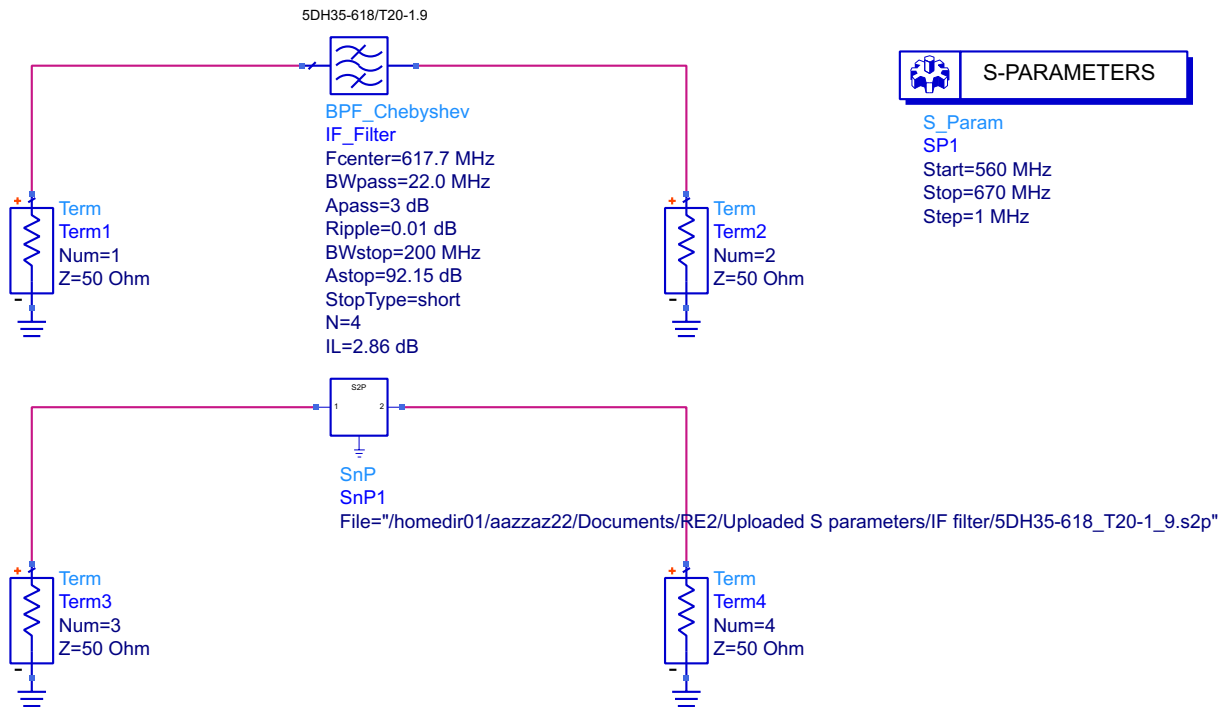


Figure 1.5: IF filter comparison test bench

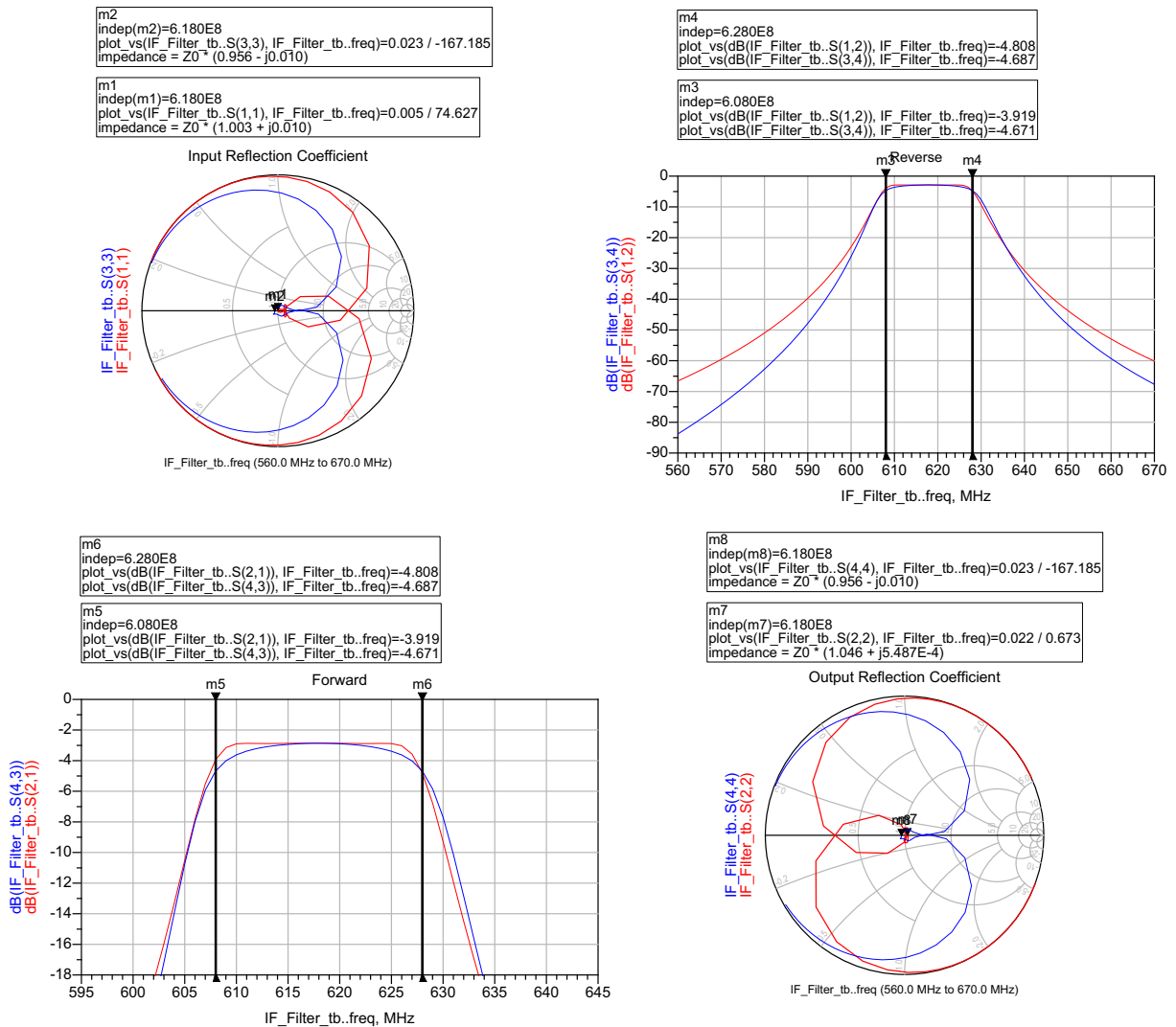


Figure 1.6: IF filter comparison results

There are minor differences between the response of modeled RF and IF filters and the manufacturer's response. The main difference is that the stop band rejection of the real filters is guaranteed only to 60 dBc but the modeled filters extends to 70 dBc and beyond. This difference might result in extra attenuation of any interference near the cutoff of the filters which might result in the simulation performing better than reality.

1.2.2 RF Amplifier (LNA)

The RF amplifier (LNA) is selected to operate over the system's total bandwidth and provide high enough gain with minimum noise figure deterioration. The selected amplifier has the part number [NSM3-08001200-11](#) and its specs are shown in Figure 1.7. The RF amplifier has a surface mount package, as shown in Figure 1.8, with three leads and the body used as a ground.

Model	Quote	Freq Min (MHz)	Freq Max (MHz)	Gain Min (dB)	Gain Flatness (dB+/-)	Noise Figure (dB)	Noise Temperature (K)	Input VSWR	Output VSWR	P1dB Out (dBm)	IP3 Out Typ (dBm)	V1 Nom. (V)	Curr1 Nom (mA)	Data sheet
NSM3-08001200-11	Quote	8000	12000	24	1	1.1	84	2	2	8	18	15	80	PDF

Figure 1.7: RF amplifier manufacturer's specifications

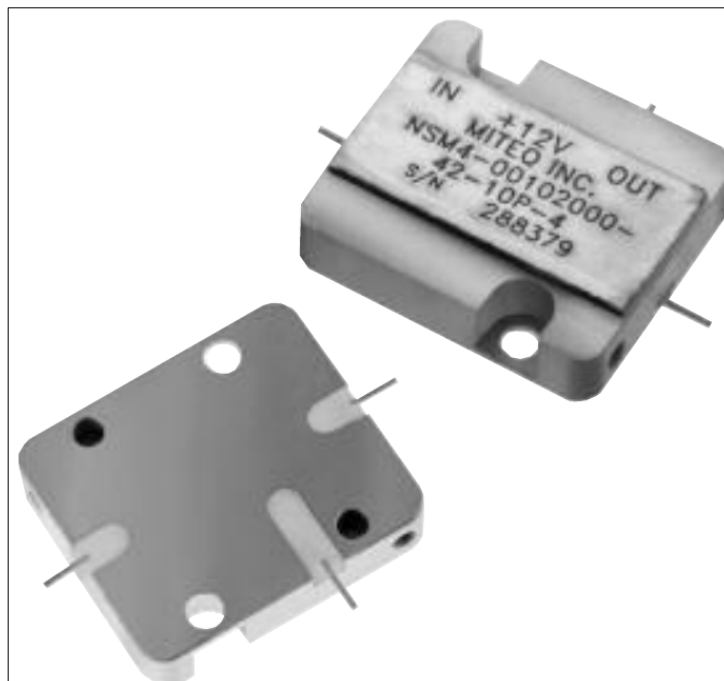


Figure 1.8: RF amplifier package

1.2.3 IF Amplifier

The IF amplifier is selected to operate over a single channel bandwidth and mainly provide high enough gain and higher compression point. The selected amplifier has the part number [NSM3-00100200-14-10P-4](#) and its specs are shown in Figure 1.9. The mixer has two ranges for the LO power, the lower range of 10 dBm is selected. The IF amplifier has the same package as the RF one, a surface mount package, as shown in Figure 1.8, with three leads and the body used as a ground.

Model	Quote	Freq Min (MHz)	Freq Max (MHz)	Gain Min (dB)	Gain Flatness (dB+/-)	Noise Figure (dB)	Noise Temperature (K)	Input VSWR	Output VSWR	P1dB Out (dBm)	IP3 Out Typ (dBm)	V1 Nom. (V)	Curr1 Nom (mA)	Data sheet
◆		◆	◆	◆	◆	◆	◆	◆	◆	◆	◆	◆	◆	◆
NSM3-00100200-14-10P-4	Quote	100	2000	30	1	1.4	111	2	2	10	20	15	150	PDF

Figure 1.9: IF amplifier manufacturer's specifications

1.2.4 Mixer

The mixer is selected to operate over the system's total RF bandwidth and provide low conversion loss with minimum noise figure deterioration. The selected mixer has the part number [DB0418HW6](#) and its specs are shown in Figure 1.10. The mixer has a drop in substrate package, as shown in Figure 1.11, with three leads and the body used as a ground.






















































Model	Quote	RF Freq Min (MHz)	RF Freq Max (MHz)	LO Freq Min (MHz)	LO Freq Max (MHz)	IF Freq Min (MHz)	IF Freq Max (MHz)	Conversion Loss Typical dB	Conversion Gain Typical dB	LO to RF Isolation Typical (dB)	LO to IF Isolation Typical (dB)	RF to IF Isolation Typical (dB)	LO Power Min (dBm)	LO Power Typ (dBm)	LO Power Max (dBm)	IP3 In Typ (dBm)	IP3 Out Typ (dBm)	P1dB In Typ (dBm)	P1dB Out Typ (dBm)	RF VSWR Typical	LO VSWR Typical	IF VSWR Typical	Single Bal.	Double Bal.	Triple Bal.				
																													
DB0418HW6	Quote	4000	18000	4000	18000	0	2000	8		30	20	25	17	20	23	23	15	13	5	1.5	1.25	2	0	1	0				
DB L	Triple Bal.	Quad IF	Image Reject	Image Rejection (dB)	Low Noise	SSB Noise Figure Typical (dB)	Sub Harmonic	Active Device	V1 Min (V)	V1 Nom. (V)	V1 Max (V)	Curr1 Nom (mA)	NxM 1X1 (dB)	NxM 1X2 (dB)	NxM 1X3 (dB)	NxM 2X1 (dB)	NxM 2x2 (dB)	NxM 2X3 (dB)	NxM 3X1 (dB)	NxM 3X2 (dB)	NxM 3X3 (dB)	Op. Temp Low (°C)	Op. Temp High (°C)	Package	Surface Mount	Package	Hermetic	ROHS	Outline Drawing
																													
0	0	0		0	7.5	0	0						0	-53	-57	-25	-60	-68	-12	-48	-55	-54	85	Drop In Substrate	0	Drop In Substrate	0	0	

Figure 1.10: Mixer manufacturer's specifications

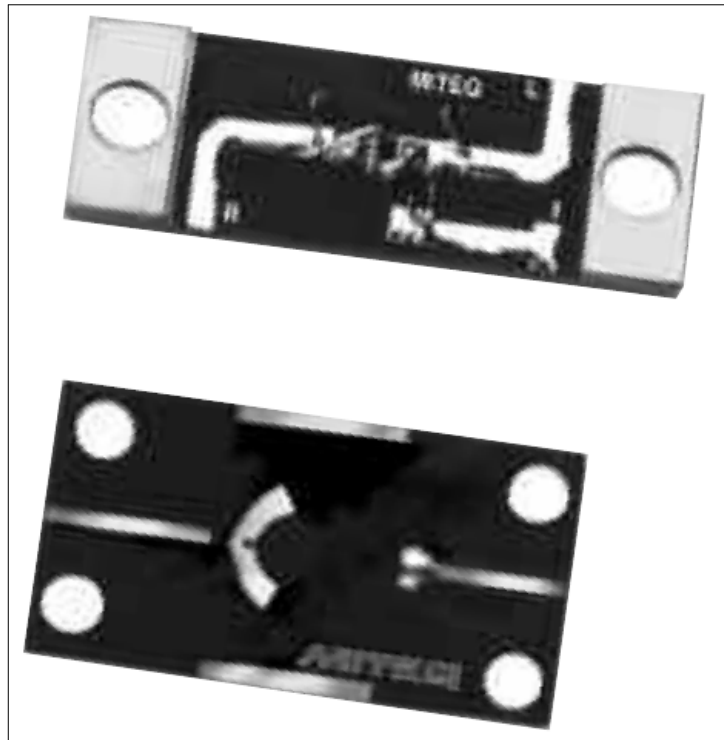


Figure 1.11: Mixer package

All the selected components for the receiver are surface mount devices with either pins (RF filters, RF and IF amplifiers, mixer) or castellated edges (mixer, IF filter) which restricted the components selection but this gives a more accurate idea about the performance of the receiver if it is implemented in reality.

1.3 Budget Simulation

The budget simulation is performed to determine the total gain, noise figure, output power and the 1 dB compression point of the receiver chain. Figure 1.12 shows the whole modeled receiver chain. Parameterization of the components is carried out as per the given typical specifications. Figures 1.13 and 1.14 show the results for the budget simulation. At the sensitivity level (-87 dBm), the results are:

- The output power is 12.898 dBm which surpasses the +4 dBm specified.
- The receiver noise figure is 3.018 dB which is well below the calculated 4.97 dB.
- The total gain of the receiver is 99.898 dB which is above the calculated 91 dB.
- The SNR of the receiver is $12.898 - 2.154 = 10.744$ dB which meets the specified 9 dB.
- The output 1 dB compression point is 19.977 dB which surpasses the required +17 dBm.

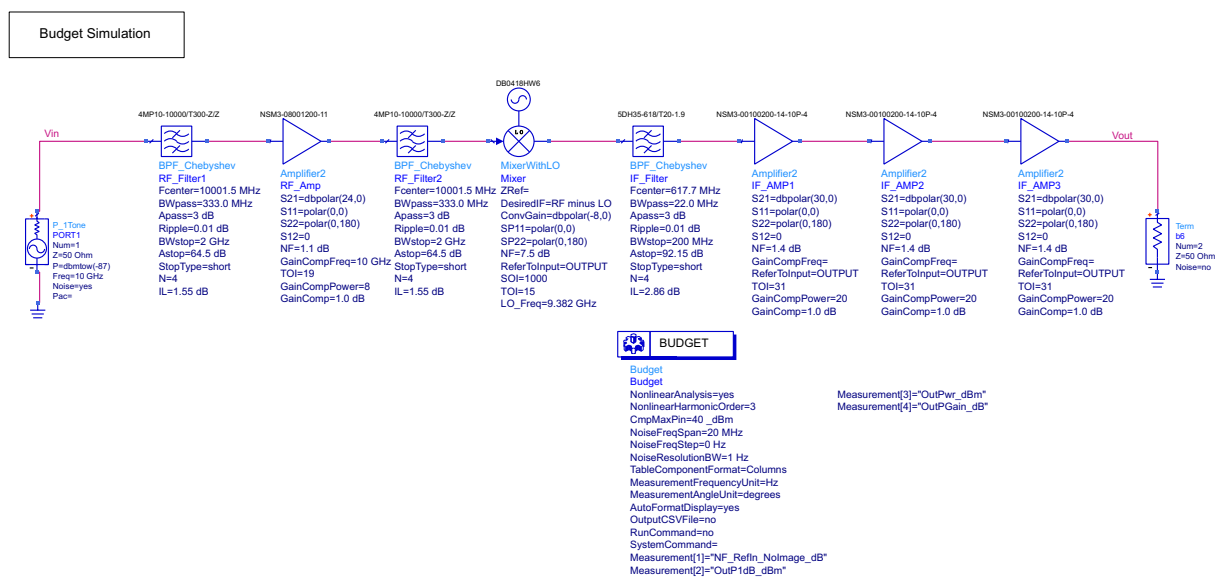


Figure 1.12: Budget simulation schematic

Measurements Tables

Meas_Name	RF_Filter1	RF_Amp	RF_Filter2
NF_RefIn_NoImage_dB	1.549	2.649	2.655
OutP1dB_dBm	1000.000	30.950	29.400
OutPwr_dBm	-88.550	-64.550	-66.100
OutPGain_dB	-1.550	22.450	20.900

Meas_Name	Mixer	IF_Filter
NF_RefIn_NoImage_dB	2.743	2.852
OutP1dB_dBm	4.337	1.477
OutPwr_dBm	-74.100	-76.960
OutPGain_dB	12.900	10.040

Meas_Name	IF_AMP1	IF_AMP2	IF_AMP3
NF_RefIn_NoImage_dB	2.936	2.936	2.936
OutP1dB_dBm	19.790	19.977	19.977
OutPwr_dBm	-46.960	-16.960	12.898
OutPGain_dB	40.040	70.040	99.898

Summary Tables

System_Name	System_Value
SystemInN0_dBm	-173.855
SystemInNPwr_dBm	-100.844
SystemInP1dB_dBm	-79.063
SystemNF_dB	3.018
SystemOutN0_dBm	-70.857
SystemOutNPwr_dBm	2.154
SystemOutP1dB_dBm	19.977
SystemPGain_SS_dB	100.040
SystemPGain_dB	99.898
SystemPOut_dBm	12.898
SystemS11_dB	-80.588
SystemS11_mag	9.345E-5
SystemS11_phase	-89.707
SystemS12_dB	-400.000
SystemS12_mag	0.000
SystemS12_phase	0.000
SystemS21_dB	99.898
SystemS21_mag	98829.416
SystemS21_phase	-5.675
SystemS22_dB	-315.399
SystemS22_mag	1.698E-16
SystemS22_phase	180.000

Setup_Name	Setup_Value
System_AnalysisType	1.000
System_NoiseResBW	1.000
System_NoiseSimBW	2.000E7
System_NoiseSimFStep	2.000E7
System_PilotFreq	1.000E10
System_PilotPwr_dBm	-87.000
System_RefR	50.000
System_SourceFreq	1.000E10
System_SourcePwr_dBm	-87.000
System_SourceTemp	25.000

Figure 1.13: Budget simulation results

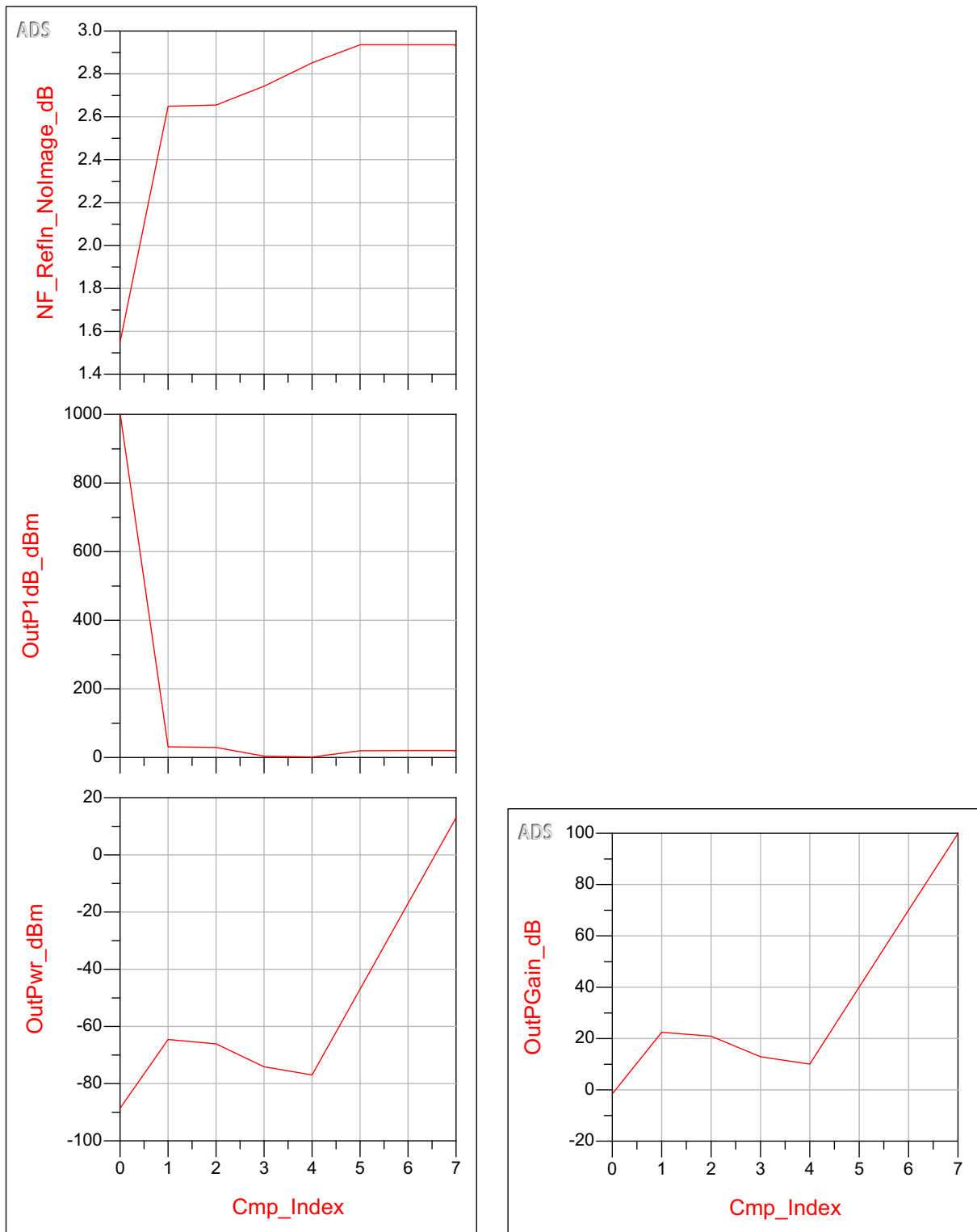


Figure 1.14: Budget simulation results in plots

In case of a -20 dBm input signal level, using the measured gain, the output signal would be + 79.898 dBm which surpasses the output 1dB compression point of the receiver. Therefore, for a -20 dBm input signal, the use of an AGC is mandatory to avoid the compression of the receiver by lower the gain of the RF or IF stages.

1.4 Spurious responses Simulation

A spurious response of a receiver refers to an unwanted, extraneous signal that is received by the receiver and interpreted as a valid signal. These signals are often generated by various sources, such as other transmitters operating on nearby frequencies, atmospheric or environmental conditions, or the internal components of the receiver itself.

Spurious responses can cause interference, distortion, and even complete loss of the desired signal. They are typically characterized by their frequency, amplitude, and phase, and can be minimized through careful design and testing of the receiver. Various techniques are used to mitigate spurious responses, including filtering, shielding, and frequency tuning. In this receiver, the second RF filter (RF_Filter2 in all the schematics) was added to attenuate the self-mixing of the LO due the finite LO to RF rejection.

1.4.1 $f_{IF} = 618 \text{ MHz}$

In this section, the receiver chain schematic from the budget simulation was used except the mixer model. The frequency of the local oscillator signal of the receiver is set to a value that is used if the receiver receives a useful signal at the center frequency of the system frequency band. The maximum level for the input signals causing spurious responses is -45 dBm. Performance criterion for the spurious responses at the output of the IF-stage must be below -95 dBm. For the spurious response test shown in Figure 1.15, three frequencies were used, $f_{IF} = 618 \text{ MHz}$, $f_{LO} = 9.382 \text{ GHz}$, and RF frequency was set at the image $f_{Image} = f_{LO} - f_{IF} = 8.764 \text{ GHz}$. In addition, the center frequency of the IF filter was changed to 200 MHz.

As shown in Figure 1.16, all the spurious responses are well below the specified -95 dBm and with lower spurious tones than the previous test.

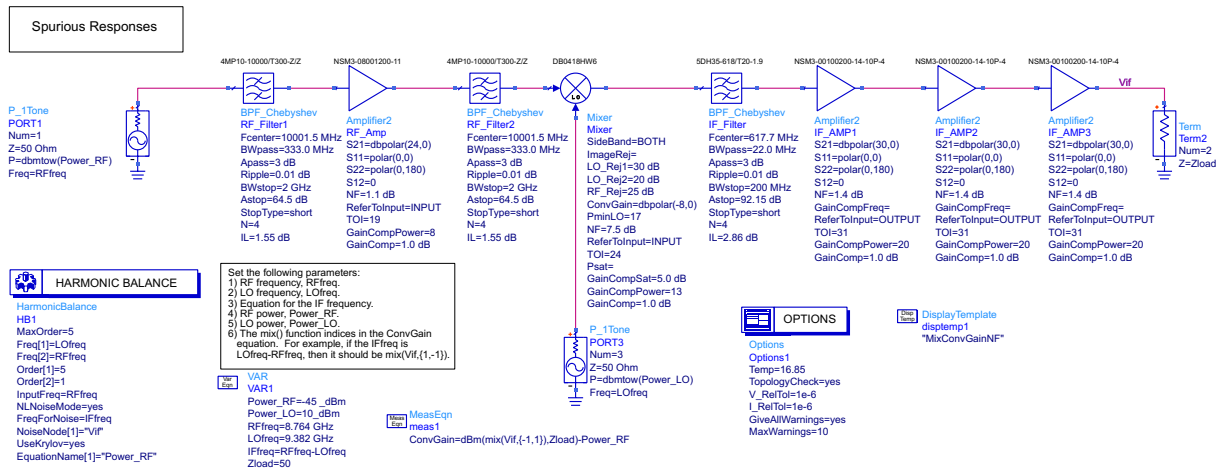


Figure 1.15: Spur simulation schematic with $f_{IF} = 618 \text{ MHz}$

1.4.2 $f_{IF} = 200 \text{ MHz}$

In this section, the previous spurious response test is repeated with a 200 MHz IF frequency. For the test shown in Figure 1.17, three frequencies were used, $f_{IF} = 200 \text{ MHz}$, $f_{LO} = 9.8 \text{ GHz}$,

Eqn IF_spectrum=dBm(HB.Vif)

freq	Mix		freq	IF_spectrum
	Mix(1)	Mix(2)		
0.0000 Hz	0	0	0.0000 Hz	-433.061
618.0 MHz	1	-1	618.0 MHz	-103.484
8.764 GHz	0	1	8.764 GHz	-327.454
9.382 GHz	1	0	9.382 GHz	-137.534
10.00 GHz	2	-1	10.00 GHz	-430.976
18.15 GHz	1	1	18.15 GHz	-341.321
18.76 GHz	2	0	18.76 GHz	-185.312
19.38 GHz	3	-1	19.38 GHz	-441.984
27.53 GHz	2	1	27.53 GHz	-405.474
28.15 GHz	3	0	28.15 GHz	-426.257
28.76 GHz	4	-1	28.76 GHz	-427.420
36.91 GHz	3	1	36.91 GHz	-434.677
37.53 GHz	4	0	37.53 GHz	-306.962
46.29 GHz	4	1	46.29 GHz	-430.089
46.91 GHz	5	0	46.91 GHz	-436.783

Spectrum at IF port, dBm

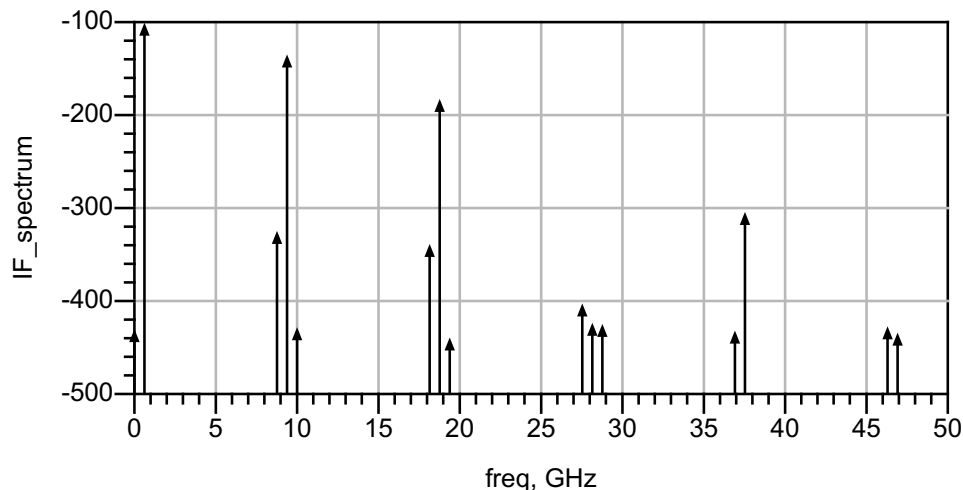
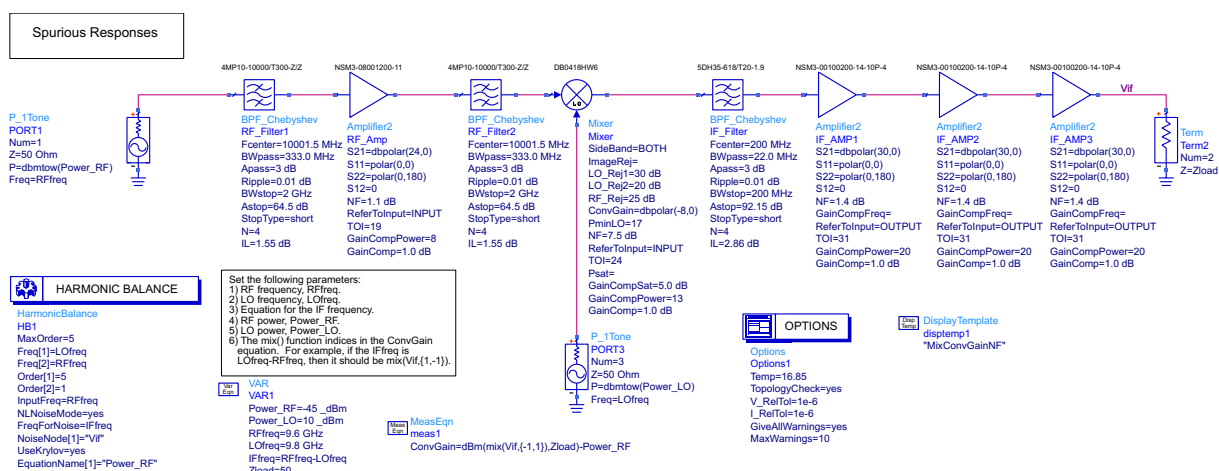


Figure 1.16: Spur simulation results with $f_{IF} = 618\text{MHz}$

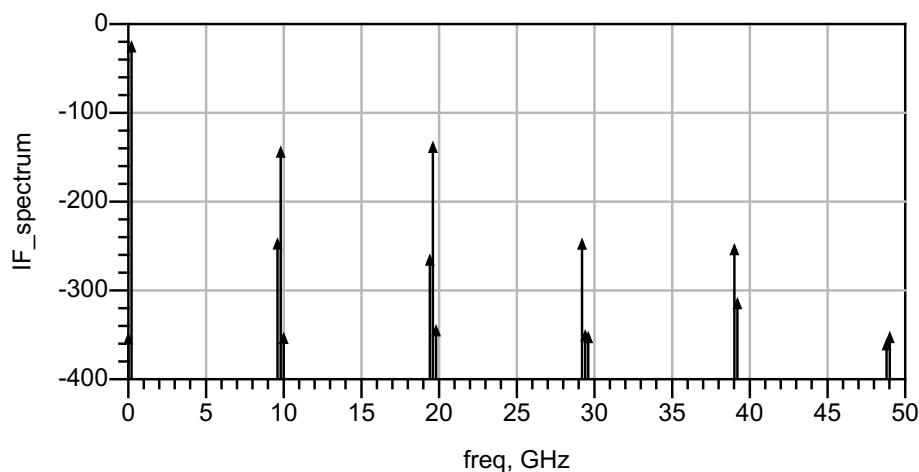
and RF frequency was set at the image $f_{Image} = f_{LO} - f_{IF} = 9.6\text{GHz}$.

As shown in Figure 1.18, not the spurious responses are below the specified -95 dBm. The result IF signal at 200 MHz is setting at -20.806 dBm. By reducing the IF frequency, the frequency of the image spur will also be reduced and it will fall closer to the RF signal. Thus, the image signal will be closer to the center of the RF filters and it will experience less attenuation and it would fold/mix down with the LO and resulting in a higher IF spur.

Figure 1.17: Spurious responses simulation schematic with $f_{IF} = 200\text{MHz}$

freq	Mix		freq	IF_spectrum
	Mix(1)	Mix(2)		
0.0000 Hz	0	0	0.0000 Hz	-349.623
200.0 MHz	1	-1	200.0 MHz	-20.806
9.600 GHz	0	1	9.600 GHz	-242.782
9.800 GHz	1	0	9.800 GHz	-139.376
10.00 GHz	2	-1	10.00 GHz	-349.237
19.40 GHz	1	1	19.40 GHz	-261.000
19.60 GHz	2	0	19.60 GHz	-133.966
19.80 GHz	3	-1	19.80 GHz	-340.695
29.20 GHz	2	1	29.20 GHz	-242.978
29.40 GHz	3	0	29.40 GHz	-346.153
29.60 GHz	4	-1	29.60 GHz	-348.110
39.00 GHz	3	1	39.00 GHz	-249.163
39.20 GHz	4	0	39.20 GHz	-310.074
48.80 GHz	4	1	48.80 GHz	-356.302
49.00 GHz	5	0	49.00 GHz	-348.274

Spectrum at IF port, dBm

Figure 1.18: Spurious responses simulation results with $f_{IF} = 200\text{MHz}$

1.5 TOI Simulation

The third-order intercept point (IP3/TOI) is a measure of the linearity of a system or device, such as an amplifier or a mixer. It is defined as the point at which the third-order intermodulation products (IM3) generated by two input signals become equal in power to the fundamental signal. In other words, the third-order intercept point is the input power level at which the third-order intermodulation products generated by two input signals will be equal in power to the desired signal. A higher third-order intercept point indicates better linearity and less distortion in the device.

Two input signals both at level -40 dBm are fed to the input connector of the antenna. The frequency of the first signal is 10 MHz and the frequency of the second signal is 20 MHz away from the carrier frequency (10 GHz) so that they both are located on the same side in frequency band in respect to the carrier frequency. Hence the two input tones at the frequencies of 10010 MHz and 10020 MHz were used, as shown in Figure 1.19.

The result of the TOI, Figure 1.19 is shown in Figure 1.21 and the equations used to obtain these results are presented in Figure 1.20. The equations were configured to measure the high and lower TOIs by setting the input arguments to the *Mix* function (*TOIoutput_low* and *TOIoutput_high* equations). From the results, the output referred TOIs are 10.46 dBm which meets the specified +9 dBm. When referring the TOIs to the input, we get 0 dBm.

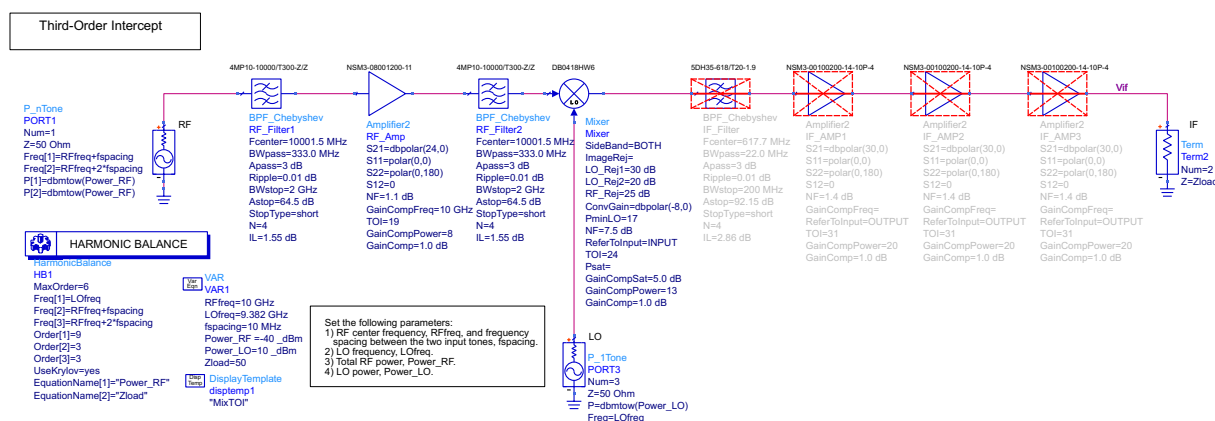


Figure 1.19: TOI simulation schematic

1.6 Extra tasks

1.6.1 TOI measurements with the IF filter

In this tasks, the TOI simulation is performed again but with the IF filter being enables as shown in Figure 1.22. All the measurement equations were kept as the previous TOI simulation, as presented in Figure 1.20.

The results of this TOI measurements are shown in Figure 1.23. When adding the IF filter, there's a noticeable decrease in the TOI (about 10 dB decrease).

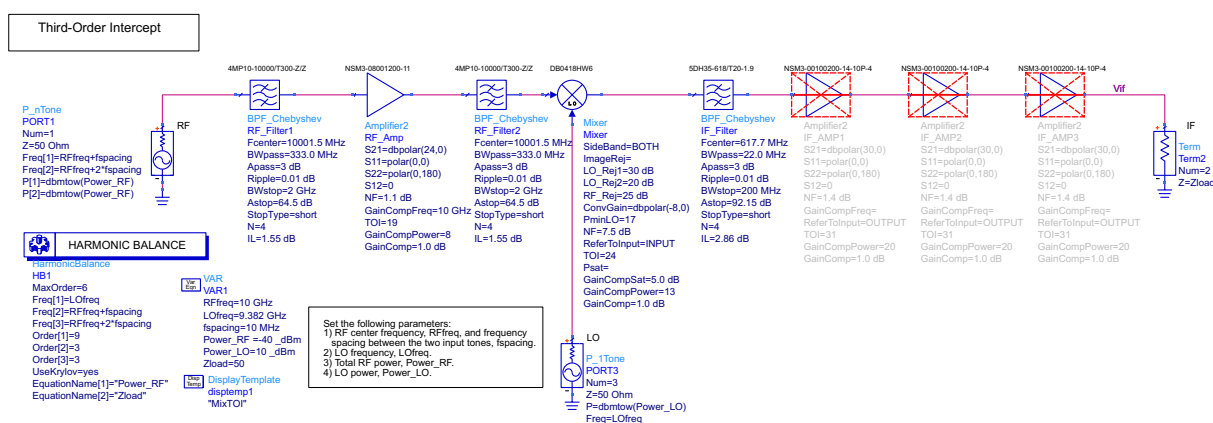


Figure 1.22: TOI simulation schematic with enabled IF filter

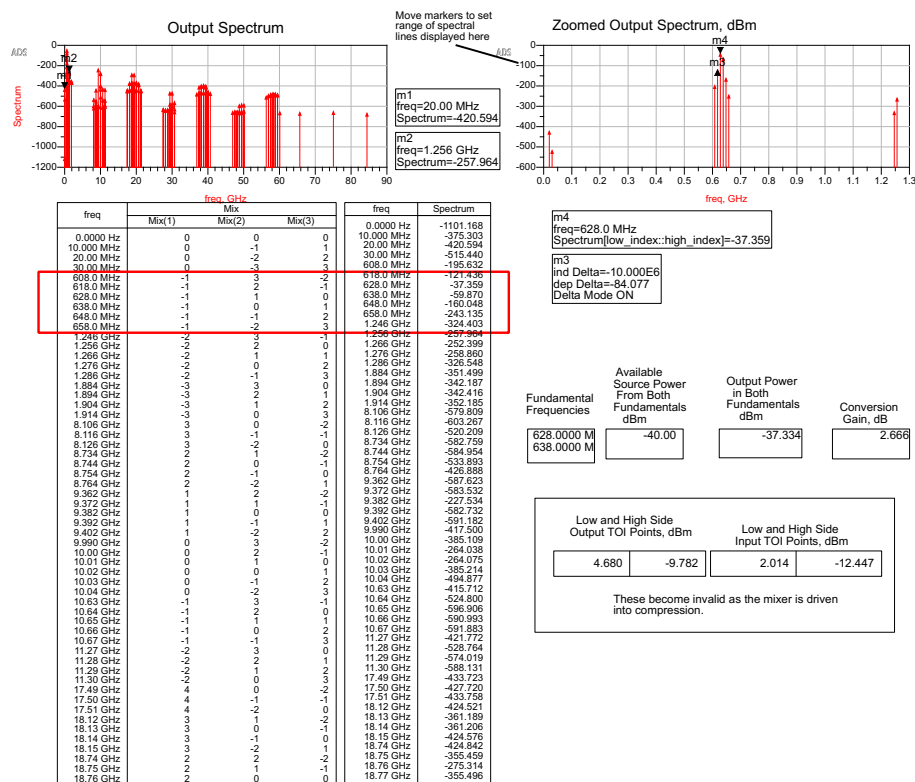


Figure 1.23: TOI simulation results with enabled IF filter

1.6.2 Equivalent noise bandwidth of the IF filter

In this task, the equivalent noise bandwidth of the IF filter is examined using the budget simulation. The simulation controller was configured to measure the noise bandwidth of the DUT, as shown in Figure 1.24.

The equivalent noise bandwidth is a measure of the effective noise bandwidth of the filter. It represents the bandwidth of an ideal rectangular filter that would produce the same level of noise power as the IF filter. The bandwidth obtained from the simulation results, shown in Figure 1.25 is 22.44 MHz which is a bit higher than the actual bandwidth of the IF filter.

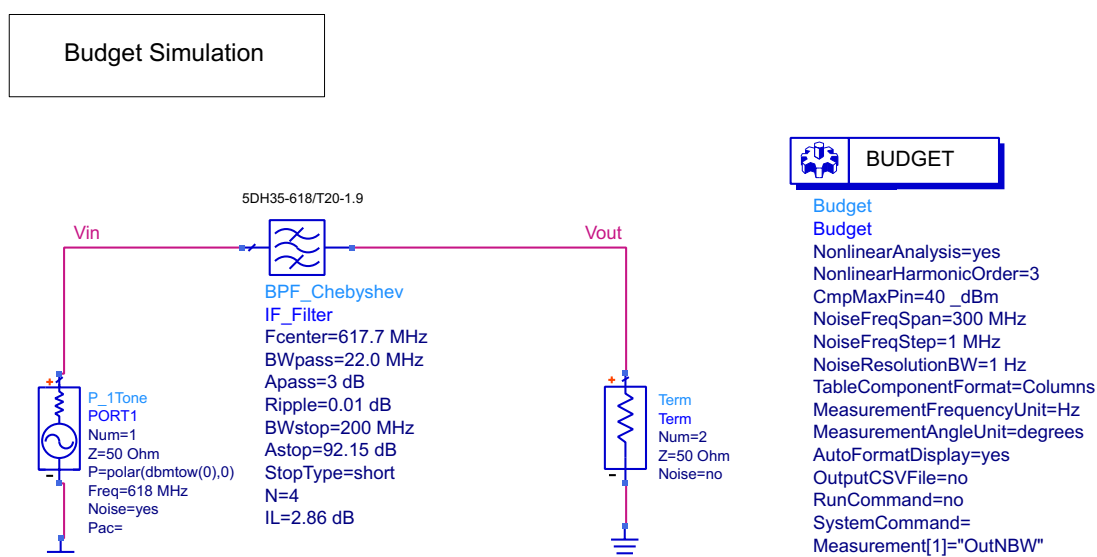


Figure 1.24: Budget simulation setup for the equivalent noise bandwidth of the IF filter

Meas_Name	IF_Filter
OutNBW	2.244E7

Setup_Name	Setup_Value
System_AnalysisType	1.000
System_NoiseResBW	1.000
System_NoiseSimBW	3.000E8
System_NoiseSimFStep	1.000E6
System_PilotFreq	6.180E8
System_PilotPwr_dBm	-6.268E-15
System_RefR	50.000
System_SourceFreq	6.180E8
System_SourcePwr_dBm	-6.268E-15
System_SourceTemp	25.000

System_Name	System_Value
SystemInN0_dBm	-173.855
SystemInNPwr_dBm	-89.083
SystemInP1dB_dBm	1000.000
SystemInSOI_dBm	1000.000
SystemInTOI_dBm	1000.000
SystemNF_dB	2.858
SystemOutN0_dBm	-173.914
SystemOutNPwr_dBm	-100.404
SystemOutP1dB_dBm	1000.000
SystemOutSOI_dBm	1000.000
SystemOutTOI_dBm	1000.000
SystemPGain_SS_dB	-2.860
SystemPGain_dB	-2.860
SystemPOut_dBm	-2.860
SystemS11_dB	-46.026
SystemS11_mag	0.005
SystemS11_phase	74.627
SystemS12_dB	-2.860
SystemS12_mag	0.719
SystemS12_phase	-5.899
SystemS21_dB	-2.860
SystemS21_mag	0.719
SystemS21_phase	-5.899
SystemS22_dB	-33.019
SystemS22_mag	0.022
SystemS22_phase	0.673

Figure 1.25: Budget simulation results for the IF filter

2 Transmitter Design

2.1 Transmitter specifications

To begin, the different system specifications of the receiver should be determined.

The main specifications of the receiver were given as:

- Data rate 32 Mbit/s
- Symbol rate 16 Msym/s.
- Center frequency of the system tuning range 10 GHz.
- Bandwidth of the main lobe between first nulls of the pulse shaped signal 20 MHz.
- Transmitter uses square-root raised cosine pulse shaping with the roll-off factor $\alpha = 0.25$.
- The system has 15 channels.
- ACPR of the transmitted signal must be at least -52 dBc in the channels adjacent to the main channel.
- Power of the useful signal must be at least +9 dBm.
- The rms-value of the error vector (EVM) must be under 5 %.

Similar to the receiver, the total bandwidth of the transmitter is

$$BW = \text{Number of channels} \times BW_{\text{Single Channel}} \implies BW = 300\text{MHz}$$

The roll off factor α is a parameter used in the design of the Square Root Raised Cosine pulse shaping filter, which is commonly used in digital communication systems to reduce the bandwidth of transmitted signals. The roll off factor determines the shape of the filter's frequency response, particularly how quickly the filter's frequency response rolls off to zero as the frequency increases. A higher roll off factor leads to a faster roll off, which results in a narrower bandwidth for the filtered signal, but also causes greater inter-symbol interference (ISI) between adjacent symbols. On the other hand, a lower roll off factor leads to a slower roll off, which results in a wider bandwidth for the filtered signal, but also reduces ISI. In general, the choice of roll off factor depends on the specific requirements of the communication system, such as the desired spectral efficiency, noise immunity, and channel characteristics. A typical value for the roll off factor in a communication system using SRRC pulse shaping is between 0 and 1, with higher values resulting in more aggressive filtering and lower values resulting in less aggressive filtering. The roll-off factor α of all SRRC filters in the transmitter is calculated as follows:

$$BW_{\text{Channel}} = \frac{1 + \alpha}{T_{\text{Symbol}}} \implies \alpha = BW_{\text{Channel}} T_{\text{Symbol}} - 1 = \frac{BW_{\text{Channel}}}{\text{Symbol Rate}} - 1 = 0.25$$

2.2 Transmitter components selection

2.2.1 RF Amplifier

The transmitter amplifier is mostly optimized for high linearity and high output power. Accordingly, the power amplifier is needed to be selected to ensure a high linearity at high transmit power.

The amplifier with the part number [LNA-40-08001200-30-20P](#) is selected as the transmit amplifier PA as it meets all the required transmitter specifications (system bandwidth and frequency, high P_{1dB} and gain), as shown in Figure 2.1. Its TOI was set 11 dB higher than its 1dB compression to ensure the Convergence of the amplifier Chebyshev polynomial model. The PA has a metallic package with a connectorized interface for the input and output as shown in Figure 2.2.

Model	Quote	Freq Min (MHz)	Freq Max (MHz)	Gain Min (dB)	Gain Flatness (dB+/-)	Noise Figure (dB)	Noise Temperature (K)	Input VSWR	Output VSWR	P1dB Out (dBm)	IP3 Out Typ (dBm)	V1 Nom. (V)	Curr1 Nom (mA)	Data sheet
LNA-40-08001200-30-20P	Quote	8000	12000	40	1.5	3	0	2	2	20		15	325	PDF

Figure 2.1: PA manufacturer's specifications



Figure 2.2: PA package

2.3 ACPR Test

ACPR stands for "Adjacent Channel Power Ratio", which is a measure of the amount of power that is emitted by a communication system in a frequency band adjacent to the primary frequency band. It is a critical parameter in wireless communication systems, particularly those that operate in frequency bands that are closely spaced. The ACPR is calculated by comparing the total power in a specified adjacent channel to the total power in the primary channel. It is typically expressed in decibels (dB) and is defined as the ratio of the power in the adjacent channel to the power in the primary channel, with a specified offset frequency.

In this transmitter, the ACPR schematic (test bench) shown in Figure 2.3 is configured to

transmit as per the system specifications by setting the different variables and parameters in the Envelope simulation controller and other blocks.. The test bench simulates two paths. First, the ideal/undistorted path with only transmit and receive side filtering and is used to determine the ideal EVM and constellation points. Second, the real path with transmit and receive side filtering and the PA parametrized model, and this path is divided in the receive side into the lower, main, and upper channels. The available source power was set to -30 dBm in this simulation (see section 2.4 for details).

The results of the ACPR simulation are shown in Figures 2.4, 2.5, and 2.6 with the modified measurement expressions. The main channel power is 9.673 dBm which meets the specified minimum 9 dBm. All the ACPR measurements meets the specified maximum -52 dBc, since the upper and lower transmit ACPRs are -53.5 dBc and the lower and upper receive ACPRs are -57 dBc. There's about 4.5 dB of difference when including the transmit filtering.

Amplifier ACPR, Constellation, and EVM Simulation with an Input Signal with PI/4 DQPSK Modulation

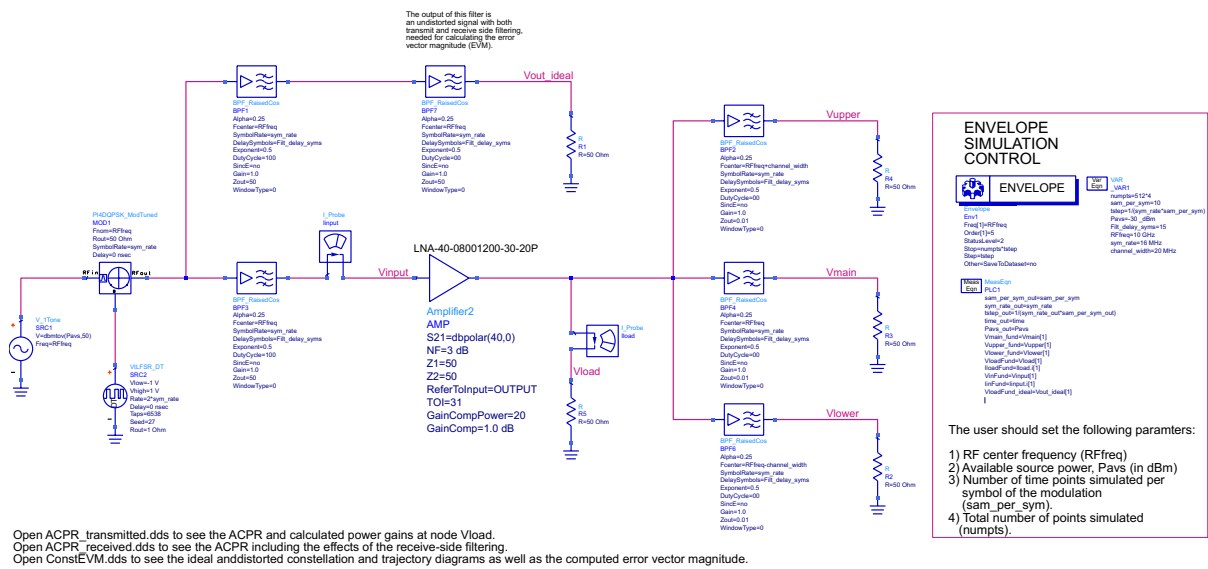


Figure 2.3: ACPR test schematic with a single valued of P_{avS}

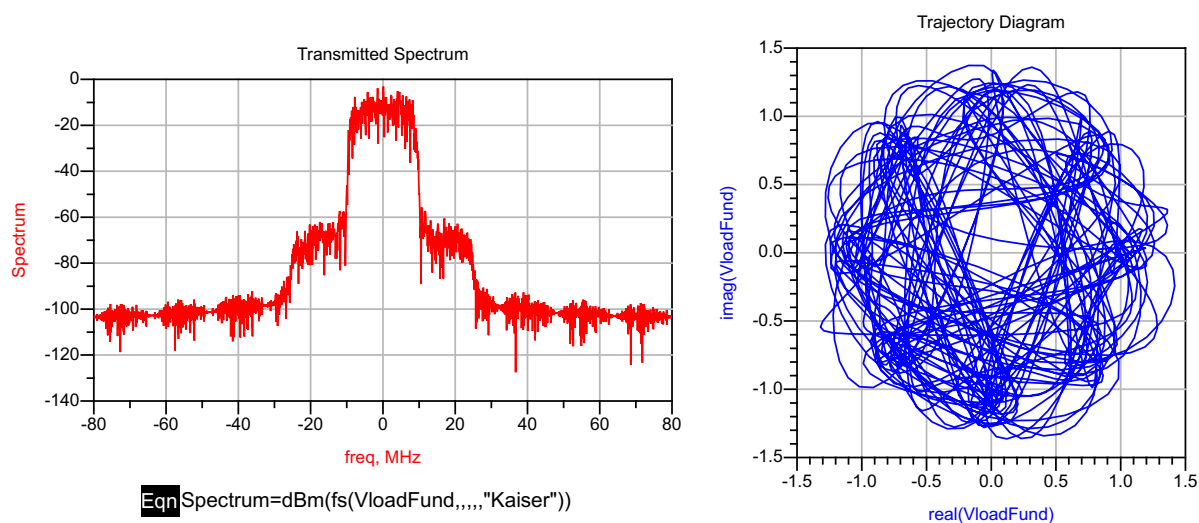
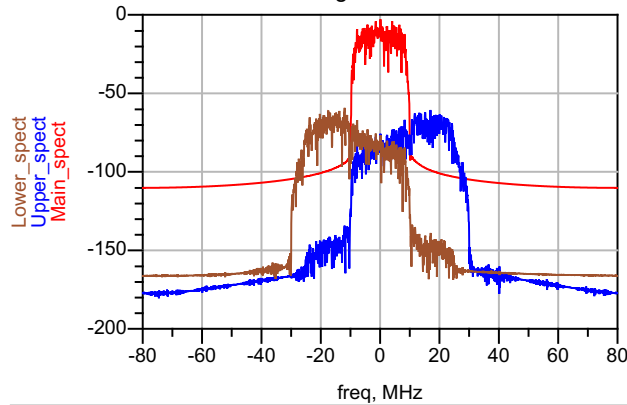


Figure 2.4: ACPR simulation results without receive side filtering effects

```
Eqn Main_spect=dBm(fs(Vmain_fund,,,,,"Kaiser"))
Eqn Upper_spect=dBm(fs(Vupper_fund,,,,,"Kaiser"))
Eqn Lower_spect=dBm(fs(Vlower_fund,,,,,"Kaiser"))
```

Upper adjacent channel, Main channel, and Lower adjacent channel spectra, after receive-side filtering



This data display computes the upper and lower adjacent-channel power ratios when the receive-side filtering present in the system is included. In this case, the upper adjacent channel power ratio is computed as the ratio of powers at the output of two filters, one centered on the upper adjacent channel and the other on the main channel. The lower adjacent channel power ratio is computed similarly. This is why three different named nodes, Vmain, Vupper, and Vlower are used in the calculation.

See the ACPR transmitted.dds file to see the ACPR of the transmitted signal (without including the receive-side filtering). That data display file also has power gain calculations. See the ConstEVM.dds file to see the constellation diagrams and EVM calculation.

Adjacent-channel power calculations:

```
Eqn mainlimits={-10 MHz,10 MHz}
Eqn UpChlimits={mainlimits+20 MHz}
Eqn LoChlimits={mainlimits-20 MHz}
Eqn Main_Ch_power=channel_power_vr(Vmain_fund,50,mainlimits,"Kaiser")
Eqn Upper_Ch_power=channel_power_vr(Vupper_fund,50,UpChlimits,"Kaiser")
Eqn Lower_Ch_power=channel_power_vr(Vlower_fund,50,LoChlimits,"Kaiser")
Eqn ACPRupper=10*log(Upper_Ch_power/Main_Ch_power)
Eqn ACPRlower=10*log(Lower_Ch_power/Main_Ch_power)
```

ACPRupper	ACPRlower
-57.530	-57.014

Main Channel Power in dBm

$10 \cdot \log(\text{Main_Ch_power}) + 30$
9.673

Figure 2.5: ACPR simulation results with receive side filtering effects

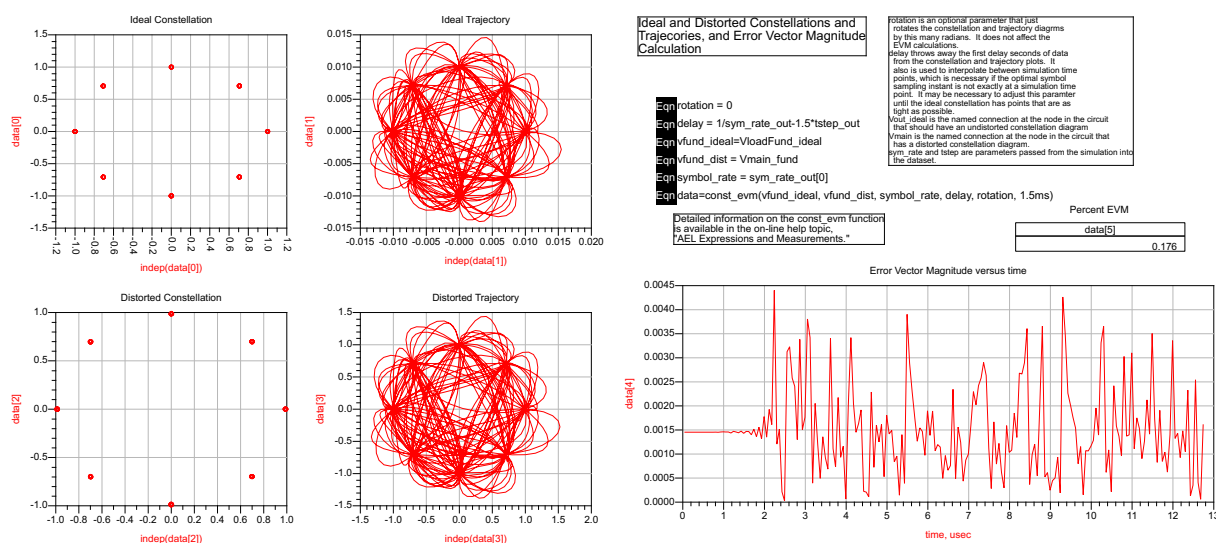


Figure 2.6: Constellation and EVM results of the ACPR simulation

2.4 ACPR Sweep Test

To determine the available source power (P_{avs}) that meets the specifications the provided ACPR schematic is modified to sweep P_{avs} , as shown in Figure 2.7. The -30 dBm value of P_{avs} meets the specifications as shown in Figure 2.8.

The backoff needed at the power amplifier in order to fulfill the requirements could be calculated. The saturated power of the amplifier could be extracted from Figure 2.8 and markers m3 and m4 and it is $P_{sat,out} = 22.575\text{dBm}$ (could be approximated with $P_{1dB} = 20\text{dBm}$ of the PA) and $P_{sat,in} = -15\text{dBm}$. The operating power is $P_{avs} = -30\text{dBm}$, therefore the back off is 15 dB.

$$\text{Back off} = P_{sat,in} - P_{avs} = 15\text{ dB}$$

Amplifier ACPR, Constellation, and EVM Simulation with an Input Signal with PI/4 DQPSK Modulation

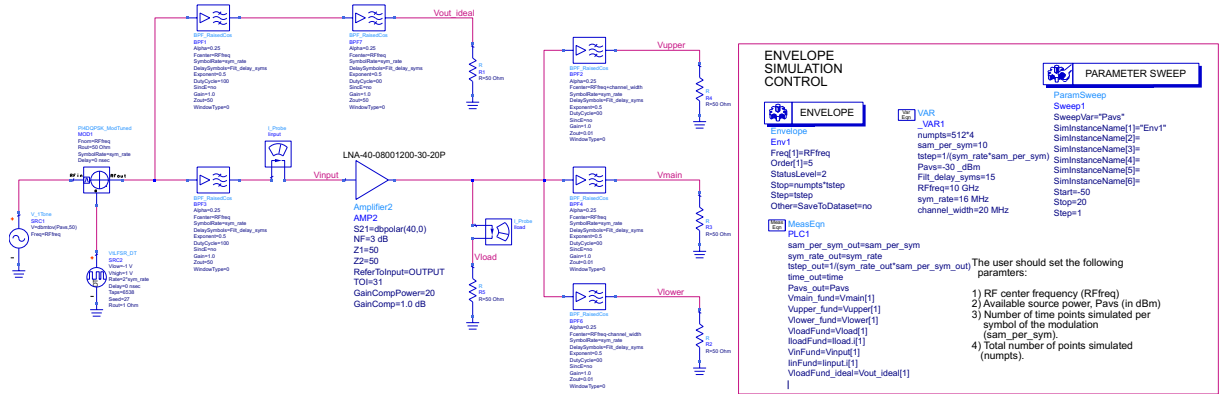
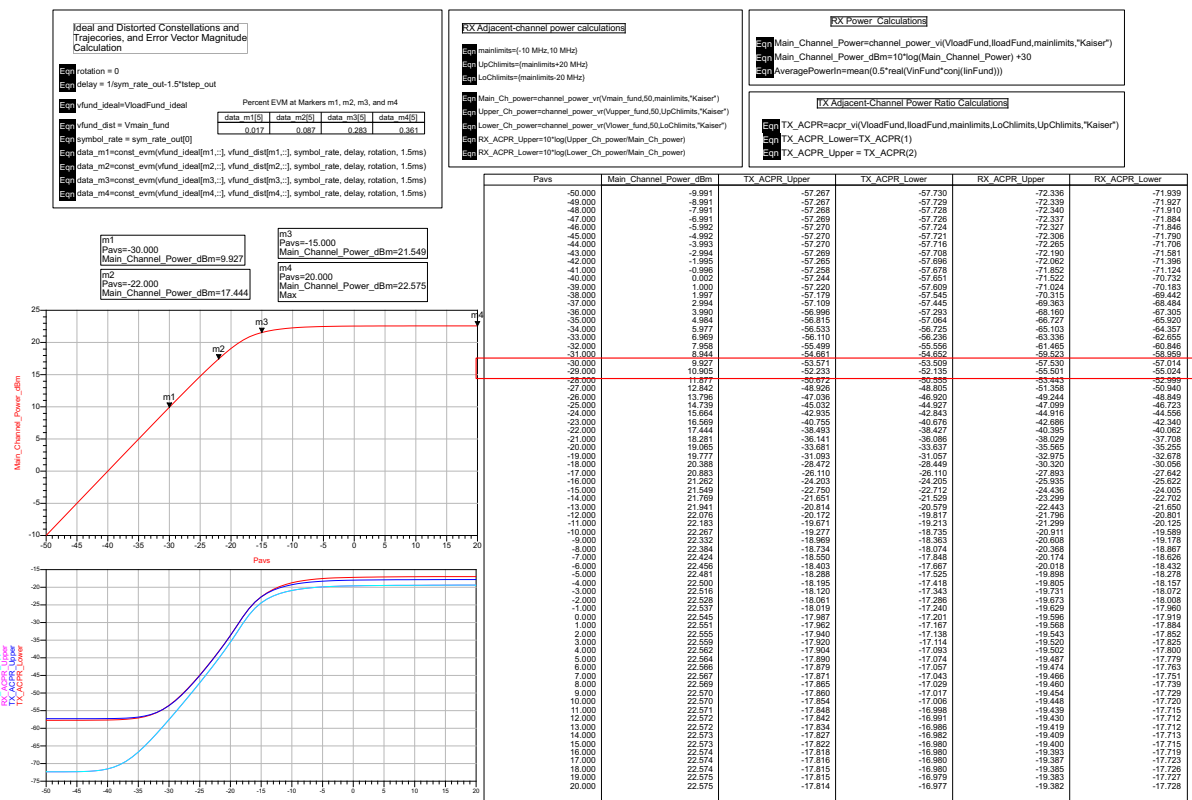


Figure 2.7: ACPR test schematic with sweeping P_{avs}

Figure 2.8: ACPR simulation results with sweeping P_{avs}

2.5 EVM Test

EVM (Error Vector Magnitude) is a measure of the accuracy of a digital radio communication system. It is a metric that quantifies the difference between a transmitted signal and the corresponding received signal, in terms of both amplitude and phase. EVM is typically expressed as a percentage or in decibels (dB) and represents the difference between the ideal and actual received signal. The ideal signal is the one that would be received if there were no errors in the communication system. A high EVM value indicates that there is a high level of distortion in the received signal, which can result in poor quality of the communication. In contrast, a low EVM value indicates that the received signal is very close to the ideal signal and that the communication system is operating with high accuracy.

In this transmitter, the ACPR schematic (test bench) shown in Figure 2.11 has been modified by adding a SAW filter and configured to transmit as per the system specifications by setting the different variables and parameters in the Envelope simulation controller and other blocks. In addition, the amplifier is ensured to operate in the linear region, by lowering the available source power to -60 dBm.

To determine the shape factor of the SAW filter, a sweep of the shape factor is made, as shown in Figure 2.9. The results of the sweep are presented in Figure 2.10. To have an acceptable EVM (less than 5%), the shape factor should be set to 6 or 7. Increasing the shape factor results in a better lower/better EVM but reduces the attenuation in the adjacent channels which defeats the purpose of adding the SAW filter.

The shape factor in the EVM test was set, as shown in Figure 2.11. The results of the test are presented in Figure 2.12. With the shape factor set to 7 the percentage EVM is 4.170 % which meets the specs.

Amplifier ACPR, Constellation, and EVM Simulation with an Input Signal with PI/4 DQPSK Modulation

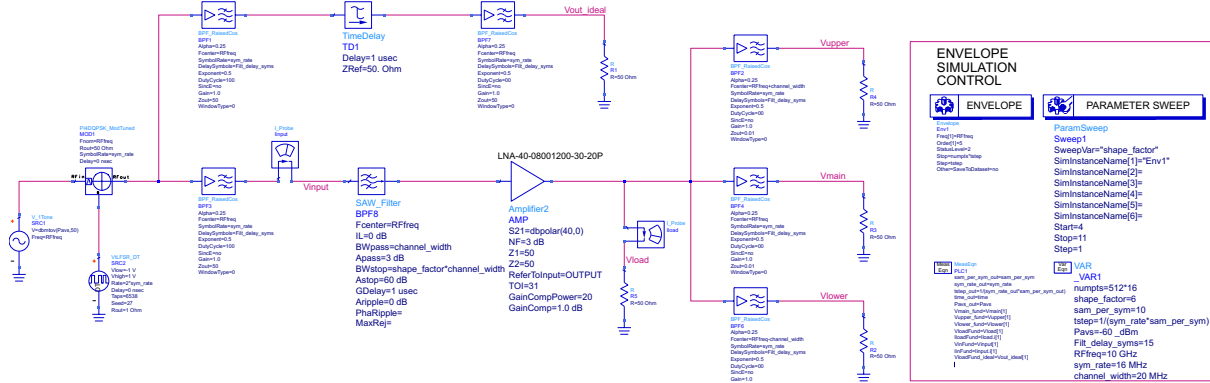


Figure 2.9: EVM test schematic with a sweep of the shape factor

Error Vector Magnitude Calculation

$$\text{Eqn rotation} = 0$$

$$\text{Eqn delay} = 1/\text{sym_rate_out} - 1.5 \cdot \text{tstep_out}$$

$$\text{Eqn vfund_ideal} = \text{VloadFund_ideal}$$

$$\text{Eqn vfund_dist} = \text{Vmain_fund}$$

$$\text{Eqn symbol_rate} = \text{sym_rate_out}[0]$$

$$\text{Eqn shape_factor_vals} = [4::1::11]$$

$$\text{Eqn data0} = \text{const_evm}(\text{vfund_ideal}[0,:], \text{vfund_dist}[0,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn data1} = \text{const_evm}(\text{vfund_ideal}[1,:], \text{vfund_dist}[1,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn data2} = \text{const_evm}(\text{vfund_ideal}[2,:], \text{vfund_dist}[2,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn data3} = \text{const_evm}(\text{vfund_ideal}[3,:], \text{vfund_dist}[3,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn data4} = \text{const_evm}(\text{vfund_ideal}[4,:], \text{vfund_dist}[4,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn data5} = \text{const_evm}(\text{vfund_ideal}[5,:], \text{vfund_dist}[5,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn data6} = \text{const_evm}(\text{vfund_ideal}[6,:], \text{vfund_dist}[6,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn data7} = \text{const_evm}(\text{vfund_ideal}[7,:], \text{vfund_dist}[7,:], \text{symbol_rate}, \text{delay}, \text{rotation}, 1.5\text{ms})$$

$$\text{Eqn Percentage_EVM} = [\text{data0}[5], \text{data1}[5], \text{data2}[5], \text{data3}[5], \text{data4}[5], \text{data5}[5], \text{data6}[5], \text{data7}[5]]$$

shape_factor_vals	Percentage_EVM
4	6.401
5	5.508
6	4.761
7	4.170
8	3.701
9	3.322
10	3.012
11	2.754

Figure 2.10: EVM test results with a sweep of the shape factor

Amplifier ACPR, Constellation, and EVM Simulation with an Input Signal with PI/4 DQPSK Modulation

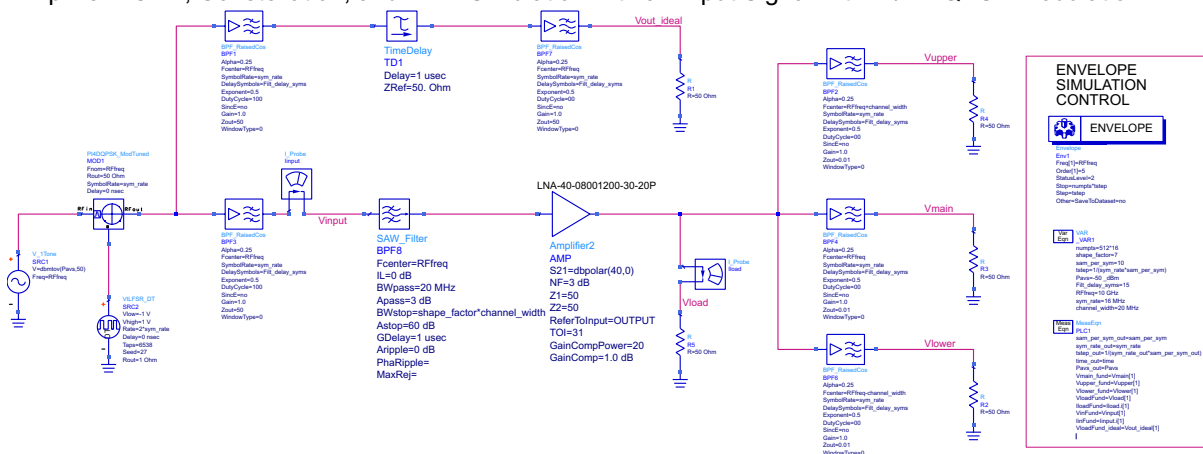


Figure 2.11: EVM test schematic with a single valued of the shape factor

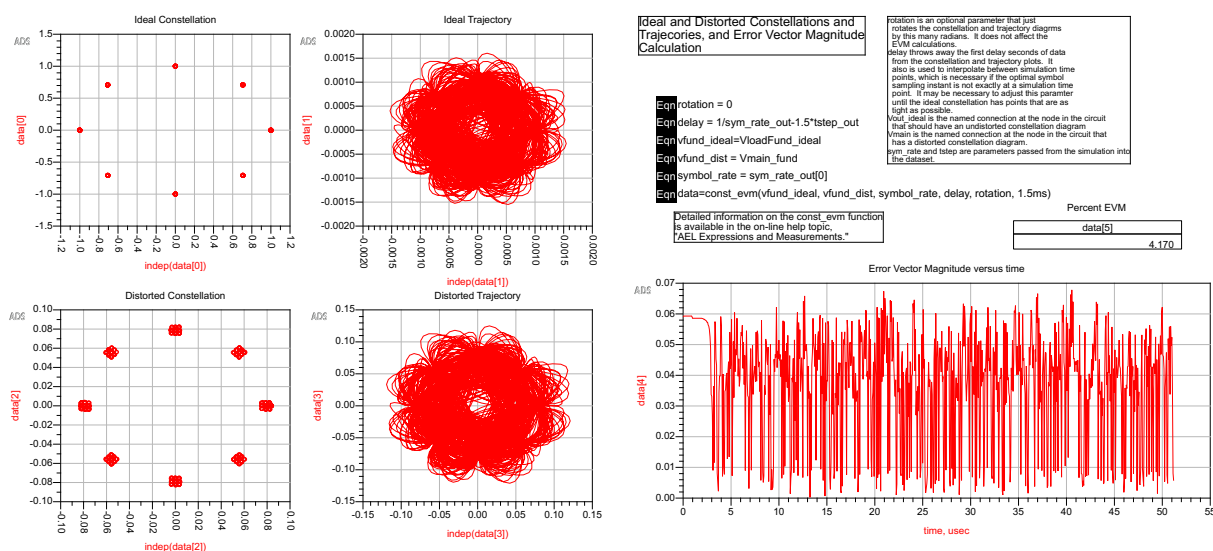


Figure 2.12: EVM test results with a single valued of the shape factor

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