

ADITYA INSTITUTE OF TECHNOLOGY AND MANAGEMENT

(AN AUTONOMOUS INSTITUTION)

EXAMINATION BRANCH

I M. TECH - II SEMESTER – (AR13 REGULATION)
REGULAR / SUPPLEMENTARY EXAMINATIONS, AUGUST-2016

T I M E T A B L E

Examination Timings 10.00 AM to 01.00 PM

Date & Day of Examination	SE	TE	DECS	PEED	VLSI System Design	CSE
03-08-2016 (WEDNESDAY)	Computer Applications and CAD (13MSE1012)	Energy Management (13MTE1012)	Wireless Communication & Networks (13MDE1004)	Power electronic control of AC drives (13MPE1010)	Algorithms for VLSI Design Automation (13MVL1010)	Computer Networks (13MCS1008)
05-08-2016 (FRIDAY)	Stability of Structures (13MSE1013)	Finite Element Analysis (13MTE1013)	Optical Communication (13MDE1005)	Flexible AC Transmission systems (13MPE1011)	Low Power VLSI Design (13MVL1011)	Object Oriented Analysis & Design (13MCS1009)
08-08-2016 (MONDAY)	Structural Dynamics and Earthquake Resistant Design (13MSE1014)	Computational Fluid Dynamics (13MTE1014)	Coding Theory & Practice (13MDE1006)	Advanced digital signal processing and applications (13MPE1012)	Design of Fault Tolerant Systems (13MVL1012)	Web Technologies (13MCS1010)
10-08-2016 (WEDNESDAY)	Pre-Stressed Concrete (13MSE1015)	Thermal & Nuclear Power Plants (13MTE1017)	System Modeling & Simulation (13MVL1013)	Analysis of Dynamic Systems (13MPE1013) Intelligent Control (13MPE1014)	System Modeling & Simulation (13MVL1013)	Mobile Computing (13MCS1011)
12-08-2016 (FRIDAY)	Design of substructures (13MSE1020)	Refrigeration & Air Conditioning (13MTE1019)	Image and Video Processing (13MDE1008)	Power Quality Management (13MPE1015)	CPLD & FPGA Architecture and Applications (13MVL1015)	Network Security and Cryptography (13MCS1016) Image Processing (13MCS1018)
16-08-2016 (TUESDAY)	Finite Element Method (13MSE1011)	Fuels, Combustion & Environment (13MTE1011)	DSP Processors and architecture (13MVL1009)	Switched mode power converters (13MPE1009)	DSP Processors and architecture (13MVL1009)	Data Warehousing And Data Mining (13MCS1007)

**Copy to: Director / Principal / Controller of the Examinations / HODs / Officer I/c, Examinations,
Dy. Director-Diploma / Estate Manager / In-charge, Transport**

07-07-2016

**Sd/-
PRINCIPAL**

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Branch	01-08-2016 (MONDAY)	03-08-2016 (WEDNESDAY)	05-08-2016 (FRIDAY)	08-08-2016 (MONDAY)	10-08-2016 (WEDNESDAY)	12-08-2016 (FRIDAY)
IT	Image Processing (13MIT1007)	Parallel Computing Architecture (13MIT1008)	Machine Learning (13MIT1009)	Data Mining & Knowledge Discovery (13MIT1010)	Information Security (13MIT1013)	Bio-Informatics (13MIT1016)

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