Module Guidelines

Speed Wiring

INTRODUCTION

Tap your inner 'nerd' and get ready to be blown away as we put your mind to test.

Digital Logic Design is a fun filled yet brain twisting competition that will turn your mind upside down .Speed wiring competition will be based on the knowledge of Basic electronic circuit analysis and digital logic design . The competition will consists of two rounds and each round is designed to assess not only your DLD skills but also your skills as a team. If you really think you have what it takes, you do not want to miss this.

PROCEDURE

Round 1 : Basic DLD circuits

Round 2 : Advance DLD circuits

Round 1:

In First round, all teams will be given a problem related to DLD and they have to

• Determination of Inputs and outputs

• Derive truth table

• Simplify the Boolean expression

• Draw Logic diagram

After theoretical work they have to patch it on bread board in a maximum time of 1 hour and 30 minutes. (time may vary)

Round 2:

In Second round, 5 best teams (depending upon turnout) selected will compete against each other for a different circuit with a maximum time of 1 hours.

This round will be similar to the first only the difficulty level will be increased.

RULES AND REGULATIONS

• Top positions will be decided on the basis of circuit working, neatness and time taken by the team to implement the circuit

• Participants have only one chance to get their circuit evaluated. There would be no second chance.

• Decision of the management would be final therefor the contestants cannot appeal against it.

• All teams must report at least 15 mints before the competition starts.

• If participant has any related queries, consult the management of the contest before the session starts as such queries will not be entertained during contest.

• Supplies, components and wires would be provided to the participants

• Conversation and sharing of any material between teams is strictly prohibited.