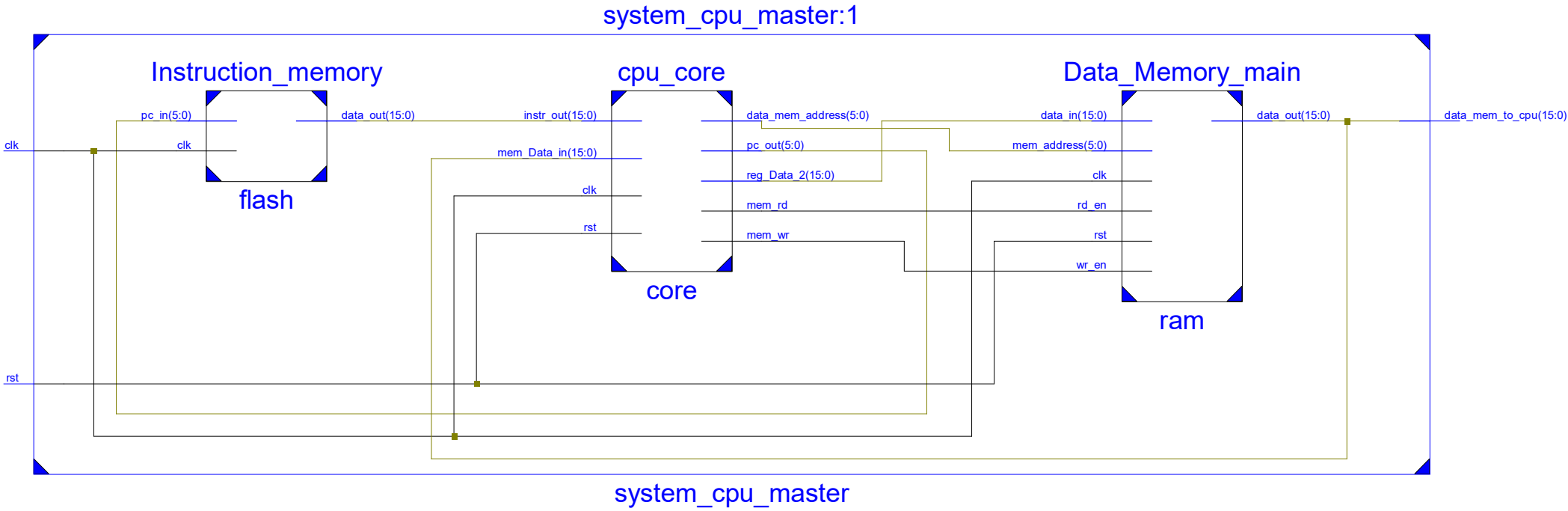
system_cpu_master:1
Instruction_memory
pc_in(5:0)
clk
clk
data_out(15:0)
flash
cpu_core
instr_out(15:0)
mem_Data_in(15:0)
clk
rst
data_mem_address(5:0)
pc_out(5:0)
reg_Data_2(15:0)
mem_rd
mem_wr
core
Data_Memory_main
data_in(15:0)
mem_address(5:0)
clk
rd_en
rst
wr_en
data_out(15:0)
ram
data_mem_to_cpu(15:0)
clk
rst
system_cpu_master