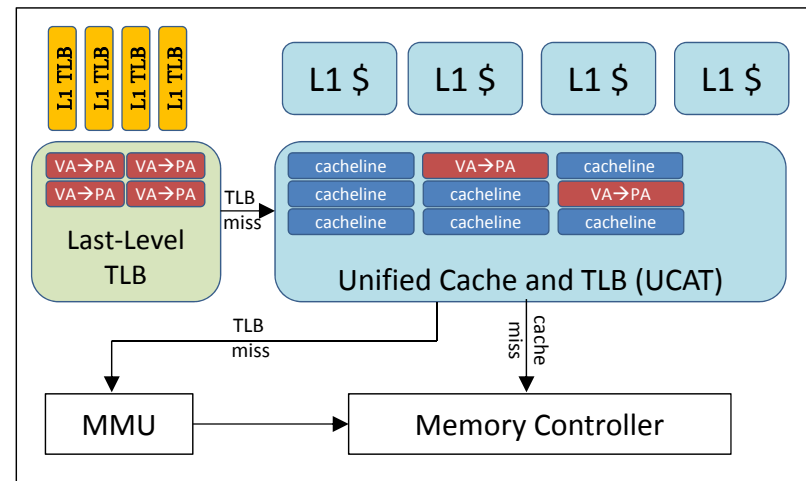
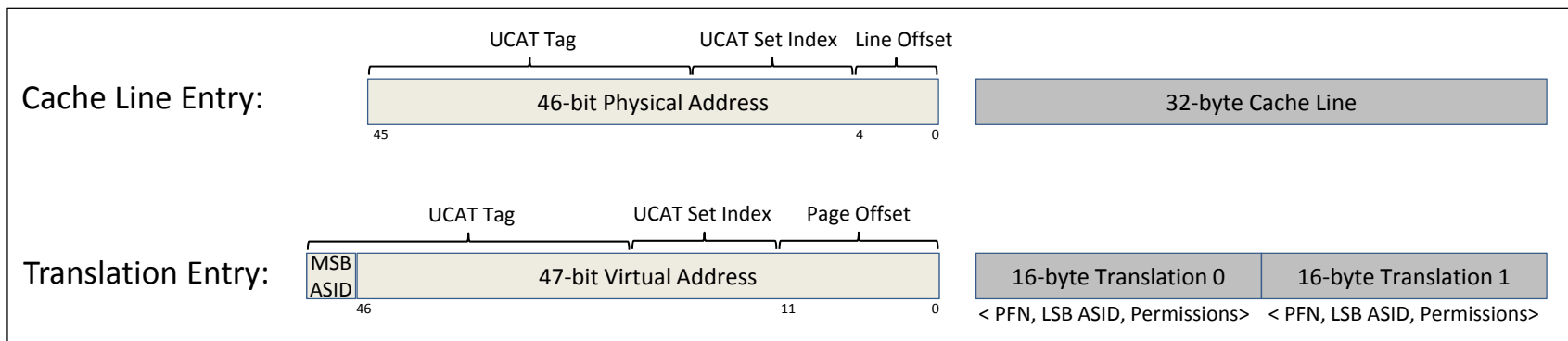


(a) Baseline



(b) Unified Cache and TLB (UCAT) Architecture



(c) Overview of UCAT Access and UCAT Entry