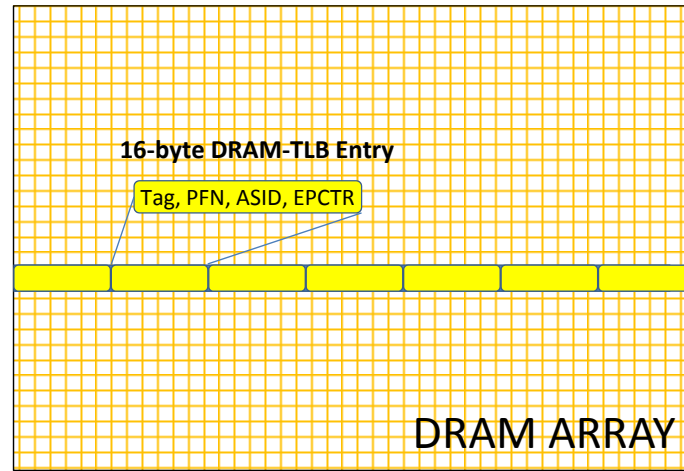
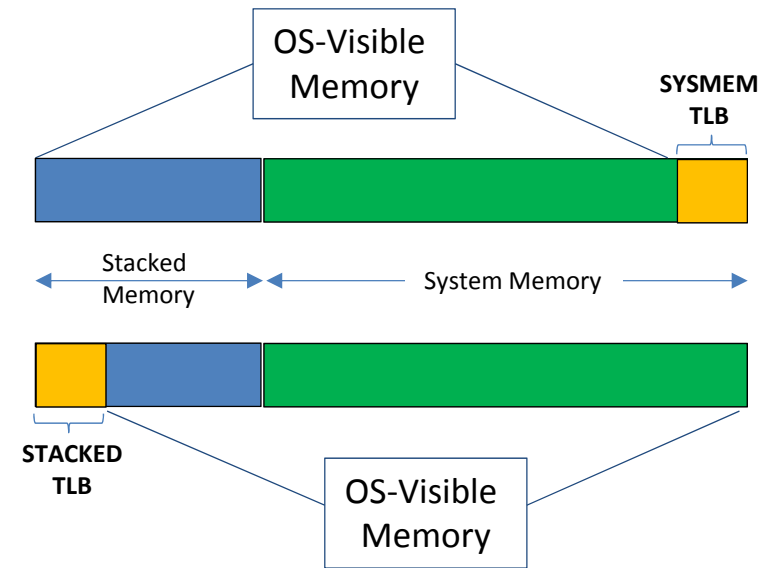


(a) DRAM-TLB Overview



(b) DRAM-TLB Architecture



(c) DRAM-TLB Placement