# COM SCI 132 Week 10

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June 3, 2024

# **Advanced Register Allocation**

### Chordal vs. Non-chordal graphs

Remember that register allocation can be written as a graph coloring problem where the color represents registers.

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Non-chordal graph:



Chordal graphs: (there are no 4(+)-cycles without chords)





• about 95% of interference graphs contain chords.

## **Graph Coloring Complexity**

Type of Graph	Graph Coloring Complexity
Interval graphs	polynomial time
$\subseteq$ Chordal graphs	polynomial time
$\subseteq$ General graphs	NP-complete

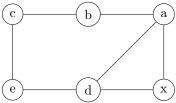
- For chordal and interval graphs, greedy coloring is optimal.
- Theorem: A graph G is chorded  $\iff$  G has a simplified elimination order.
  - This is good since most register allocation interference graphs are chorded.

### Example

Consider the following code:

```
int m(int x, a, d) {
                                     d e
                       l x a b c
    int b, c
                          if (x > 0) {
                          0
                            0
                                      1
       e = 0
                                     0
       c = d
                                        0
    } else {
                                        0
       b = 0
                             0
                                c = a
                                0
                                  1
    }
                                        return e + c
                                   0
                                        0
}
```

This code generates an interval graph (not chordal), since (a, b, c, e, d) is a 5-cycle without chords.

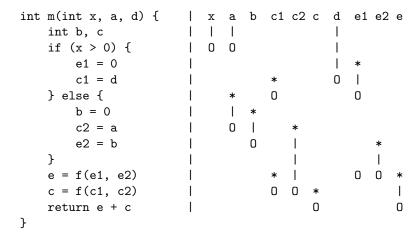


## Static Single Assignment (SSA)

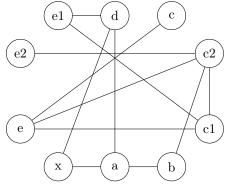
This form occurs when two different branches in a program assign to the same variable. Here are some examples:

 $\bullet$  GCC and LLVM compile to this form.

## Example 2



This code generates the following graph:



(This is a chordal graph.)

### Spilling

• Spilling is NP-complete even for chordal graphs

 ${\bf Compiler:}$ 

$$\begin{array}{c} \text{Source} \longrightarrow \text{SSA} \longrightarrow \text{RISC-V} \\ \uparrow \end{array}$$

Optimal register allocation in polynomial time

(We can't have optimal register allocation and optimal spilling in polynomial time)

### Graphs

Interval Graphs  $\subseteq$  Chordal Graphs  $\subseteq$  All undirected graphs

- 100% of procedures in SSA form have chordal interference graphs
- $\bullet$  95% of procedures have chorded interference graphs
- All of the procedures are possible as interference graphs

# Parallel Assignment

Consider Example 2. Instead of writing

write

$$c, e = f(c1, c2), f(e1, e2)$$

This actually reduces the complexity because by doing this, e and c1, c2 no longer exist at the same time.