

# AJAY MAURYA

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## PROFESSIONAL SUMMARY

Motivated Engineer with a B.Tech (ECE) and experience in developing applications and optimizing systems. Skilled in SQL, DSA, UI/UX designing, Embedded system. Eager to contribute enthusiasm, adaptability, and a fresh perspective to a dynamic team.

## SKILLS

**Programming Languages:** C, C++, MATLAB | **Web Development:** HTML, CSS, Framer Motion, Bootstrap | **Databases:** SQL | **Version Control:** Git | **Design Tools:** Figma, Adobe XD | **Simulation Tools:** Cadence, Multisim, Model Sim | **Embedded Systems:** Arduino

## EXPERIENCE

### Project Intern, DRDO, KANPUR

Aug 2023

- Developed MATLAB programs to calculate surface impedance in the X-band microwave region using Vector Network Analyzer (VNA) data.
- Plotted real and imaginary impedance as functions of varying reflection coefficients and substrate thicknesses.
- Applied advanced engineering concepts to research and analyze functional non-metallic materials.
- Analyzed material properties within the X-band frequency region through VNA measurements.

### Accenture Nordics Consultant Job Simulation on Forge

July 2023

- Completed a job simulation to improve a Healthcare client's mobile app.
- Identified key project priorities from the client brief.
- Analysed data to inform UX decisions.
- Managed client issues and prioritized long-term initiatives.

## EDUCATION

### Bachelor of Technology (Electronics and Communication Engineering)

Vellore Institute of Technology, Vellore, Tamil Nadu

Sept 2021-July 2025

**CGPA 9.06/10**

### Class 12<sup>th</sup> - CBSE

Avadh Collegiate, Lucknow

May 2020

**Percentage – 87.33%**

### Class 10<sup>th</sup> -UP board

Shakti Montessori High School, Lucknow

May 2018

**Percentage – 88.8%**

## PROJECTS

### Biomedical Operational Amplifier Optimization Using Machine Learning

Aug 2023 - Dec 2023

- Built an optimized Operational Amplifier design with the Improved **Golden Jackal Algorithm** for biomedical systems.
- Minimized circuit area while balancing noise and power, resulting in superior performance.
- Validated the design with a circuit area of **492  $\mu\text{m}^2$** , noise of **136 nV**, and power consumption of **6.11  $\mu\text{W}$**  using **Cadence Virtuoso**.

### Portfolio using React, Tailwind CSS & Framer Motion

May 2023

- Developed a **personal portfolio** using React for dynamic content.
- Used **Tailwind CSS** for responsive design and modern styling.
- Integrated **Framer Motion** to enhance user experience with fluid animations.

### Microwave Surface Impedance Measurement Using MATLAB

Aug 2023 – Sept 2023

- Measured surface impedance of resistive coatings at **microwave** frequencies using MATLAB.
- Developed MATLAB programs for computing and analyzing **impedance** characteristics.
- Plotted data to evaluate the performance and properties of coatings at various frequencies.

## ACHIEVEMENTS

- Participated** in Enigma8.0 By IEEE chapter.
- Participated** in YANTRA 2023: IGNITIA- HackA-Thon.
- Participated** in Indian Intelligence test(2016 season 3)
- Demonstrated Leadership** as a core member in web development and **UI/UX design** roles with IET VIT and VIT Stellar.
- High Academic Achievement (2021-2024):** Consistently maintained a CGPA of 9 or higher.

## CERTIFICATIONS

- Google **Cloud Computing** Foundations
- AWS Iot foundation: Telemetry
- Introduction to **Web Development**- by meta
- Ready, Set, Agile! Virtual Experience Program (**Cognizant**)
- Consultant Virtual Experience Program(**Accenture**)
- Intel: **AI** for All and **AI** aware