

### Spice deck for lower nodes(250nm technology)

\*\*\* MODEL Descriptions \*\*\*

\*\*\* NETLIST Description \*\*\*

M1 vdd n1 0 0 nmos W=0.375u L=0.25u

R1 in n1 55

Vdd vdd 0 2.5

Vin in 0 2.5

\*\*\* SIMULATION Commands \*\*\*

.op

.dc Vdd 0 2.5 0.1 Vin 0 2.5 0.5

\*\*\* .include mosis\_1um\_model.mod \*\*\*

.LIB "tsmc\_025um\_model.mod" CMOS\_MODELS

.end