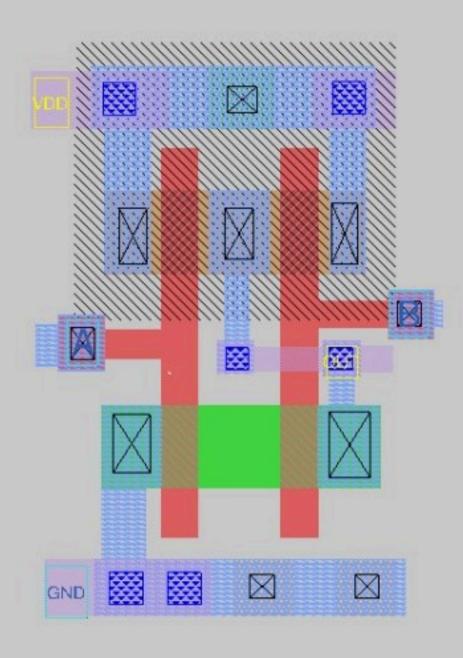
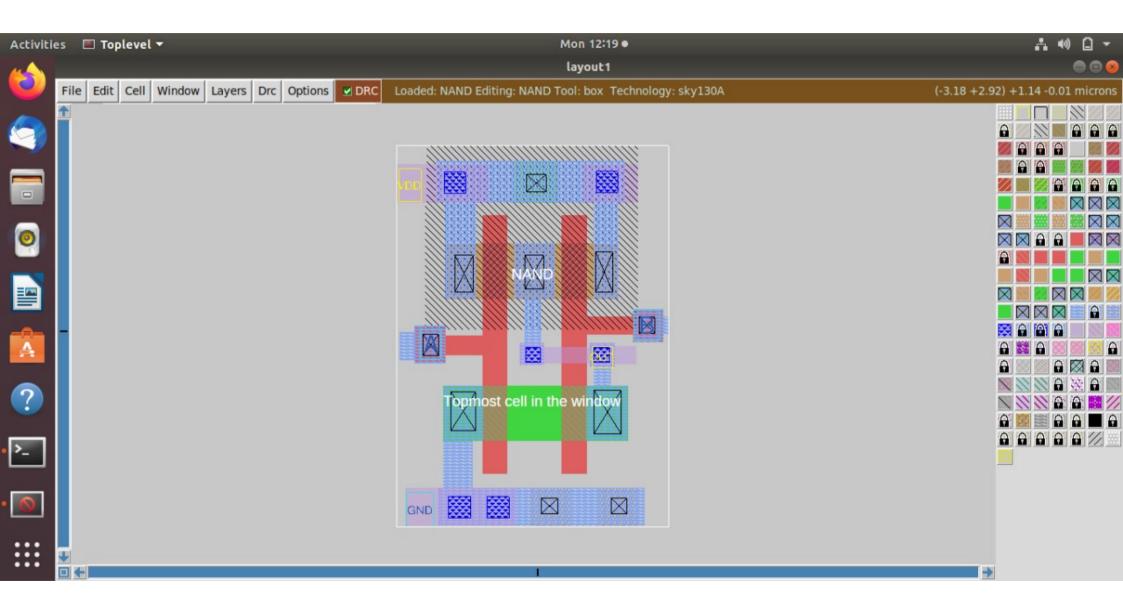
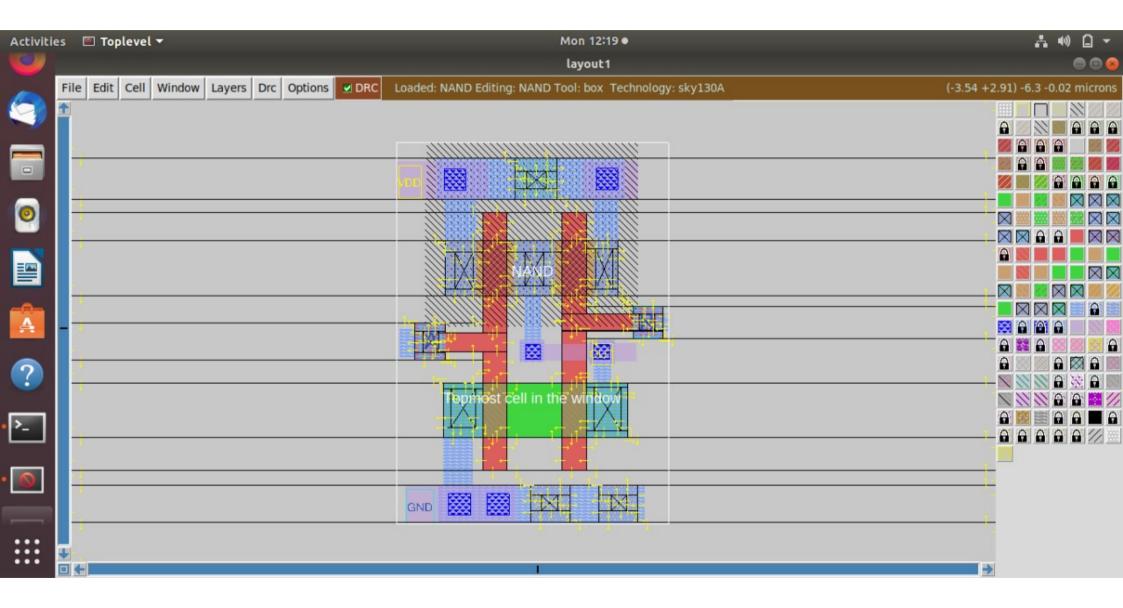
Layers Drc Options

■ DRC

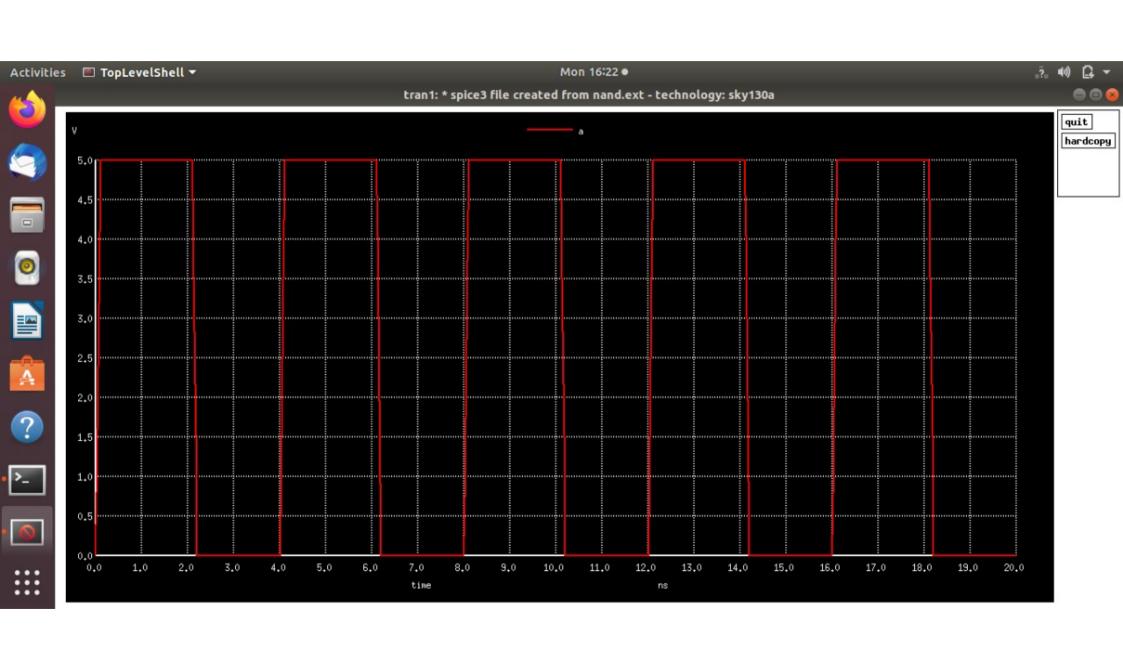
Loaded: NAND Editing: NAND Tool: box Technology: sky130A

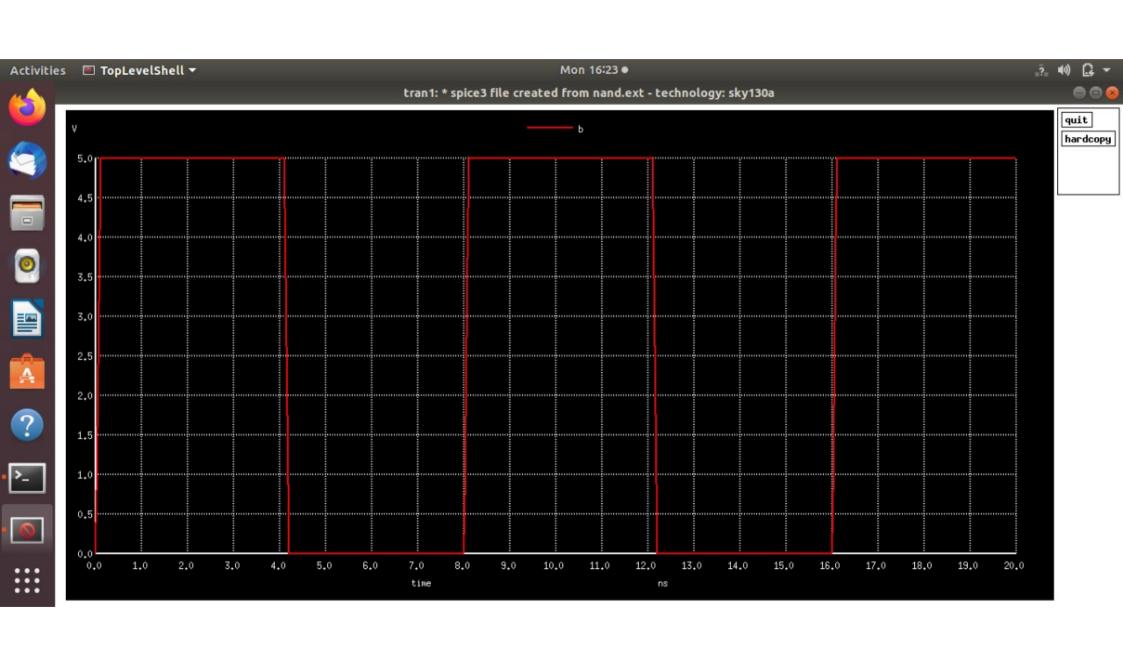


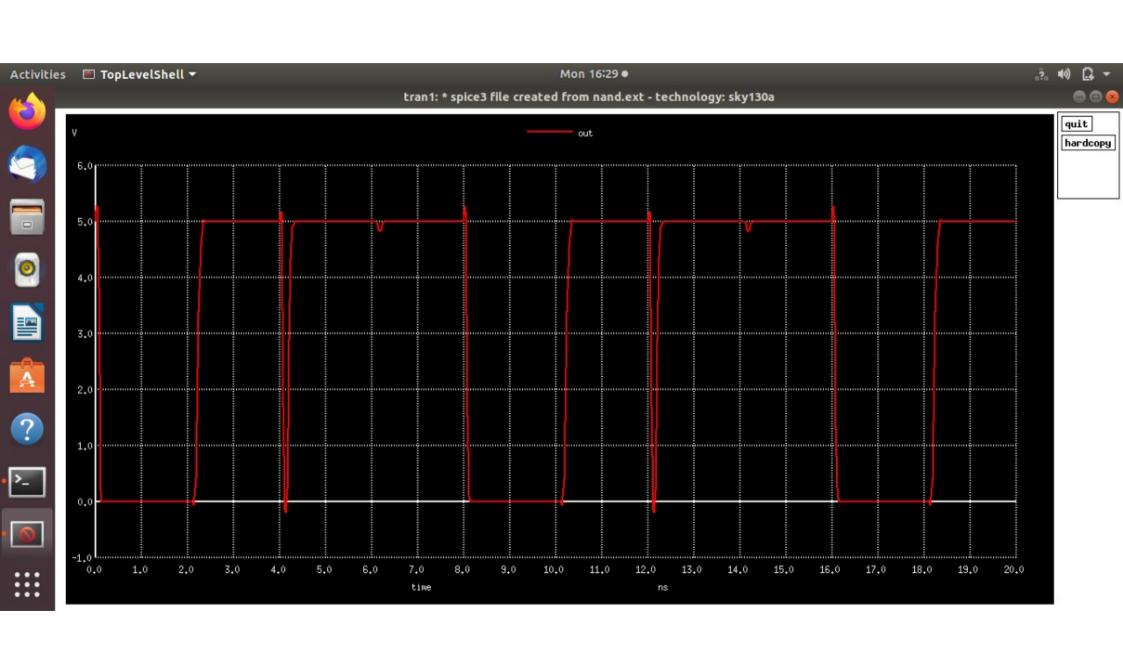




```
1 * SPICE3 file created from NAND.ext - technology: sky130A
 2 .option scale=0.01u
 3 //.subckt NAND B A GND
 4 .include ./libs/pshort.lib
 5 .include ./libs/nshort.lib
 6 M1000 OUT A VDD VDD pshort model.0 ad=1920 pd=124 as=2600 ps=208 w=62 l=27
 7 M1001 VDD B OUT VDD pshort model.0 ad=2170 pd=194 as=1920 ps=124 w=62 l=28
 8 M1002 OUT B a 21 n85# GND nshort model.0 ad=2180 pd=214 as=1890 ps=123 w=61 l=28
 9 M1003 a 21 n85# A GND GND nshort model.0 ad=1890 pd=123 as=2680 ps=210 w=61 l=27
10 VDD VDD 0 5V
11 VSS VSS 0 0
12 Va A VSS PULSE(0 5V 0 0.1ns 0.1ns 2ns 4ns)
13 Vb B VSS PULSE(0 5V 0 0.1ns 0.1ns 4ns 8ns)
14 CO OUT a 21 n85# 0.017f
15 C1 A B 0.0616f
16 C2 OUT B 0.0867f
17 C3 B VDD 0.0386f
18 C4 OUT A 0.0474f
19 C5 A VDD 0.0418f
20 C6 OUT VDD 0.178f
21 C7 B GND 0.496f
22 C8 A GND 0.53f
23 C9 a 21 n85# GND 0.016f **FLOATING
24 C10 OUT GND 0.279f **FLOATING
25 C11 VDD GND 1.08f **FLOATING
26 //.ends
27 .tran 1n 20ns
28 .control
29 run
30 .endc
31 .end
```







tran1: * spice3 file created from nand.ext - technology: sky130a

