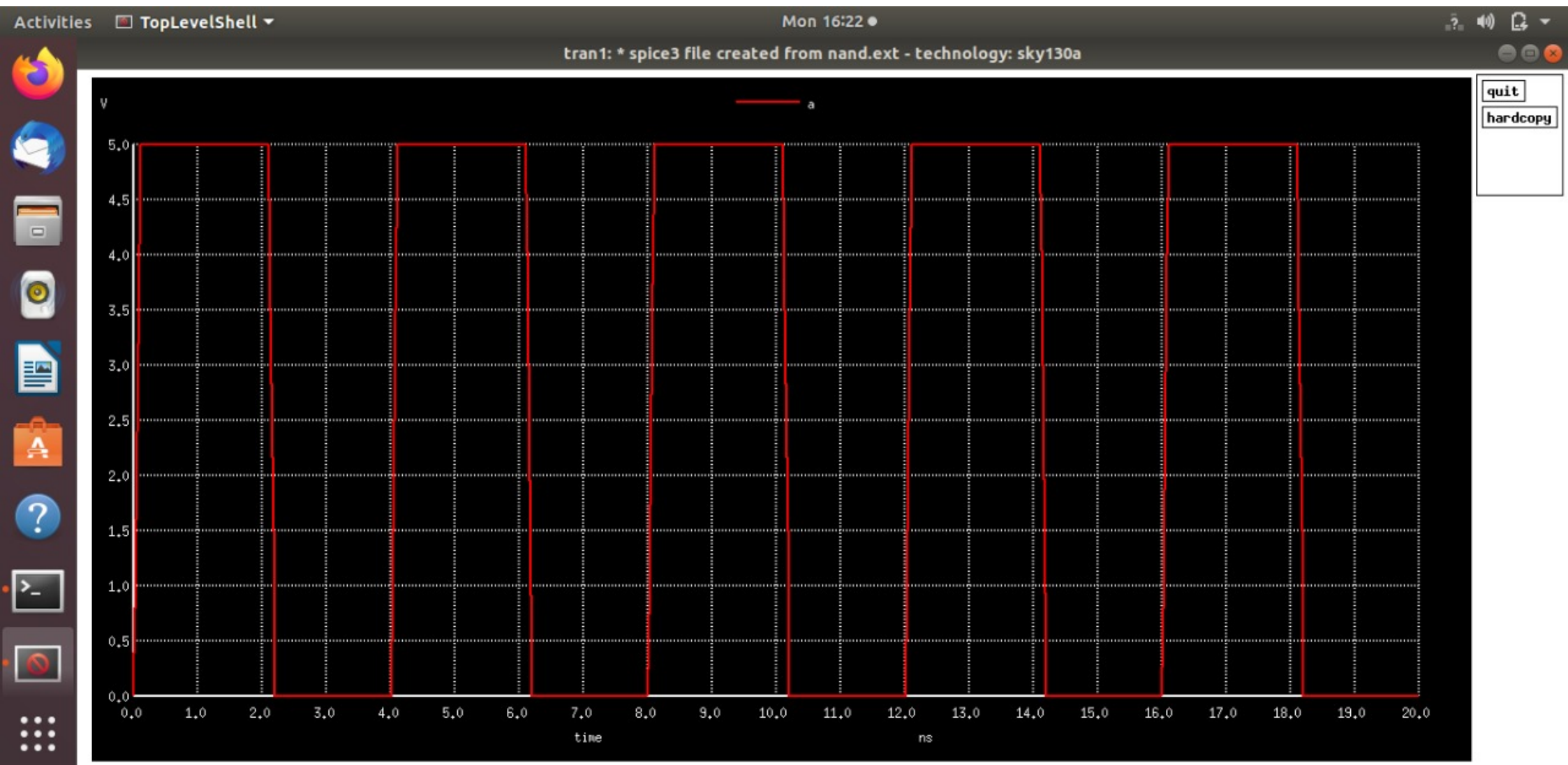
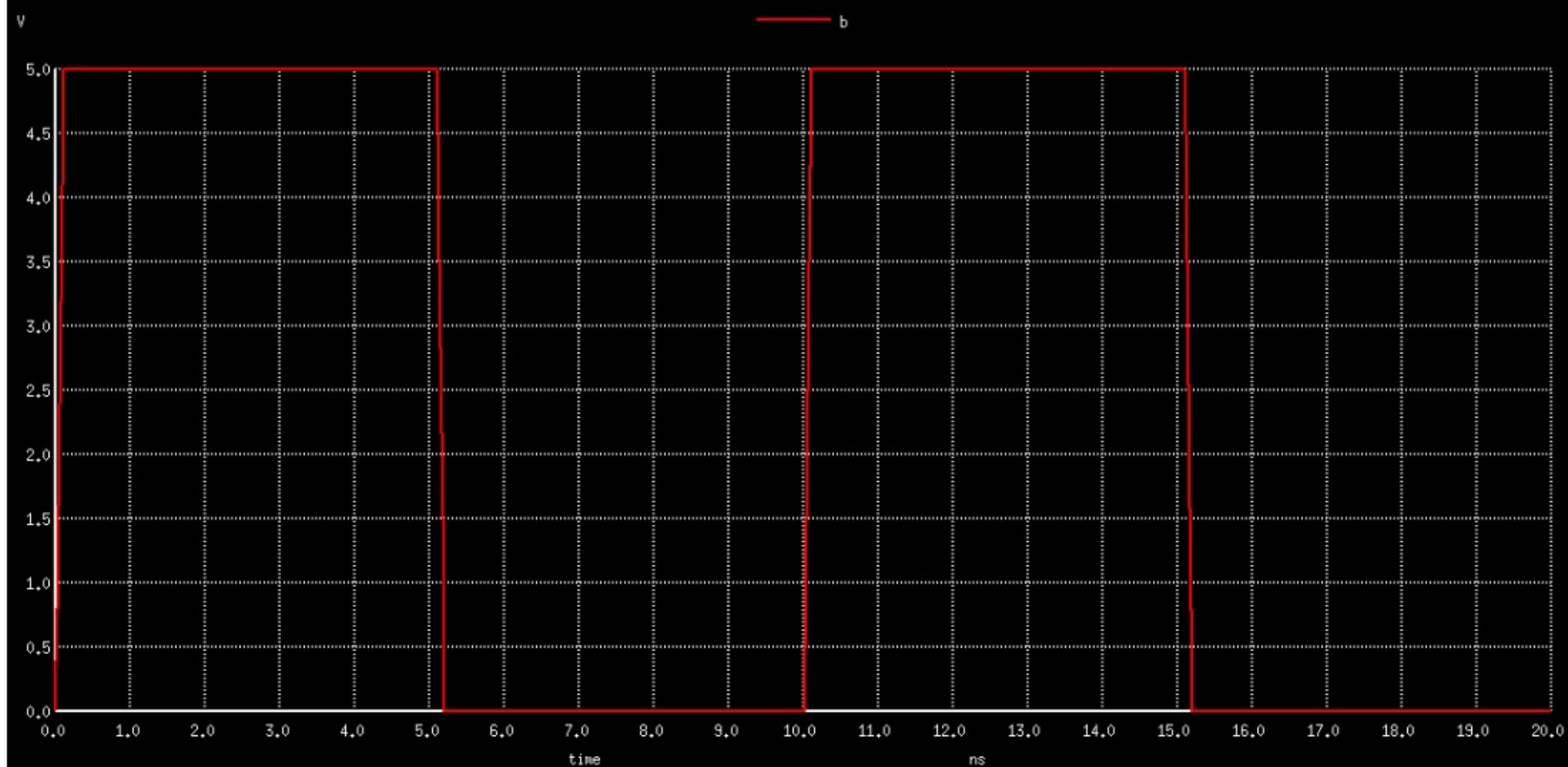


```
1 * SPICE3 file created from AND.ext - technology: sky130A
2 .option scale=0.01u
3 .include ./libs/pshort.lib
4 .include ./libs/nshort.lib
5 M1000 OUT a_21_74# GND GND nshort_model.0 ad=3380 pd=240 as=3380 ps=240 w=75 l=22
6 M1001 a_21_74# A VDD VDD pshort_model.0 ad=1920 pd=124 as=2600 ps=208 w=62 l=27
7 M1002 VDD B a_21_74# VDD pshort_model.0 ad=2170 pd=194 as=1920 ps=124 w=62 l=28
8 M1003 OUT a_21_74# VDD VDD pshort_model.0 ad=3770 pd=252 as=3470 ps=244 w=77 l=22
9 M1004 a_21_74# B a_21_n85# GND nshort_model.0 ad=2810 pd=214 as=1890 ps=123 w=61 l=28
10 M1005 a_21_n85# A GND GND nshort_model.0 ad=1890 pd=123 as=2680 ps=210 w=61 l=27
11 VDD VDD 0 5V
12 VSS VSS 0 0
13 Va A VSS PULSE(0 5V 0 0.1ns 0.1ns 2ns 4ns)
14 Vb B VSS PULSE(0 5V 0 0.1ns 0.1ns 5ns 10ns)
15 C0 a_21_74# B 0.113f
16 C1 OUT B 0.00727f
17 C2 VDD a_21_74# 0.265f
18 C3 OUT VDD 0.126f
19 C4 OUT a_21_74# 0.0553f
20 C5 a_21_n85# a_21_74# 0.017f
21 C6 A B 0.0616f
22 C7 VDD A 0.0418f
23 C8 a_21_74# A 0.0474f
24 C9 VDD B 0.0498f
25 C10 OUT GND 0.326f
26 C11 B GND 0.46f
27 C12 A GND 0.53f
28 C13 VDD GND 2.04f
29 .tran 1n 20n
30 .control
31 run
32 .endc
```



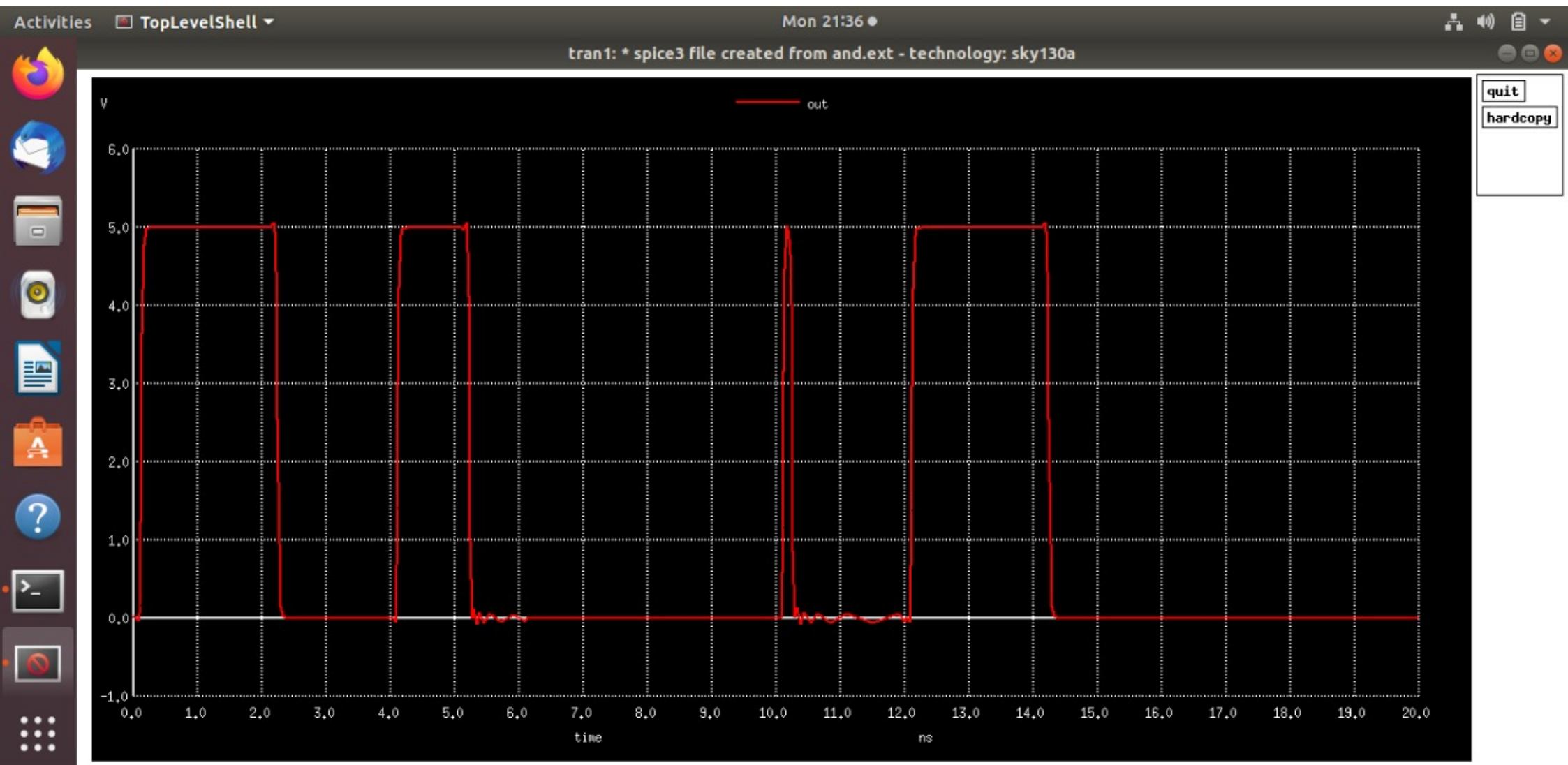


tran1: \* spice3 file created from and.ext - technology: sky130a

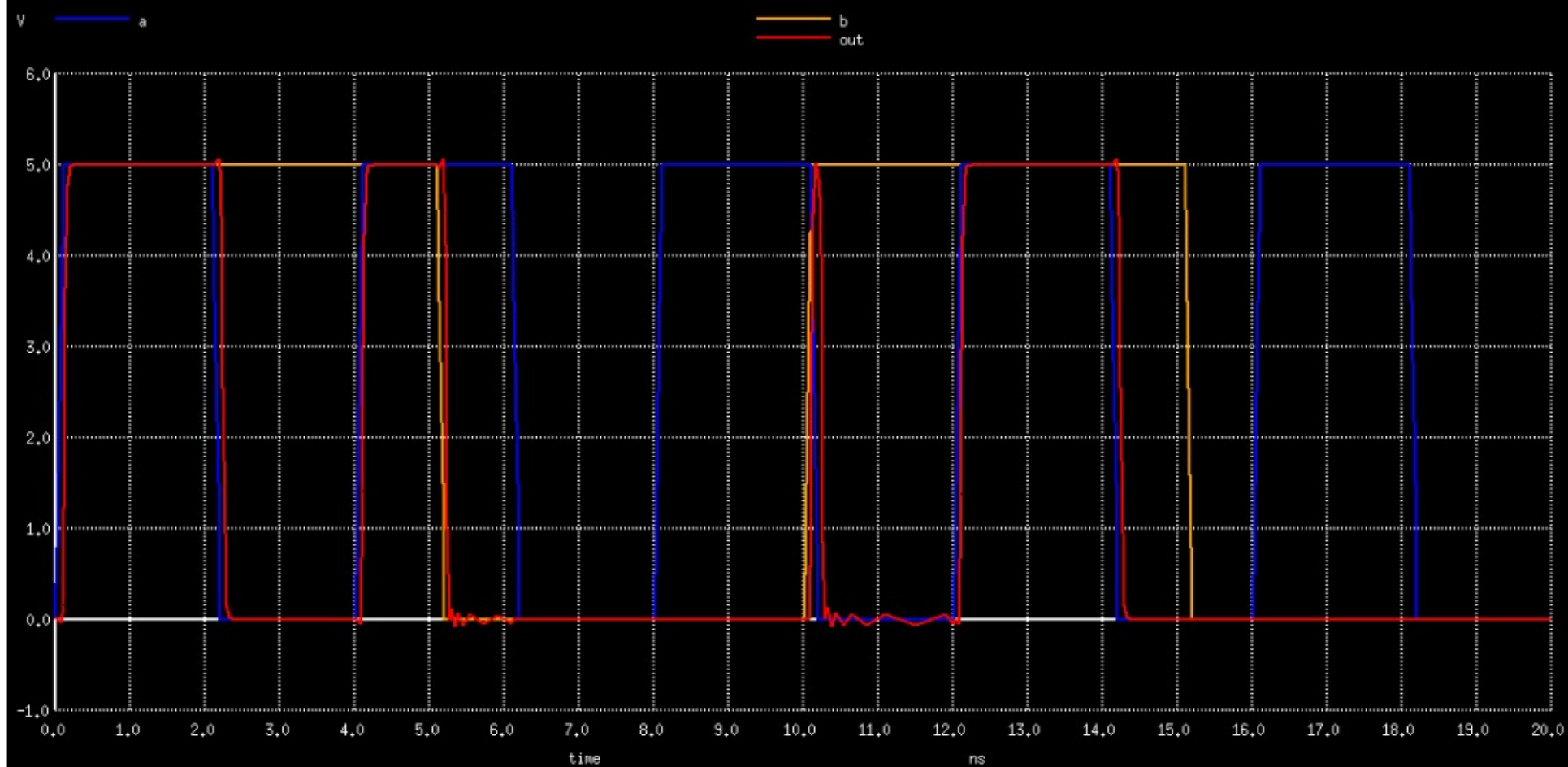


quit

hardcopy



tran1: \* spice3 file created from and.ext - technology: sky130a



quit

hardcopy