CSE530: Assignment#1 Report

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In this notebook, we will be performing ablation studies on the effect of cache parameters on the following kernels

- 2-D Matrix column wise copy
- 2-D Matrix transpose
- · 2-D Matrix gather
- 2-D Matrix scatter

NOTE:

- Kernel code can be found in ./content/CachePerformanceOnMatMul/src
- Traces can be found in ./content/CachePerformanceOnMatMul/traces
- Logs can be found in ./content/CachePerformanceOnMatMul/logs
- All our experiments use a matrix size of 10x10 with a sparsity of 50% and has no optimization on storing sparse matrices.

```
import pathlib
In [ ]:
        import pickle
        import pprint
        import matplotlib.pyplot as plt
In [ ]: | # setup root directory path
        abs_root_dir = pathlib.Path().parent.resolve()
        print(abs root dir)
        /home/ajay/Documents/CSE 530/Assignments/assignment 1/question
In [ ]: | # change directory to the source files
        %cd $abs root dir/content/CachePerformanceOnMatMul/
        /home/ajay/Documents/CSE 530/Assignments/assignment 1/question/content/Cach
        ePerformanceOnMatMul
In [ ]: # To run experiments again, use the following command
        !source ./run kernel.sh 10 10 1 '50' input matrix.in true clean
In [ ]: traces = ["mat scatter", "mat gather", "mat transpose", "mat column wise cor
```

return ans

```
In [ ]: def read pkl contents(tracename):
            Reads contents of pkl file
            data = []
            filename = f"{abs root dir}/content/CachePerformanceOnMatMul/Simulator/l
            with open(filename, 'rb') as fr:
                try:
                    while True:
                         data.append(pickle.load(fr))
                except E0FError:
                    pass
            return data
In [ ]: # load pkl data into object
        all_data = {}
        for tracename in traces:
            all_data[tracename] = read_pkl_contents(tracename)
        # print(pprint.pformat(all data))
In [ ]: # change font size for x label
        plt.rc('xtick', labelsize=4)
        def convert_dict_to_list(experiments, results):
            ans = []
            for experiment in experiments:
                ans.append(results[experiment])
```

```
In [ ]: # plotting for each config
        def plot each cache data(plt obj, data, type, title, experiments=None):
            if experiments is None :
                experiments = []
                for result in data:
                    experiments.append(result["experiment name"])
            cache 1 results = {}
            cache 2 results = {}
            cache 3 results = {}
            for result in data:
                if result["experiment name"] in experiments:
                    cache 1 results[result["experiment name"]] = (result["caches"].d
                    cache 2 results[result["experiment name"]] = (result["caches"].g
                    cache 3 results[result["experiment name"]] = (result["caches"].d
            cache 1 results = convert dict to list(experiments, cache 1 results)
            cache 2 results = convert dict to list(experiments, cache 2 results)
            cache 3 results = convert dict to list(experiments, cache 3 results)
            print(experiments, cache 1 results, cache 2 results, cache 3 results)
            plt_obj.plot(experiments, cache_1_results, marker='o', color='blue', lir
            plt obj.plot(experiments, cache 2 results, marker='o', color='orange', l
            plt obj.plot(experiments, cache 3 results, marker='o', color='green', li
            # plt obj.bar(experiments, cache 1 results, color='blue', label=f'{type}
            # plt obj.bar(experiments, cache 2 results, color='orange', label=f'{tyk
            # plt obj.bar(experiments, cache 3 results, color='green', label=f'{type
            plt obj.set title(title, loc='left')
            plt obj.legend()
        def plot amat(plt obj, data, type, title, experiments=None):
            if experiments is None :
                experiments = []
                for result in data:
                    experiments.append(result["experiment name"])
            cache 1 results = {}
            cache 2 results = {}
            cache 3 results = {}
            for result in data:
                if result["experiment name"] in experiments:
                    cache 1 results[result["experiment name"]] = (result["caches"].d
                    cache 2 results[result["experiment name"]] = (result["caches"].d
                    cache 3 results[result["experiment name"]] = (result["caches"].c
            cache 1 results = convert dict to list(experiments, cache 1 results)
            cache 2 results = convert dict to list(experiments, cache 2 results)
            cache 3 results = convert dict to list(experiments, cache 3 results)
            print(experiments, cache_1 results, cache 2 results, cache 3 results)
            # plt obj.plot(experiments, cache 1 results, marker='o', color='blue',
            # plt obj.plot(experiments, cache 2 results, marker='o', color='orange',
            # plt obj.plot(experiments, cache 3 results, marker='o', color='green',
            plt obj.bar(experiments, cache 1 results, color='blue', label=f'{type} f
            plt obj.bar(experiments, cache 2 results, color='orange', label=f'{type}
            plt_obj.bar(experiments, cache_3_results, color='green', label=f'{type}
```

```
pri_obj.set_title(title, toc="tert")
    plt obj.legend()
# plotting rate for each config
def plot rate(plt obj, data, type, title, experiments=None):
    if experiments is None :
        experiments = []
        for result in data:
            experiments.append(result["experiment name"])
    cache 1 results = {}
    cache 2 results = {}
    cache 3 results = {}
    for result in data:
        if result["experiment name"] in experiments:
            cache 1 results[result["experiment name"]] = (result["caches"].d
            cache 2 results[result["experiment name"]] = (result["caches"].g
            cache 3 results[result["experiment name"]] = (result["caches"].d
    cache 1 results = convert dict to list(experiments, cache 1 results)
    cache 2 results = convert dict to list(experiments, cache 2 results)
    cache 3 results = convert dict to list(experiments, cache 3 results)
    print(experiments, cache 1 results, cache 2 results, cache 3 results)
   plt obj.plot(experiments, cache 1 results, marker='o', color='blue', lir
   plt obj.plot(experiments, cache 2 results, marker='o', color='orange', l
   plt obj.plot(experiments, cache 3 results, marker='o', color='green', li
    plt obj.set title(title, loc='left')
    plt obj.legend()
# plotting rate for each config
def plot cycles(plt obj, data, title, experiments=None):
    if experiments is None :
        experiments = []
        for result in data:
            experiments.append(result["experiment name"])
    results = []
    for result in data:
        if result["experiment name"] in experiments:
            results.append(result["tot cycles"])
    print(experiments, results)
   plt obj.plot(experiments, results, marker='o', color='blue', linestyle='
    plt obj.set title(title, loc='center')
```

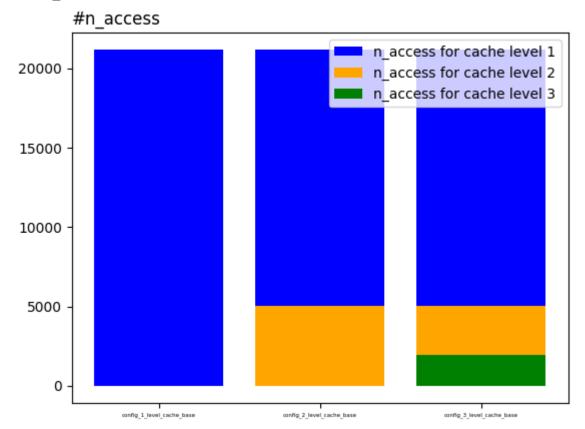
Cache Study - Scatter Kernel

Scatter operation writes data to a given matrix from a list of indices which are random in nature. Since the operations are arbitary, i.e, no spatial and temporal locality, we expect our cache results to match with that of any general cache study.

Experiments on number of cache layers

Here we consider 3 level cache, 2 level cache and 1 level cache and plot amat vs cache layer graph

```
In []: figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_1_level_cache_base", "config_2_level_cache_base", "config_amat(axis, all_data["mat_scatter"], "n_access", "#n_access", experiment
    ['config_1_level_cache_base', 'config_2_level_cache_base', 'config_3_level_cache_base'] [21200, 21200, 21200] [-1, 5057, 5057] [-1, -1, 1932]
```

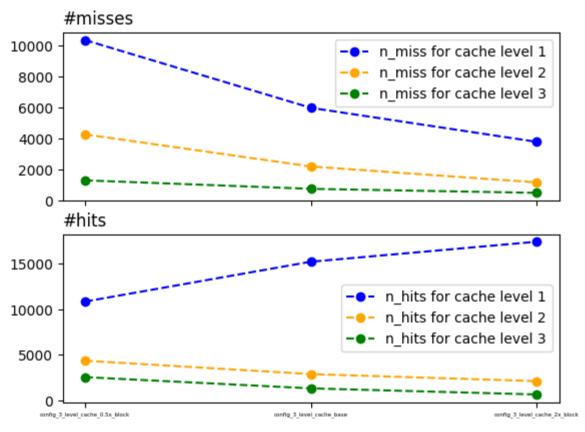


Experiments on Block size

Here we take the 3 level cache as base and reduce its block size by half, and also increase the base's block size twice.

Number of misses and hits vs Block Size

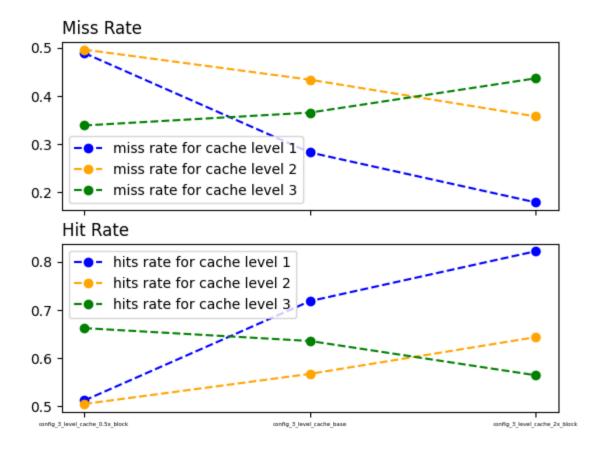
['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_
level_cache_2x_block'] [10359, 5982, 3785] [4263, 2190, 1162] [1293, 745, 4
85]
['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_
level_cache_2x_block'] [10841, 15218, 17415] [4335, 2864, 2092] [2529, 129
6, 627]



We observe the following for number of misses/hits vs block size,

- Increasing block size -> Reduces number of misses in all cache layers
- Increasing block size -> Increases number of hits in L1 cache and slightly reduces number of hits in L2 and L3 cache

Miss and hit rate vs block size

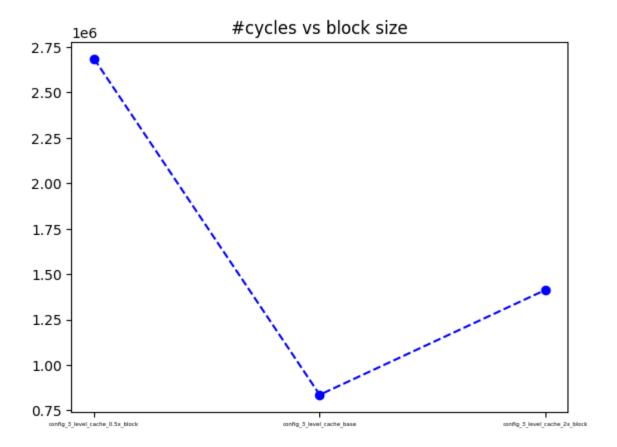


We observe the following for number of miss/hit rate vs block size,

- Increasing block size -> Reduces miss rate in L1 and L2 caches but increases slightly in L3 cache
- Increasing block size -> Increases hit rate in L1 and L2 cache and slightly reduces the hit rate in L3 cache

We believe the deviation in L3 layer is due to the reduced number of access to L3 cache.

Number of cycles vs block size



 Increasing block size -> Reduces the number of cycles taken to complete and then the #cycles increases. We conclude that the base model with block size of 16 works the best.

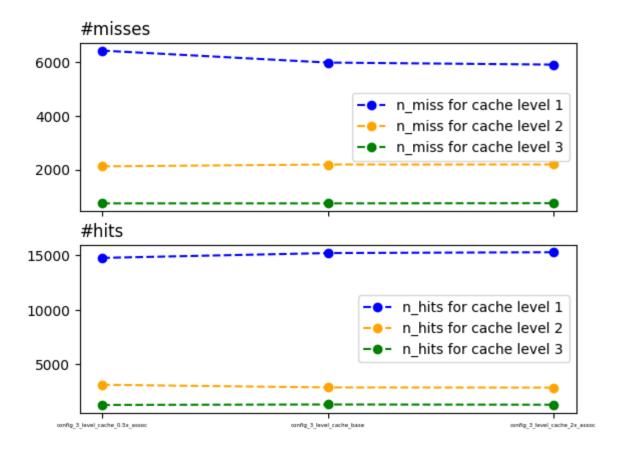
Experiments on Cache Associativity

Here we take the 3 level cache as base and reduce the associativity of L1 cache size by half, and also increase the base's L1 cache associativity twice.

Number of misses and hits vs Associativity

```
In []: figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
    plot_each_cache_data(axis[0], all_data["mat_scatter"], "n_miss", "#misses",
    plot_each_cache_data(axis[1], all_data["mat_scatter"], "n_hits", "#hits", ex

['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_
    level_cache_2x_assoc'] [6426, 5982, 5906] [2121, 2190, 2191] [745, 745, 75
2]
    ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_
    level_cache_2x_assoc'] [14774, 15218, 15294] [3109, 2864, 2846] [1252, 129
6, 1272]
```

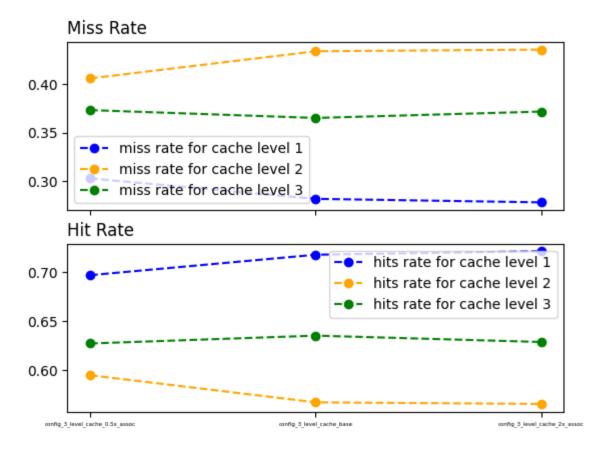


We observe the following for number of misses/hits vs Associativity,

 Increasing associativity from 1 to 4 in L1 cache -> Reduces number of misses in L1 cache but this change doesn't propage to other higher layers.

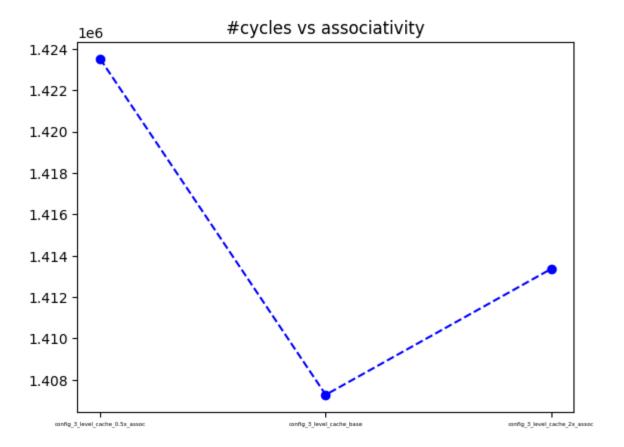
The opposite is observed for number of hits.

Miss and hit rate vs Associativity



Increasing associativity of L1 cache, reduces miss rate in L1. However, a similar observation as that of above is found here, i.e, miss rate is compensated in higher layers (L2, L3)

Number of cycles vs Associativity



 Increasing associativity size -> Reduces the number of cycles taken to complete and then the #cycles increases. We conclude that the base model with associativity of 2 runs in least number of cycles.

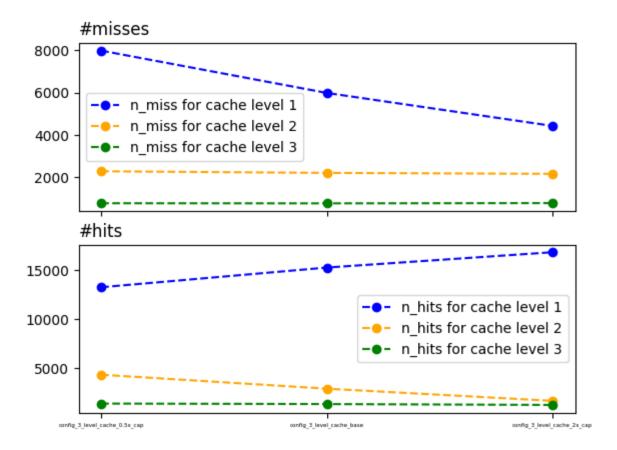
Experiments on Cache Capacity

Here we take the 3 level cache as base and reduce the L1 cache capacity by half, and also increase the base's L1 cache capacity twice.

Number of misses and hits vs Cache Capacity

```
In []: figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
    plot_each_cache_data(axis[0], all_data["mat_scatter"], "n_miss", "#misses",
    plot_each_cache_data(axis[1], all_data["mat_scatter"], "n_hits", "#hits", ex

    ['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_level_cache_2x_cap'] [7990, 5982, 4419] [2267, 2190, 2145] [751, 745, 758]
    ['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_level_cache_2x_cap'] [13210, 15218, 16781] [4288, 2864, 1623] [1345, 1296, 12 04]
```

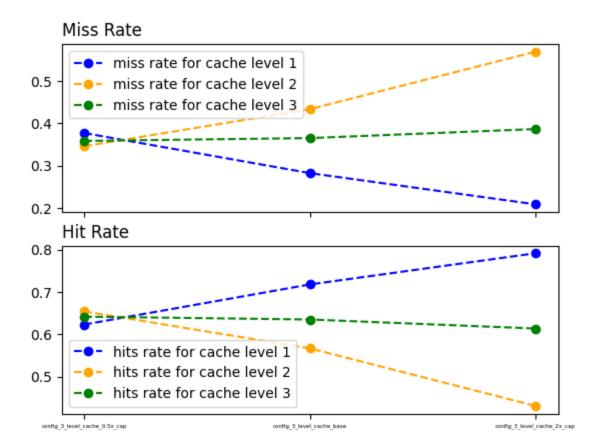


We observe the following for number of misses/hits vs Cache Capacity,

 Increasing Cache Capacity from 8 to 32 in L1 cache -> Reduces number of misses in L1 cache by a huge margin but this change doesn't propage to other higher layers.

The opposite is observed for number of hits.

Miss and hit rate vs Cache Capacity



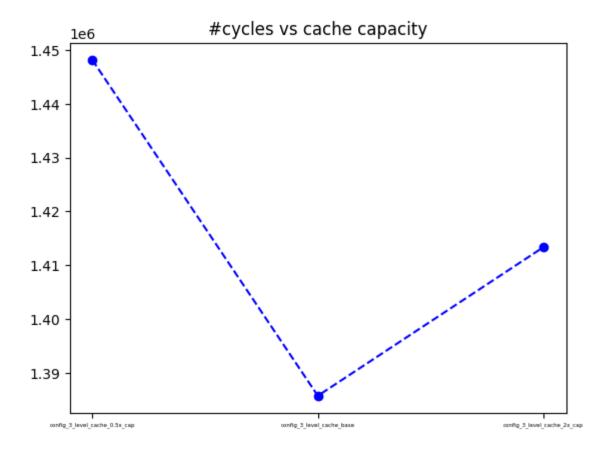
 Increasing L1 cache capacity improves the hit rate. However, L2 and L3 suffer a loss in hit rate. We believe that this is due to cache capacity of L1 being almost similar to that of L2.

Thus, we can conclude that the L2, L3 layer should have higher cache capacity when compared to L1 in order to optimize for hit rate

Number of cycles vs Cache Capacity

```
In []: figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_scatter"], "#cycles vs cache capacity", expe

['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_level_cache 2x cap'] [1448168, 1385748, 1413368]
```



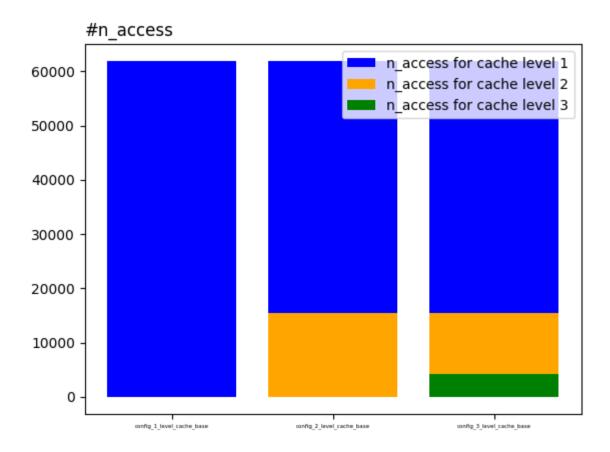
Cache Study - Gather Kernel

Gather operation reads data from random indices of a given matrix. Since the operations are arbitary, i.e, no spatial and temporal locality, we expect our cache results to match with that of general cache study.

Experiments on number of cache layers

Here we consider 3 level cache, 2 level cache and 1 level cache and plot amat vs cache layer graph

```
In []: figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_1_level_cache_base", "config_2_level_cache_base", "config_amat(axis, all_data["mat_gather"], "n_access", "#n_access", experiments
    ['config_1_level_cache_base', 'config_2_level_cache_base', 'config_3_level_cache_base'] [61957, 61957, 61957] [-1, 15406, 15406] [-1, -1, 4179]
```



Experiments on Cache Capacity

Here we take the 3 level cache as base and reduce the L1 cache capacity by half, and also increase the base's L1 cache capacity twice.

```
In []: figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
    plot_each_cache_data(axis[0], all_data["mat_gather"], "n_miss", "#misses", e
    plot_each_cache_data(axis[1], all_data["mat_gather"], "n_hits", "#hits", exp

figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
    plot_rate(axis[0], all_data["mat_gather"], "n_miss", "Miss_Rate", experiments
    plot_rate(axis[1], all_data["mat_gather"], "n_hits", "Hit_Rate", experiments

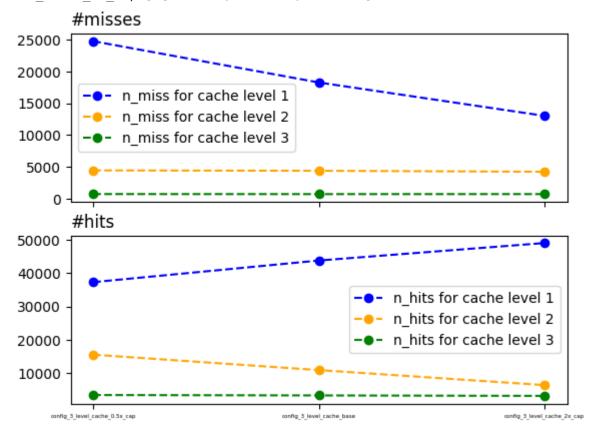
figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_gather"], "#cycles vs_cache_capacity", experiments
```

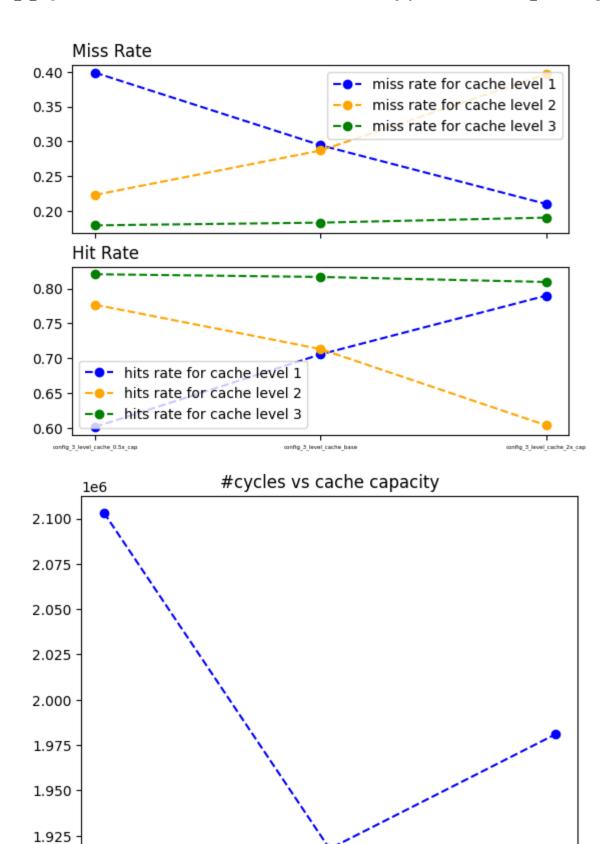
['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
vel_cache_2x_cap'] [24711, 18243, 13016] [4477, 4421, 4265] [773, 766, 769]
['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
vel_cache_2x_cap'] [37246, 43714, 48941] [15583, 10985, 6489] [3536, 3413,
3267]

['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le vel_cache_2x_cap'] [0.39884113175266717, 0.29444614813499687, 0.21008118533 821843] [0.2231804586241276, 0.28696611709723485, 0.39659661521294404] [0.17939197029473195, 0.18329743957884662, 0.19053518334985134]

['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
vel_cache_2x_cap'] [0.6011588682473328, 0.7055538518650032, 0.7899188146617
816] [0.7768195413758724, 0.7130338829027651, 0.603403384787056] [0.8206080
29705268, 0.8167025604211534, 0.8094648166501487]

['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le vel cache 2x cap'] [2103197, 1917753, 1980873]





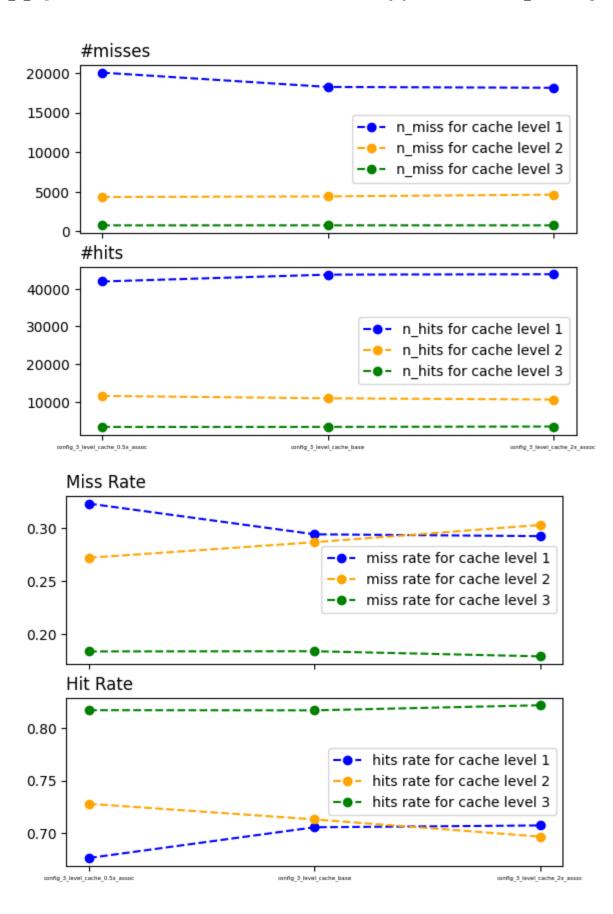
17 of 41 9/13/22, 23:46

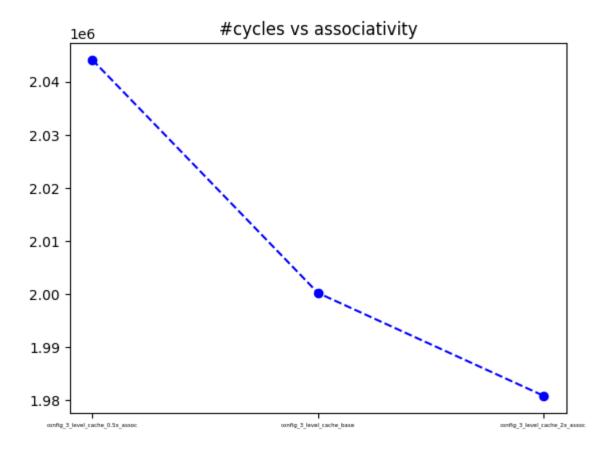
config_3_level_cache_0.5x_cap

Experiments on Cache Associativity

Here we take the 3 level cache as base and reduce the associativity of L1 cache size by half, and also increase the base's L1 cache associativity twice.

```
In [ ]: figure, axis = plt.subplots(2, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
        plot_each_cache_data(axis[0], all_data["mat_gather"], "n_miss", "#misses", &
        plot each cache data(axis[1], all data["mat gather"], "n hits", "#hits", exp
        figure, axis = plt.subplots(2, sharex=True,)
        experiments = ["config 3 level cache 0.5x assoc", "config 3 level cache base
        plot rate(axis[0], all data["mat gather"], "n miss", "Miss Rate", experiment
        plot_rate(axis[1], all_data["mat_gather"], "n_hits", "Hit Rate", experiments
        figure, axis = plt.subplots(1, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
        plot cycles(axis, all data["mat gather"], "#cycles vs associativity", experi
        ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_
        level cache 2x assoc'] [20051, 18243, 18136] [4342, 4421, 4642] [762, 766,
        ['config 3 level cache 0.5x assoc', 'config 3 level cache base', 'config 3
        level cache 2x assoc'] [41906, 43714, 43821] [11611, 10985, 10660] [3399, 3
        413, 3513]
        ['config 3 level cache 0.5x assoc', 'config 3 level cache base', 'config 3
        level cache 2x assoc'] [0.32362767726003516, 0.29444614813499687, 0.2927191
        439223978] [0.2721745126308531, 0.28696611709723485, 0.3033590380342439]
        [0.18312905551550107, 0.18329743957884662, 0.1784377923292797]
        ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_
        level cache 2x assoc' [0.6763723227399648, 0.7055538518650032, 0.707280856
        0776022] [0.7278254873691469, 0.7130338829027651, 0.6966409619657561] [0.81
        68709444844989, 0.8167025604211534, 0.8215622076707203]
        ['config 3 level cache 0.5x assoc', 'config 3 level cache base', 'config 3
        level cache 2x assoc'] [2044105, 2000229, 1980873]
```





Experiments on Block size

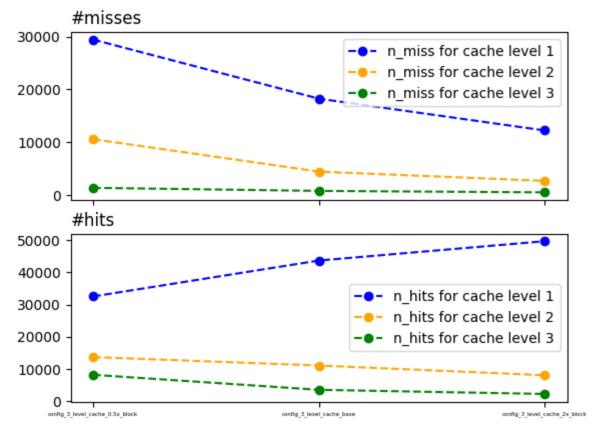
Here we take the 3 level cache as base and reduce its block size by half, and also increase the base's block size twice.

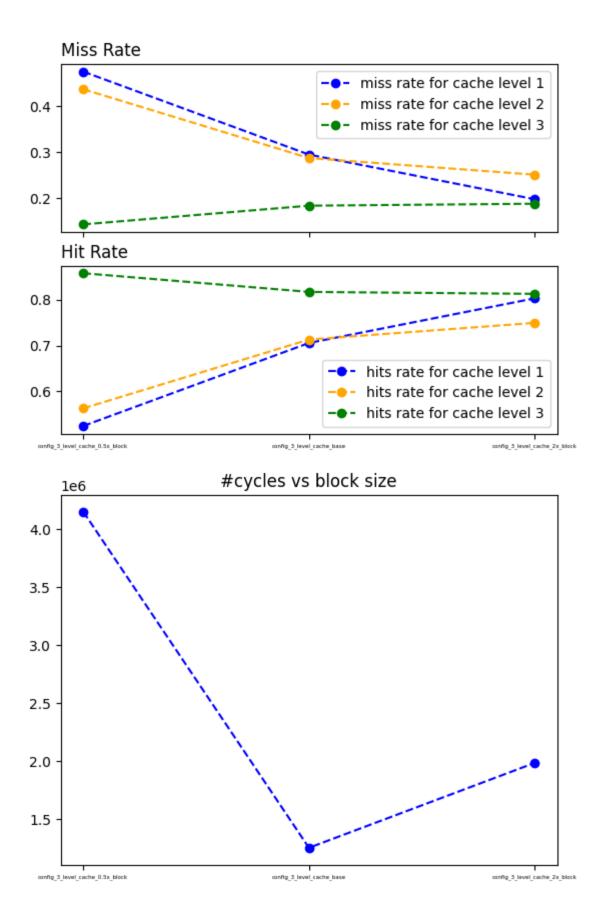
```
In []: figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
    plot_each_cache_data(axis[0], all_data["mat_gather"], "n_miss", "#misses", e
    plot_each_cache_data(axis[1], all_data["mat_gather"], "n_hits", "#hits", exp

figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
    plot_rate(axis[0], all_data["mat_gather"], "n_miss", "Miss Rate", experiment
    plot_rate(axis[1], all_data["mat_gather"], "n_hits", "Hit Rate", experiments

figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
    plot_cycles(axis, all_data["mat_gather"], "#cycles vs block size", experiment
```

['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_level_cache_2x_block'] [29431, 18243, 12225] [10561, 4421, 2664] [1346, 76 6, 490]
['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_level_cache_2x_block'] [32526, 43714, 49732] [13623, 10985, 7961] [8090, 34 13, 2123]
['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_level_cache_2x_block'] [0.4750229998224575, 0.29444614813499687, 0.19731426 634601418] [0.4366936817730731, 0.28696611709723485, 0.25072941176470587] [0.14264518863925393, 0.18329743957884662, 0.18752391886720246]
['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_level_cache_2x_block'] [0.5249770001775425, 0.7055538518650032, 0.802685733 6539859] [0.5633063182269269, 0.7130338829027651, 0.7492705882352941] [0.8573548113607461, 0.8167025604211534, 0.8124760811327976]
['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_level_cache_2x_block'] [4146517, 1249213, 1980873]





We observe the following for gather kernel after playing around with the cache parameters

- Increasing block size from 8 to 32.
 - Increases #hits, hit rate for L1 cache
 - Decreases #cycles and then increases
- Increasing associativity from 1 to 4
 - Reduces #misses, miss rate for L1 cache
 - Decreases #cycles and then increases
- Increasing cache capacity from 8 to 32
 - Increases #hits, hit rates
 - Decreases #cycles and then increases

Cache Study - Transpose Kernel

Transpose operation reads exchanges columns and rows of a given matrix. We implement a naive kernel in this case and observe the effects of temporal and spatial locality. We theorize that the naive implementation, having minimal spatial and temporal locality will not be cache friendly.

Experiments on Block size

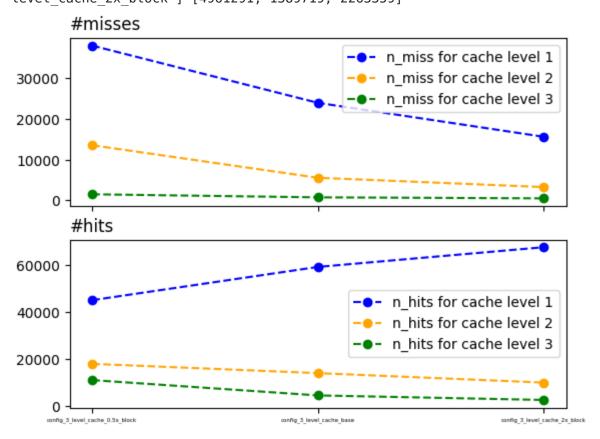
Here we take the 3 level cache as base and reduce its block size by half, and also increase the base's block size twice.

```
In []: figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
    plot_each_cache_data(axis[0], all_data["mat_transpose"], "n_miss", "#misses"
    plot_each_cache_data(axis[1], all_data["mat_transpose"], "n_hits", "#hits",

    figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
    plot_rate(axis[0], all_data["mat_transpose"], "n_miss", "Miss Rate", experime

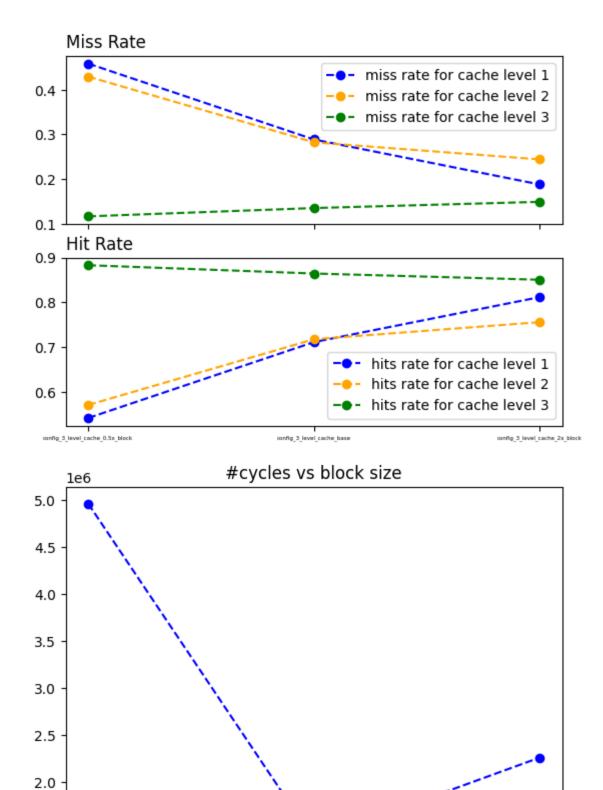
    figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs block size", experi
```

['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_ level cache 2x block'] [38073, 23990, 15646] [13551, 5545, 3258] [1476, 72 9, 479] ['config 3 level cache 0.5x block', 'config 3 level cache base', 'config 3 level cache 2x block'] [44994, 59077, 67421] [18017, 14088, 10079] [11134, 4648, 2725] ['config 3 level cache 0.5x block', 'config 3 level cache base', 'config 3 level cache 2x block'] [0.4583408573801871, 0.2888030144341315, 0.188353979 31790002] [0.4292638114546376, 0.2824326389242602, 0.24428282222388842] [0. 11704996034892942, 0.13557745954993491, 0.14950062421972535] ['config 3 level cache 0.5x block', 'config 3 level cache base', 'config 3 level cache 2x block'] [0.541659142619813, 0.7111969855658685, 0.8116460206 821] [0.5707361885453623, 0.7175673610757398, 0.7557171777761116] [0.882950 0396510706, 0.8644225404500651, 0.8504993757802747] ['config 3 level cache 0.5x block', 'config 3 level cache base', 'config 3 level cache 2x block'] [4961291, 1389719, 2263359]



1.5

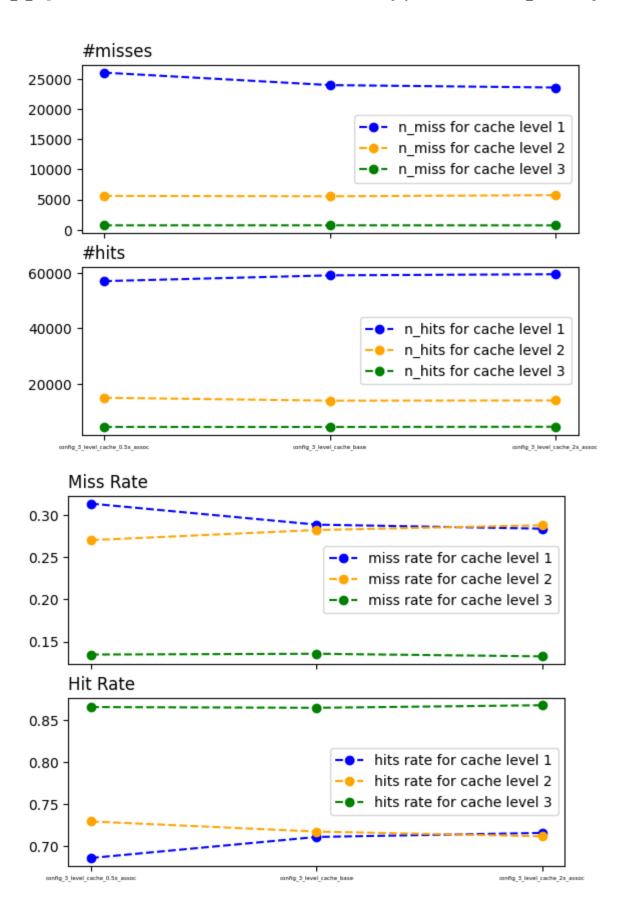
config_3_level_cache_0.5x_block

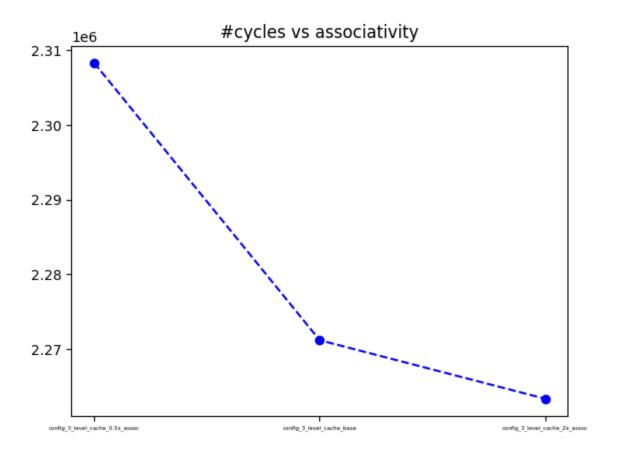


Experiments on Cache Associativity

Here we take the 3 level cache as base and reduce the associativity of L1 cache size by half, and also increase the base's L1 cache associativity twice.

```
In [ ]: figure, axis = plt.subplots(2, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
        plot each_cache_data(axis[0], all_data["mat_transpose"], "n_miss", "#misses"
        plot each cache data(axis[1], all data["mat transpose"], "n hits", "#hits",
        figure, axis = plt.subplots(2, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base"]
        plot rate(axis[0], all data["mat_transpose"], "n_miss", "Miss Rate", experin
        plot_rate(axis[1], all_data["mat_transpose"], "n_hits", "Hit Rate", experime
        figure, axis = plt.subplots(1, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
        plot cycles(axis, all data["mat transpose"], "#cycles vs associativity", exp
        ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_
        level cache 2x assoc'] [26060, 23990, 23590] [5608, 5545, 5730] [725, 729,
        ['config 3 level cache 0.5x assoc', 'config 3 level cache base', 'config 3
        level cache 2x assoc'] [57007, 59077, 59477] [15130, 14088, 14161] [4658, 4
        648, 4713]
        ['config 3 level cache 0.5x assoc', 'config 3 level cache base', 'config 3
        level cache 2x assoc'] [0.31372265761373347, 0.2888030144341315, 0.28398762
        444773495] [0.2704214485485582, 0.2824326389242602, 0.2880699813986225] [0.
        1346832621214936, 0.13557745954993491, 0.13252346769740475]
        ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_
        level cache 2x assoc'] [0.6862773423862665, 0.7111969855658685, 0.716012375
        552265] [0.7295785514514418, 0.7175673610757398, 0.7119300186013775] [0.865
        3167378785064, 0.8644225404500651, 0.8674765323025952]
        ['config 3 level cache 0.5x assoc', 'config 3 level cache base', 'config 3
        level cache 2x assoc'] [2308291, 2271191, 2263359]
```





Experiments on Cache Capacity

Here we take the 3 level cache as base and reduce the L1 cache capacity by half, and also increase the base's L1 cache capacity twice.

```
In []: figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
    plot_each_cache_data(axis[0], all_data["mat_transpose"], "n_miss", "#misses"
    plot_each_cache_data(axis[1], all_data["mat_transpose"], "n_hits", "#hits",

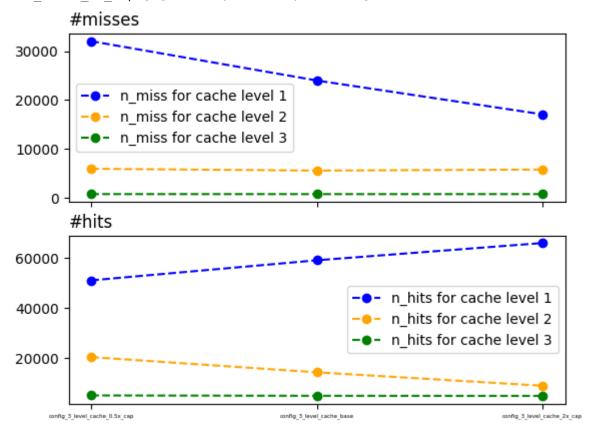
    figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
    plot_rate(axis[0], all_data["mat_transpose"], "n_miss", "Miss Rate", experiments = ["config_3_level_cache_o.5x_cap", "n_hits", "Hit Rate", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity", experiments = ["config_3_level_cache_o.5x_cap", "config_3_level_cache_base",
    plot_cycles(axis, all_data["mat_transpose"], "#cycles vs cache capacity",
```

['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
vel_cache_2x_cap'] [32048, 23990, 17060] [5916, 5545, 5766] [734, 729, 731]
['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
vel_cache_2x_cap'] [51019, 59077, 66007] [20194, 14088, 8695] [4798, 4648,
4611]

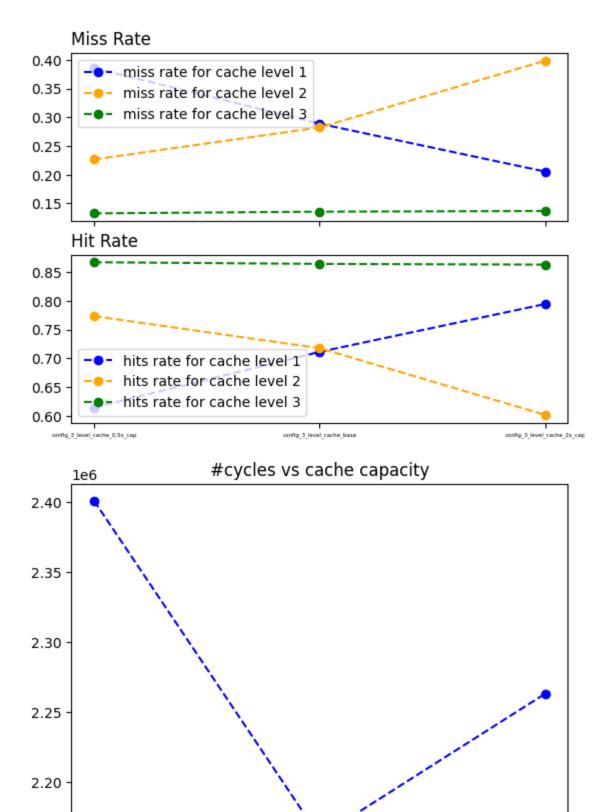
['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
vel_cache_2x_cap'] [0.38580904571008945, 0.2888030144341315, 0.205376382919
81172] [0.226579854461892, 0.2824326389242602, 0.39872761219832653] [0.1326
825741142444, 0.13557745954993491, 0.1368401347809809]

['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le vel_cache_2x_cap'] [0.6141909542899106, 0.7111969855658685, 0.7946236170801 882] [0.773420145538108, 0.7175673610757398, 0.6012723878016735] [0.8673174 258857556, 0.8644225404500651, 0.8631598652190191]

['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le vel cache 2x cap'] [2401015, 2161079, 2263359]



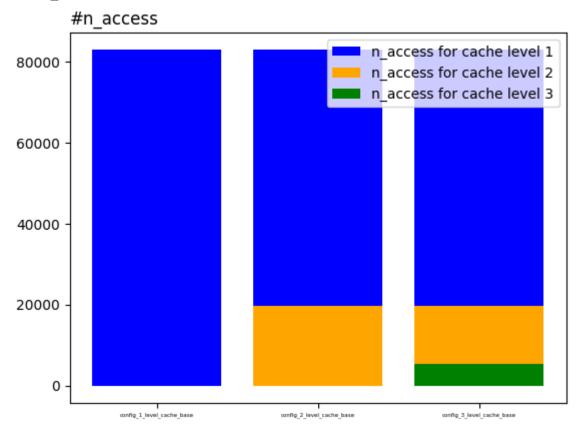
2.15



Experiments on number of cache layers

Here we consider 3 level cache, 2 level cache and 1 level cache and plot amat vs cache layer graph

```
In []: figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_1_level_cache_base", "config_2_level_cache_base", "config_amat(axis, all_data["mat_transpose"], "n_access", "#n_access", experime
    ['config_1_level_cache_base', 'config_2_level_cache_base', 'config_3_level_cache_base'] [83067, 83067, 83067] [-1, 19633, 19633] [-1, -1, 5377]
```



We observe the following for transpose kernel after playing around with the cache parameters

- Increasing block size from 8 to 32.
 - Increases #hits, hit rate for almost all caches
 - Decreases #cycles
- Increasing associativity from 1 to 4
 - Reduces #misses, miss rate for L1 cache
 - Decreases #cycles
- Increasing cache capacity from 8 to 32
 - Increases #hits, hit rates
 - Decreases #cycles and then increases

Cache Study - Column wise Matrix Copy Kernel

Column wise matrix copy operation reads data column by column and then writes the entry to a new matrix. We theorize that this implementation, having minimal spatial and temporal locality will not be cache friendly.

Experiments on number of cache layers

Here we consider 3 level cache, 2 level cache and 1 level cache and plot amat vs cache layer graph

```
In [ ]:
                                          figure, axis = plt.subplots(1, sharex=True,)
                                            experiments = ["config 1 level_cache_base", "config_2_level_cache_base", "config_2_level_cache_base, "config_2_l
                                           ['config_1_level_cache_base', 'config_2_level_cache_base', 'config_3_level_
                                           cache base'] [27458, 27458, 27458] [-1, 5997, 5997] [-1, -1, 1982]
                                                                                   #n access
                                                                                                                                                                                                                                                                                        n_access for cache level 1
                                                                                                                                                                                                                                                                                         n_access for cache level 2
                                              25000
                                                                                                                                                                                                                                                                                        n_access for cache level 3
                                              20000
                                               15000
                                                10000
                                                    5000
                                                                     0
```

32 of 41 9/13/22, 23:46

config_2_level_cache_base

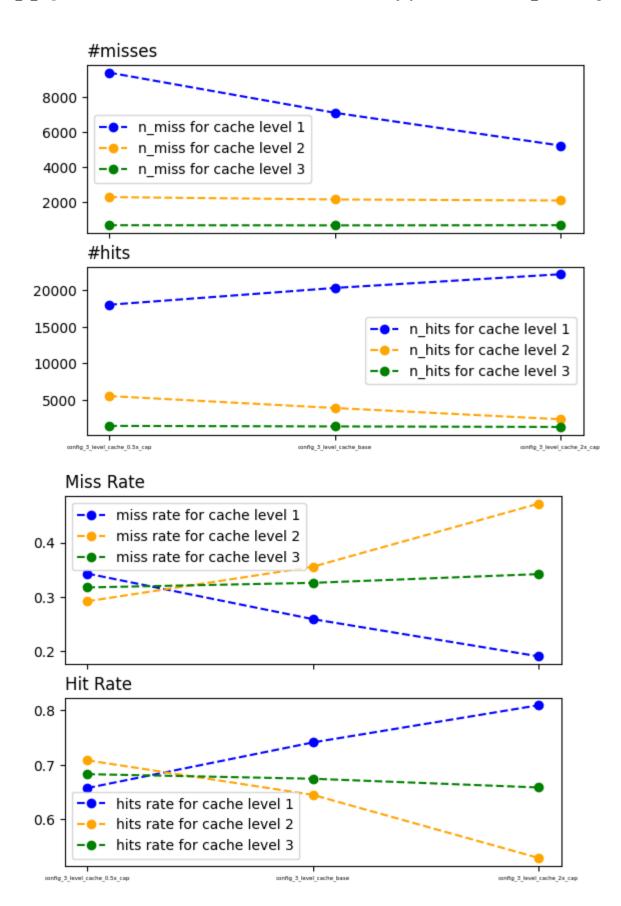
config_3_level_cache_base

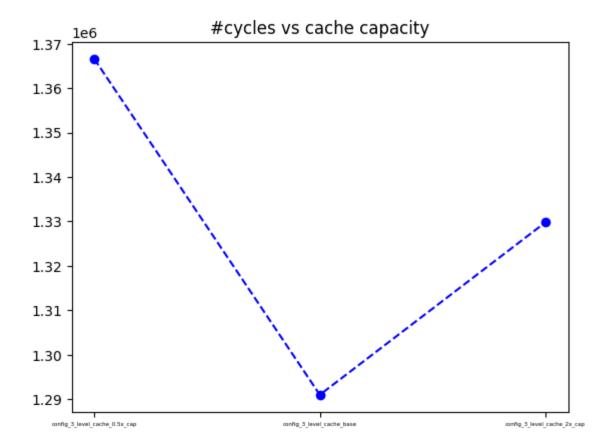
config_1_level_cache_base

Experiments on Cache Capacity

Here we take the 3 level cache as base and reduce the L1 cache capacity by half, and also increase the base's L1 cache capacity twice.

```
In [ ]: figure, axis = plt.subplots(2, sharex=True,)
         experiments = ["config 3 level cache 0.5x cap", "config 3 level cache base"
         plot_each_cache_data(axis[0], all_data["mat_column_wise_copy"], "n_miss", "#
         plot each cache data(axis[1], all data["mat column wise copy"], "n hits", "#
         figure, axis = plt.subplots(2, sharex=True,)
         experiments = ["config_3_level_cache_0.5x_cap", "config_3_level_cache_base",
         plot rate(axis[0], all data["mat column wise copy"], "n miss", "Miss Rate",
         plot_rate(axis[1], all_data["mat_column_wise_copy"], "n_hits", "Hit Rate", exist of the column_wise_copy"]
         figure, axis = plt.subplots(1, sharex=True,)
         experiments = ["config 3 level cache 0.5x cap", "config 3 level cache base"
         plot cycles(axis, all data["mat column wise copy"], "#cycles vs cache capaci
         ['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
         vel cache 2x cap'] [9420, 7112, 5230] [2270, 2133, 2075] [655, 646, 656]
        ['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le vel_cache_2x_cap'] [18038, 20346, 22228] [5504, 3864, 2322] [1408, 1336, 12
         ['config 3 level cache 0.5x cap', 'config 3 level cache base', 'config 3 le
         vel cache 2x cap'] [0.3430694151067084, 0.25901376647971447, 0.190472721975
         3806] [0.29199897092873683, 0.35567783891945975, 0.47191266772799634] [0.31
         74987881725642, 0.32593340060544906, 0.3420229405630866]
         ['config_3_level_cache_0.5x_cap', 'config_3_level_cache_base', 'config_3_le
         vel cache 2x cap'] [0.6569305848932916, 0.7409862335202855, 0.8095272780246
         194] [0.7080010290712632, 0.6443221610805403, 0.5280873322720037] [0.682501
        2118274357, 0.6740665993945509, 0.6579770594369134]
         ['config 3 level cache 0.5x cap', 'config 3 level cache base', 'config 3 le
         vel_cache_2x_cap'] [1366602, 1290974, 1329798]
```





Experiments on Cache Associativity

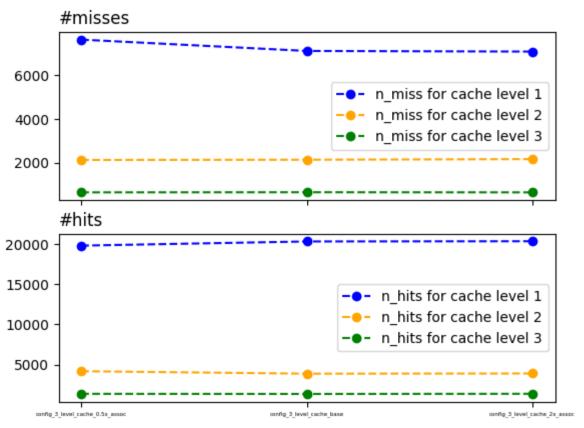
Here we take the 3 level cache as base and reduce the associativity of L1 cache size by half, and also increase the base's L1 cache associativity twice.

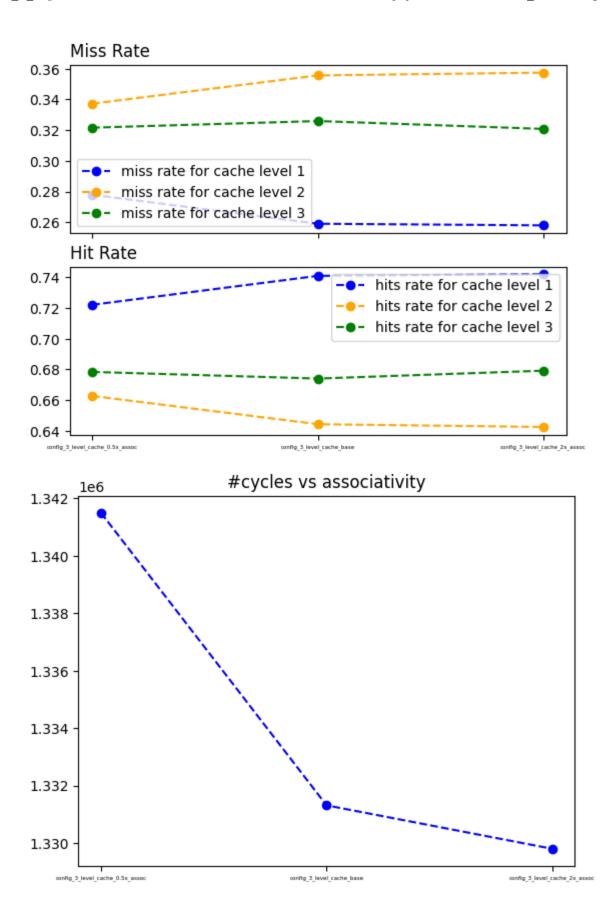
```
In []: figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
    plot_each_cache_data(axis[0], all_data["mat_column_wise_copy"], "n_miss", "#
    plot_each_cache_data(axis[1], all_data["mat_column_wise_copy"], "n_hits", "#

figure, axis = plt.subplots(2, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
    plot_rate(axis[0], all_data["mat_column_wise_copy"], "n_miss", "Miss Rate",
    plot_rate(axis[1], all_data["mat_column_wise_copy"], "n_hits", "Hit Rate", e

figure, axis = plt.subplots(1, sharex=True,)
    experiments = ["config_3_level_cache_0.5x_assoc", "config_3_level_cache_base
    plot_cycles(axis, all_data["mat_column_wise_copy"], "#cycles vs associativit
```

['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_level_cache_2x_assoc'] [7630, 7112, 7083] [2122, 2133, 2162] [640, 646, 64 0] ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_level_cache_2x_assoc'] [19828, 20346, 20375] [4170, 3864, 3886] [1350, 133 6, 1355] ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_level_cache_2x_assoc'] [0.27787894238473304, 0.25901376647971447, 0.2579576 0798310147] [0.33725365543547364, 0.35567783891945975, 0.357473544973545] [0.32160804020100503, 0.32593340060544906, 0.3208020050125313] ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_level_cache_2x_assoc'] [0.7221210576152669, 0.7409862335202855, 0.742042392 0168986] [0.6627463445645264, 0.6443221610805403, 0.642526455026455] [0.678 391959798995, 0.6740665993945509, 0.6791979949874687] ['config_3_level_cache_0.5x_assoc', 'config_3_level_cache_base', 'config_3_level_cache_2x_assoc'] [1341498, 1331310, 1329798]

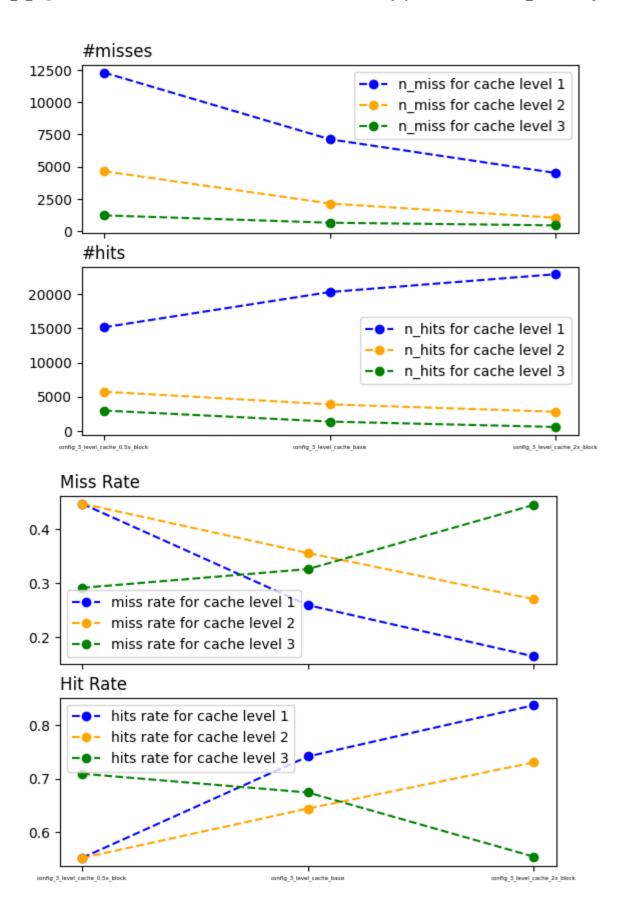


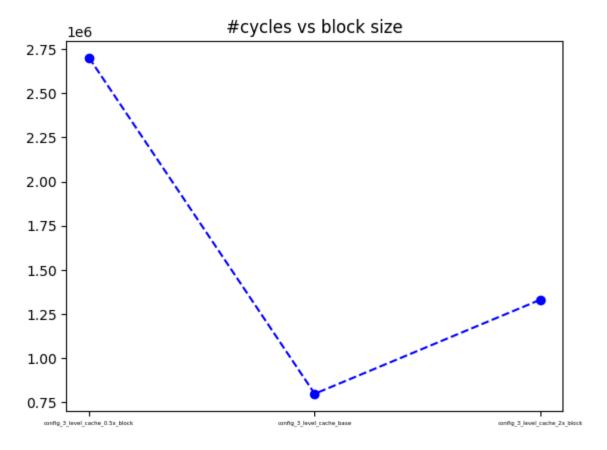


Experiments on Block size

Here we take the 3 level cache as base and reduce its block size by half, and also increase the base's block size twice.

```
In [ ]: figure, axis = plt.subplots(2, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
        plot_each_cache_data(axis[0], all_data["mat_column_wise_copy"], "n miss", "#
        plot each cache data(axis[1], all data["mat column wise copy"], "n hits", "#
        figure, axis = plt.subplots(2, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
        plot rate(axis[0], all data["mat column wise copy"], "n miss", "Miss Rate",
        plot_rate(axis[1], all_data["mat_column_wise_copy"], "n_hits", "Hit Rate", \epsilon
        figure, axis = plt.subplots(1, sharex=True,)
        experiments = ["config_3_level_cache_0.5x_block", "config_3_level_cache_base
        plot cycles(axis, all data["mat column wise copy"], "#cycles vs block size",
        ['config 3 level cache 0.5x block', 'config 3 level cache base', 'config 3
        level cache 2x block'] [12277, 7112, 4501] [4634, 2133, 1029] [1215, 646, 4
        ['config 3 level cache 0.5x block', 'config 3 level cache base', 'config 3
        level cache 2x block'] [15181, 20346, 22957] [5722, 3864, 2780] [2958, 133
        6, 550]
        ['config 3 level cache 0.5x block', 'config 3 level_cache_base', 'config_3_
        level cache 2x block'] [0.44711923665234177, 0.25901376647971447, 0.1639230
        8252603977] [0.4474700656624179, 0.35567783891945975, 0.27014964557626675]
        [0.291157440690151, 0.32593340060544906, 0.4450050454086781]
        ['config_3_level_cache_0.5x_block', 'config_3_level_cache_base', 'config_3_
        level cache 2x block'] [0.5528807633476582, 0.7409862335202855, 0.836076917
        4739602] [0.5525299343375821, 0.6443221610805403, 0.7298503544237332] [0.70
        88425593098491, 0.6740665993945509, 0.5549949545913219]
        ['config 3 level cache 0.5x block', 'config 3 level cache base', 'config 3
        level cache 2x block'] [2701426, 796362, 1329798]
```





We observe the following for column wise copy kernel after playing around with the cache parameters

- Increasing block size from 8 to 32.
 - Increases #hits, hit rate for almost all caches
 - Decreases #cycles and then increases
- Increasing associativity from 1 to 4
 - Reduces #misses, miss rate for L1 cache
 - Decreases #cycles
- Increasing cache capacity from 8 to 32
 - Increases #hits, hit rates
 - Decreases #cycles and then increases

Conclusion

As part of this exercise, we are able to understand how the architecture of cache systems impact the implementations of popular matrix operations. Although the current kernel codes analyzed as part of this assignment use naive implementation, we list down few possible optimizations here in order to work in future

- Block based matrix transpose
 - Do matrix transpose on the input matrix a block level instead of matrix level.
 - This approach will use both temporal and spatial locality to the best
- Scatter and Gather implementation using CSR matrix format
 - With increasing sparsity of the matrix, the CSR format based implementation would be a cache friendly algorithm

References

- [1] H. Patil, R. Cohn, M. Charney, R. Kapoor, A. Sun and A. Karunanidhi, Pinpointing Representative Portions of Large Intel ® Itanium ® Programs with Dynamic Instrumentation 37th International Symposium on Microarchitecture (MICRO-37'04), 2004, pp. 81-92, doi: 10.1109/MICRO.2004.28.
- [2] CacheSimulator, https://github.com/abhishekk06/CachePerformanceOnMatMul
- [3] GEM5 building, https://www.gem5.org/documentation/general_docs/building