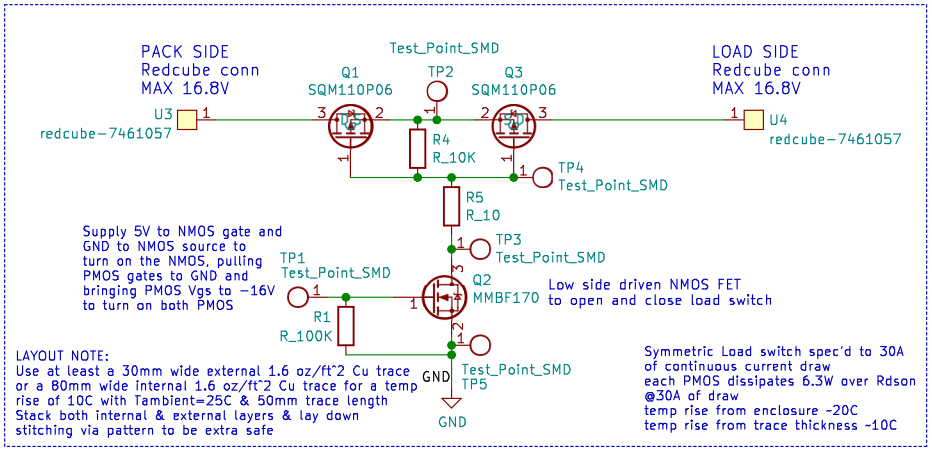
Bidirectional Discrete High-Side
Dual-PMOS Load switch



README:
This board's objective is to test high current path on the new LV BMS boards. Specifically we will be testing the thermals (heat dissipation) from the onboard power MOSFETs along with the load switch circuit behavior. We have already completed LT Spice simulations but would like to verify circuit behavior IRL before spinning up the LV BMS boards.

The battery can both be charged (load side will see +16.8V and pack side will be between 11-16.8V), discharged (pack side will see 16.8V & load side will see a small load resistor tied to GND), or in an idle state (pack side sees 11-16.8V and load side is disconnected).

NMOS gate will be driven by Atemga GPIO on LV BMS board.

See the LV BMS board in MKV-Boards for more info.

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Title: LV BMS High Current Path Tester Board

Size: A4	Date: 2021-09-17	**Rev: 1**
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