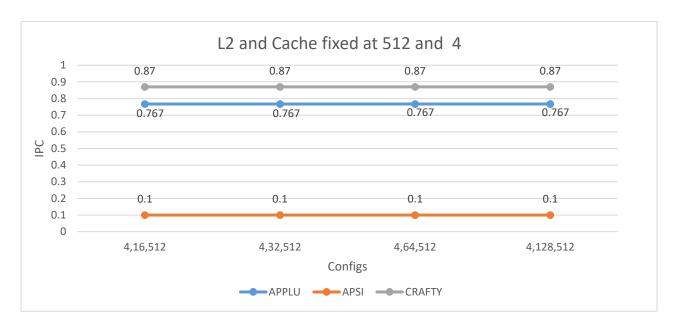
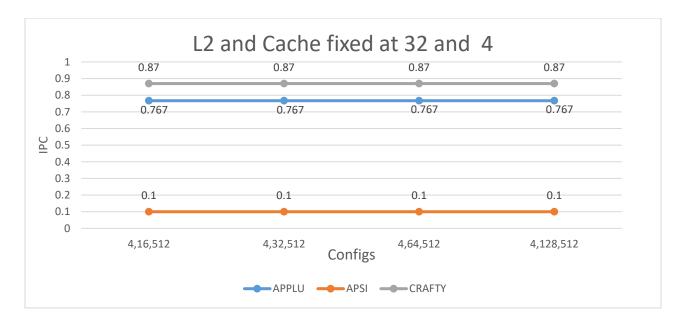
Assignment 4 Report

1. IPC as a function of L1 cache size and associativity when L2 cache size and associativity is 512KB and 4.



2. IPC as a function of L1 cache size and associativity when L2 cache size and associativity is 32KB and 4.



3. Configuration with the highest IPC.

Benchmark	IPC
Applu	0.767
Apsi	0.1
Crafty	0.87

4. Configuration with highest IPC/Power

Benchmark	IPC/Power	Configs
Applu	0.456755684	4 32 64
Apsi	0.059550937	4 32 64
Crafty	0.518093149	4 32 64

- 5. No regression models done
- 6. For **Applu** the IPC values remain constant at 0.767 for almost 99% of L1 and L2 cache size and associativity except for a few values. I don't see a pattern for IPC/Total power values. The values increase and decrease randomly.

For **Apsi** also as we go on changing the L1 and L2 cache size and associativity, IPC values are constant throughout at 0.1. The IPC/Power values are again random and increase and decrease as the configuration value changes.

For **Crafty**, as we go on changing the L1 and L2 cache size and associativity, the IPC/Power values gradually decrease and increase randomly again. IPC value also gradually increases and decreases for as the values of the L1,L2 and associativity change.