

# **FPGA RADIATION HARDENING**

## **SEMINAR REPORT**

*Submitted by*

**AJMAL M ALI**

*in partial fulfillment for the award of the degree*

*of*

**Master of Technology**

*in*

**VLSI AND EMBEDDED SYSTEMS**

*of*

**COCHIN UNIVERSITY OF SCIENCE AND TECHNOLOGY**



**DEPARTMENT OF ELECTRONICS ENGINEERING**

**MODEL ENGINEERING COLLEGE**

**COCHIN 682 021**

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**MODEL ENGINEERING COLLEGE**  
**THRIKKAKARA, KOCHI-21**  
**DEPARTMENT OF ELECTRONICS ENGINEERING**  
**COCHIN UNIVERSITY OF SCIENCE AND TECHNOLOGY**

***BONAFIDE CERTIFICATE***

***This is to certify that the SEMINAR report entitled***

**FPGA RADIATION HARDENING**

***Submitted by***

**AJMAL M ALI**

***is a bonafide account of the work done by him under our supervision***

***Dr. Jayasree V K***

***HOD &***

***Seminar coordinator***

***Mr. Jagadeesh Kumar P***

***Seminar guide***