

Product Backlog

Sr. No.	Sprint	Task	Comments	Estimated time	Acutal time
1	Sprint-1	SIP	Create SIP logs	0.50	1.00
2		CLD	Draw class diagrams	1.00	1.50
3		ACD	Draw activity diagram	0.50	1.00
4		INT	Initialize repository	1.00	0.50
5		CCS	Create class stubs	1.50	2.50
6		CTS	Create test stubs	3.00	4.50
7		SCB	Setup CI badge	0.25	0.50
8		CRM	Create readme file	0.50	0.25
9		URB	Update release backlog	0.25	0.17
10	Sprint-2	ICL	Implement classes	7.50	8.92
11		SCA	Run static code analysis tools	1.50	1.00
12		TAC	Run travis and coverall	0.30	0.50
13		UCD	Update UML diagrams	1.00	0.50
14		URM	Update readme file	0.50	0.75
15		DOR	Create doxygen report	0.50	0.17
16		URB	Update release backlog	0.25	0.17
			Total time	20.05	23.92

Sub. No.	Task	Sub Task	Comments	Estimated time	Acutal time
5.1	CST	CMCS	Create Map class stub	0.35	1.50
5.2		CRCS	Create RandomPlanner class stub	0.35	0.50
5.3		COCS	Create OptimalPlanner class stub	0.35	0.33
5.4		CDCS	Create Output class stub	0.45	0.17
Total time:				1.50	2.50
6.1	TST	CMTS	Create Map class test stub	1.00	1.50
6.2		CRTS	Create RandomPlanner class test stub	0.75	1.00
6.3		COTS	Create OptimalPlanner class test stub	0.75	1.00
6.4		CDTS	Create Output class test stub	0.50	1.00
Total time:				3.00	4.50
10.1	ICL	IMCS	Implement Map class	1.50	0.75
10.2		IRCS	Implement RandomPlanner class	1.00	4.50
10.3		IOCS	Implement OptimalPlanner class	3.00	3.17
10.4		IDCS	Implement Output class	2.00	0.50
Total time:				7.50	8.92

Time Log

Process Enactment						Interrupt		
Sr. No.	Task	Comments	Date	Start	End	#	Time	Size
1	SIP	Create SIP logs	11/9/2018	10:00:00 PM	11:30:00 PM	1	0.50	
2	CLD	Draw class diagrams	11/9/2018	3:00:00 PM	4:30:00 PM			
3	ACD	Draw activity diagram	11/9/2018	1:00:00 PM	2:30:00 PM	1	0.50	
4	INT	Initialize repository	11/9/2018	6:00:00 PM	6:30:00 PM			
5	CCS	Create class stubs				1	0.50	
5.1	CMCS	Create Map class stub	11/9/2018	6:30:00 PM	9:00:00 PM	2	1.00	
5.2	CRCS	Create RandomPlanner class stub	11/10/2018	11:00:00 AM	11:30:00 AM			
5.3	COCS	Create OptimalPlanner class stub	11/10/2018	4:30:00 PM	4:50:00 PM			
5.4	CDCS	Create Output class stub	11/10/2018	5:00:00 PM	5:10:00 PM			
6	CTS	Create test stubs						
6.1	CMTS	Create Map class test stub	11/22/2018	7:00:00 PM	8:30:00 PM			
6.2	CRTS	Create RandomPlanner class test stub	11/23/2018	12:30:00 AM	2:00:00 AM	1	0.50	
6.3	COTS	Create OptimalPlanner class test stub	11/23/2018	3:00:00 AM	4:00:00 AM			
6.4	CDTS	Create Output class test stub	11/22/2018	8:00:00 PM	9:00:00 PM			
7	SCB	Setup CI badge	11/23/2018	4:30:00 PM	5:00:00 PM			
8	CRM	Create readme file	11/23/2018	5:00:00 PM	5:15:00 PM			
9	URB	Update release backlog	11/23/2018	5:30:00 PM	5:40:00 PM			
10	ICL	Implement classes						
10.1	IMCS	Implement Map class	11/24/2018	5:00:00 PM	6:00:00 PM	1	0.25	
10.2	IRCS	Implement RandomPlanner class	11/24/2018	6:00:00 PM	11:00:00 PM	1	0.50	
10.3	IOCS	Implement OptimalPlanner class	11/26/2018	6:20:00 PM	10:30:00 PM	2	1.00	
10.4	IDCS	Implement Output class	11/24/2018	7:30:00 PM	8:00:00 PM			
11	SCA	Run static code analysis tools	11/26/2018	10:50:00 PM	11:50:00 PM			
12	TAC	Run travis and coverall	11/27/2018	12:00:00 AM	1:00:00 AM	1	0.50	
13	UCD	Update UML diagrams	11/27/2018	1:00:00 AM	3:00:00 AM	3	1.50	
14	URM	Update readme file	11/27/2018	3:00:00 AM	4:00:00 AM	1	0.25	
15	DOR	Create doxygen report	11/27/2018	3:30:00 AM	3:40:00 AM			
16	URB	Update release backlog	11/27/2018	4:00:00 AM	4:10:00 AM			

Note: : Highlighted rows indicate subtasks

Defect Log

Defect Found						Origin		
Defect No	Date	Time	Task	Location	Description	Date	Task	Fixed
01	11/24/2018	5:55:00 PM	ICL	MapTest.cpp	In actionTest case, there was an extra while loop	11/24/2018	CTS	11/24/2018
02	11/26/2018	8:00:00 PM	IOCS	main.cpp	In manual mode the world storing way was wrong	11/26/2018	ICL	11/26/2018

Release Backlog

Plan after X hours of work	0	11.92	23.92
1: Create SIP logs	0.50	1.00	1.00
2: Draw class diagrams	1.00	1.50	1.50
3: Draw activity diagram	0.50	1.00	1.00
4: Initialize repository	1.00	0.50	0.50
5: Create class stubs	1.50	2.50	2.50
6: Create test stubs	3.00	4.50	4.50
7: Setup CI badge	0.25	0.50	0.50
8: Create readme file	0.50	0.25	0.25
9: Update release backlog	0.25	0.17	0.17
10: Implement classes	7.50	9.50	8.92
11: Run static code analysis tools	1.50	2.00	1.00
12: Run travis and coverall	0.30	0.30	0.50
13: Update UML diagrams	1.00	0.50	0.50
14: Update readme file	0.50	0.50	0.75
15: Create doxygen report	0.50	1.00	0.17
16: Update release backlog	0.25	0.25	0.17
Remaining effort	20.05	14.05	0.00
Total effort needed to reach the goal	20.05	25.97	23.92