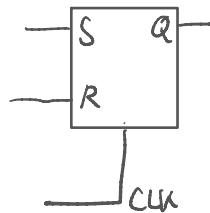


RS Flip-flop

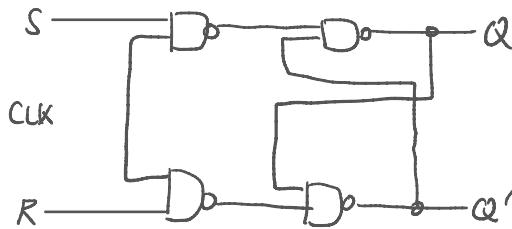


$CLK=0$ 이면, S(set) 과 R(reset) 에 무관하게 Q는 이전 값을 유지한다.

$CLK=1 \rightarrow [S=0, R=1 \rightarrow Q=0 \text{ (리셋)}$

$S=1, R=0 \rightarrow Q=1 \text{ (셋)}$

$S=1, R=1 \rightarrow \text{불안정}$



(회로도)