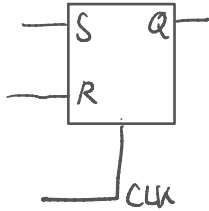


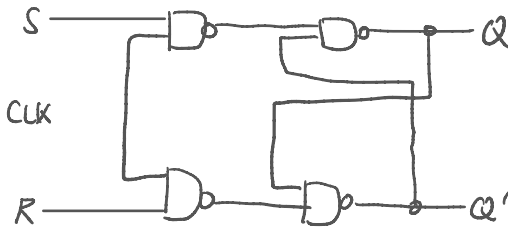
## RS Flip-Flop



$CLK=0$  이면, S(set)과 R(Reset)에 무관하게 Q는 이전 값을 유지한다.

$CLK=1 \rightarrow$

- $S=0, R=1 \rightarrow Q=0$  (리셋)
- $S=1, R=0 \rightarrow Q=1$  (셋)
- $S=1, R=1 \rightarrow$  불허



(회로5)