

# Rodrigo Valença

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## Experiences

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- **Electrical Impedance Tomography Prototype** **UFPE**  
*Researcher* *July 2016–Present*  
The techniques for the generation of tomography images through electric excitation and construction, and improvement, of the prototype were reviewed, based on the Open Hardware and Open Source paradigm.
- **Embedded System Competition** **SBESC**  
*Participant* *November 2017*  
Guided by Professor Edna Barros and jointly with Igor de Moura and Augusto Queiroz, focusing on IoT development using the SIGFOX communication tool and NXP, and Arduino, micro-controller boards.
- **FleetHive** **CIn**  
*Developer* *May 2017–Present*  
Vehicle data management tool to help the management of truck fleets. It uses IoT techniques, microcontrollers and it was necessary to develop a web platform for control and manipulation of the data obtained.
- **Codecup** **CITi+Stone**  
*Developer* *March 2018*  
Hackaton with the theme "Security". In it, a telephone call authentication tool was developed, based on voice matching and speech transcription APIs, and data organization to facilitate searches and analysis.

## Education

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Academic Qualifications.....

- **Mechanical Engineer** **Recife**  
*UFPE, Incomplete graduation* *March 2015–August 2016*
- **Computer Engineer** **Recife**  
*UFPE, Internal Transfer* *July 2016–December 2021*

Languages.....

- **Portuguese** *Native*
- **English** *Fluent*

GPA.....

- **7.76**

## Technical abilities

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- **Programming Languages:** C, C++, Octave, Python, Arduino, HTML5, CSS3, Javascript.
- **Softwares:** Weka, Octave, Unity.