Accomplishments & Retardness

14-06-15, 12:48 AM

* Made dual channel using interrupt of timer ( sampling time interval known)
* Failed to implement interrupt with both Timer0 and ADC.

19-06-15,02:13 PM

* Added dual channel
* Added FFT for both channels
* Added DAC out ( outputs predefined sequence of voltages on DAC Vout pin), with (periodic)
* Added xy plot
* Made grid system
* Not made values to display
* Tested frequency spectrum with sweep and condenser mic
* Also checked xy plot
* Limitation : lower update rate, but better than any previous trials