

The Atlys API only supports register read. custom development of FPGA code is required if we want to read raw rgb which is not a small work. But just to prove the that it works i have modified the register read FPGA code and integrated it with hdmi2usb verilog code. this code outputs X and Y resolution of image. which it calculates while the image is being displayed. to read these two 16 bit registers Digilent Adept can be used as shown in the diagram below. and follow the steps

1- enter the register no 0-3 in address space.

2- Hit Read all

3- Reg 0 is Most significant byte of X and Reg 1 is Least significant. (0x0400 = 1024)

4- Reg 2 is Most significant byte of Y and reg 3 is Least significant. (0x0300 = 768)

Link for Adept

[http://www.digilentinc.com/Data/Products/adept2/digilent.adept.system\\_v2.10.2.exe](http://www.digilentinc.com/Data/Products/adept2/digilent.adept.system_v2.10.2.exe)

