

**MODEL ENGINEERING COLLEGE THRIKKAKARA**

**Tender No. 24/2014-15: Detailed Specification**

<u>Item No.</u>	<u>Specification</u>	<u>Quantity</u>
1.	<p><b><u>Xilinx arm dual-core Cortex A9 MP core based Zynq -7000 SoC video and Imaging Kit</u></b></p> <p>ZC702 Evaluation Board with XC7Z020 SoC Accessories, IR cut filter, XADC header Video FMC board with HDMI i/p, HDMI o/p and image sensor i/p Image Sensor (<i>Camera</i>) with triggered global shutter and rolling shutter Tripod, AMS Evaluation Board Compatible Power Supply for the board SD card with bootable images of Open source Linux, Open source Android and Free RTOS Software.</p> <p>Full seat ISE® Design Suite: Embedded Edition Documentation, Reference Designs, Design Examples and Demos, Board Design Files.</p> <p><i>Warranty must be specified</i></p>	1 No.
2.	<p><b><u>Virtex-6 FPGA ML605 Evaluation Kit ( Higher version of Virtex-5 LX)</u></b></p> <p><u>Hardware:</u> ML605 XC6VLX240T-1FFG1156 Evaluation Board Cables &amp; Power Supply, Universal 12V Power Supply , 2 USB Cables, Ethernet Cable DVI-VGA Adaptor</p> <p><u>Software:</u> ISE® Design Suite: Logic Edition, Documentation, Hardware Setup Guide, Getting Started Guide, Hardware Users Guide, Reference Design and Example User Guide, Schematics and PCB Files, Reference and Example Designs and Demos, Base Reference Designs, Base Reference Design Interface and Demo, Board Diagnostic Demo, PCI Express® Gen2 (x4) Test Design, PCI Express Gen 1 (x8) Test Design, DDR3 Memory Interface Design, ChipScope™ Pro Serial IO Toolkit IBERT Transceiver Test Design, MultiBoot Reference Design featuring Fail Safe.</p> <p><i>Warranty must be specified</i></p>	1 No.

Cost of Tender Form: Original – Rs.500/- (VAT @5% extra)

*Date and time of receipt of tender* : 20.1.2015 11am

*Date and time of opening of tender* : 20.1.2015 2pm

*Last date of sale of tender form* : 19.1.2015 3pm

Sd/-

PRINCIPAL