MODEL ENGINEERING COLLEGE THRIKKAKARA

Tender No. 25/2014-15: Detailed Specification

<u>Item No.</u> <u>Specification</u> <u>Quantity</u>

1. **Function Generator**

15 Nos.

Frequency Range: 0.3 Hz to 3MHz. Amplitude: >10 Vpp ($into 50 \Omega$ load).

Impedance: $50 \Omega + /_{10}\%$. Attenuation: $-20 \text{ dB} \times 2$.

DC offset control: -5V t0 + 5V (into $50\Omega load$).

Duty cycle control: 80% to 20% to vary continuously.

Display: LED.

Waveforms: Sine, triangle, square, ramp, TTL and CMOS output.

External VCF function.

Built in 6 digit counter with INT/EXT function Range: 0.3 Hz to 3 M Hz.

INT/EXT AM/FM modulation.

Sweep mode operation: sweep time 0.2 to 30 sec adjustable

Mode: Linear and log.

2. **<u>Digital Multimeter</u>**

10 Nos.

Accuracy: 0.25% of measured value

Measurement: 3 ¾ digit

DC Current measurement ranges: 300 μ A (*resolution* = 100nA), up to 10 A with uniform resolution over all ranges

AC current measurement ranges: 3 mA (*resolution* = 1μ A), up to 10 A with uniform resolution over all ranges.

AC Voltage measurement ranges: 3 V (resolution = 1 mV), up to 1000 V with uniform resolution over all ranges

DC Voltage measurement: 30 mV (resolution = $10\mu V$), up to 1000 V with uniform resolution over all ranges

Resistance measurement: 30 Ω (resolution = 10 m Ω), up to 30 M Ω with uniform resolution over all ranges

Frequency measurement: 300 Hz (resolution = 0.1 Hz) to 100 kHz with uniform resolution over all ranges

Capacitance measurement: 30 nF (resolution = 10 pF) to 30μ F with uniform resolution over all ranges.

Diode and Continuity Testing, Autoranging/ Manual range selection, Terminal protection for over range, over voltage and current Protective Rubber Holster for rough duty with stand facility, Storage and updation of MIN/MAX values.

3. **Digital IC tester**

1No.

Testing of TTL, CMOS logic ICs (74 series, 40 series, 45 series of CMOS, microprocessors, RAM, etc)

Display: 16 digit LCD/LED, Indicate results of IC good or bad. Facility to identify unknown or unidentifiable ICs, Universal ZIF socket, Auto search facility, Easy operation tester, Loop test facility

4. <u>Digital IC Trainer kit</u>

15 Nos.

Logic input switches providing high/low indicator for status of switches (10 nos)

Logic output indicators (LED) with lamp drivers (10 nos)

Seven segment display with interface.

Fixed X-tal clock generator having output of 1 Hz, 10 Hz, 100 Hz, 1 KHz, 10 KHz, 100KHz and 1 MHz.

Variable clock providing output of 1 Hz to 100 KHz Monopulse clock 20 pin ZIF IC sockets (2 nos)

Solderless bread board with 1680 tie points.

Cost of Tender Form: Original – Rs.1000/- (VAT @5% extra)

Date and time of receipt of tender : 20.1.2015 11am

Date and time of opening of tender : 20.1.2015 2pm

Last date of sale of tender form : 19.1.2015 3pm

Sd/-

PRINCIPAL