TCET
DEPARTMENT OF ELECTRONICS ENGINEERING (ETRX)
Credit Based Grading System [CBGS - 2012(R)] / Choice Based Credit and Grading Scheme [CBCGS - 2016(R)]
CBCGS-2016(R)

DEPARTMENT OF ELECTRONICS ENGINEERING

TCET/FRM/IP-02/02(Class)	A. Y.2018_19(ODD SEM)

		CLASS: TIME-TABLE	DATE: 27/06/18
CLASS:S.E-ETRX-(SEM III)	W.E.F: 16/07/2018	Room No: C-101	Revision: A
			CLASS INCHARGE:Ms. Leena Chakraborty

DAYS/TIME	8:30 AM TO 9:30 AM	9:30 AM TO 10:30 AM	10:30 AM TO 11:30 AM	11:30 AM TO 11:45 AM	11:45 AM TO 12:45 PM	12:45 PM TO 1:45 PM	1:45 PM TO 2:30 PM	2:30 PM TO 3:30 PM	3:30 PM TO 4:30 PM	4:30 PM TO 5:30 PM	5:30 PM TO 6:30 PM
MONDAY	TBL (SK) 117	OOPM(E1-CMPN)113 EDC-I(E2-SB)116 DCD(E3-JK)117		SHORT BREAK	ENAS (AB)	ENAS (AB)	LUNCH BREAK	EDC-I(SK)	AM-III (Dr.VA)	PBL(E1-SB) 117	CL/SL
TUESDAY	PBL (E2-AB) 116	OOPM(CMPN)	EDC-I (PW)		DCD(E1-JK)117 ENAS(E2-OOPM(E3-CMPN)113 AB)114			ENAS (AB)	AM-III (Dr.VA)	EIM(SB)	CL/SL
WEDNESDAY	PBL (E3-JK) 117	AM- III (Dr.VA)	EIM(SB)		EDC-I(E1-PW)116 ENAS(E3-AB)114 DCD(E2-JK)117			DCD(JP)	DCD(JP)	EIM(SB)	CL/SL
THURSDAY	TBL (JP) 117	EIM(SB)	EDC-I (PW)		EDC-I (SK)	DCD(JK)		ENAS (AB)	Zero Hour	OOPM(CMPN)	CL/SL
FRIDAY	CL/SL	DCD(JK)	AM- III (Dr.VA)		ENAS(E1-AB)114 OOPM(E2-CMPN)113 EDC-I(E3-SB)116			AM- III (T)(Dr.VA)	Activity Based Learning (DA/PSR/PPD/TPS/RAC/ILA)		CL/SL
SATURDAY	CL/SL/Consultation Hours / Practice Session										

Venue mentioned in the time table should be strictly followed.	DA: Domain Activity	PSR:ProgramSpecific Research activity
All the Students must be present 15 min. before the start of the first lecture.	PPD: Professional Personality Development	TPS: Tutorial /Practice session
Last day of Semester: 20/10/18	ILA: Institute Level Activity	RAC: Remedial Assignment Compliance
Class Strength:63	SL: Self Learning	CL: Collaborative Learning
	TBL: Technology Based Learning	PBL: Project Based Learning

Sr.No.	Abbreviation	Name of the subject with abbreviation	Name of the Faculty with abbreviation
1	EDC-I	Electronics Devices and Circuits -1	PW: Poorva W., SK:Sumit Kumar
2	DCD	Digital Circuits andDesign	JK:Jyoti K., JP:Jalpa P.
3	ENAS	Electrical Network Analysis and Synthesis	AB:Archana B.
4	OOPM	ObjectOrientedProgrammingMethodology	CMPN
5	AM-IIII	Applied Mathematics – III	VA:Dr.Vinita A.
6	EIM	Electronic Instruments andMeasurements	SB: Sonal B.
7	TBL	TBL: Technology Based Learning: PCB Designing	SK:Sumit Kumar, JP:Jalpa P.
8	PBL	PBL: Project Based Learning	AB:Archana B., JK:Jyoti K, SB: Sonal B.

*SL/CL:resoures will be allocated as per request

Theory (22)+ Practical (8) + Tuts (1) +ABL (2) • +TBL (2)+PBL (3)+Zero Hour (1)+ Self Learning (6) = 45 hrs.

Collectively Proposed by:	Reviewed & Recommended by:	Approved By:
Mrs. Archana B., Ms.Sujata A. AP ETRX	Dr. S.C. Patil Dy. HOD	Dr. Sandhya Save HOD ETRX	Dr. R. R. Sedamkar Dean Academic	Dr. Deven Shah Vice-Principal	Dr. B. K.Mishra Principal