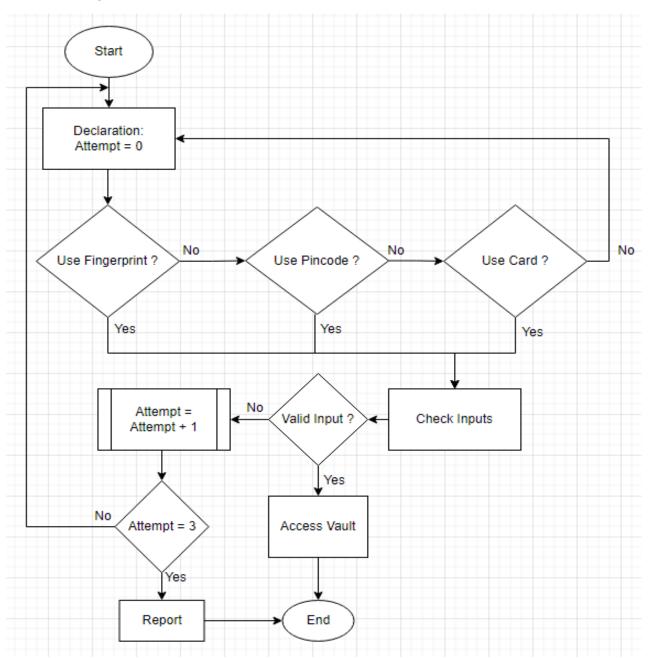
## **MIDTERM EXAM (Group 2)**

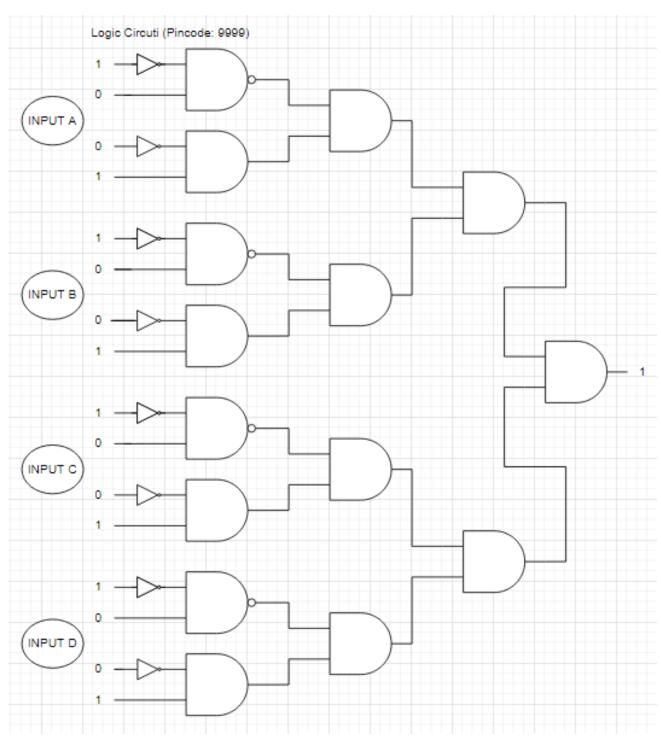
- Manuel, Leomhar
- Monacillo, Sean David Christopher
- Narag, Avor John
- Rilan, Michael Angelo
- Salvador, Nigelle Jarred

Test 1: Illustration (20 points)

### Flowchart Diagram



# Logic Circuit Diagram



Test II. Mapping

Address	Uses	
FFFF	ROM	SYSTEM PROGRAM
E000		
DFFF	RAM	VIDEO RAM
D800		
D7FF	UNUSED	
4000		
3FFF	RAM	USER RAM
0400		
03FF	RAM	FAST INSTRUCTIONS
0000		

Test 3: Programming (40 points)

ADDRESS	OPCODE							
4100	0E							
4101	00							
4102	21							
4103	00							
4104	42							
4105	7E							
4106	23							
4107	86							
4108	D2							
4109	0C							
410A	41							
410B	0C							
410C	23							
410D	77							
410E	23							
410F	71							
4110	21							
4111	00							
4112	42							
4113	7E							
4114	23							
4115	46							

4116	90
4117	D2
4118	1B
4119	41
411A	0C
411B	21
411C	04
411D	42
411E	77
411F	23
4120	71
4121	76

in		out				
4200	25	4202	49			
4201	24	4203	00			
		4204	01			
		4205	00			

#### **Screenshots:**

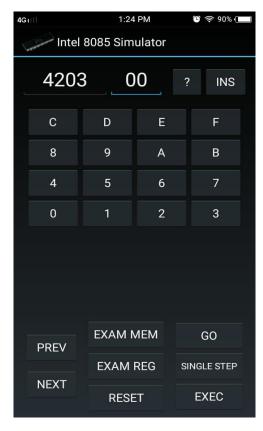
ln





Out









## Test IV. Addressing

- (a) 512bytes memory chip would have 9 address lines plus the three inputs for decoder chip resulting to 4 unused lines since the data will be stored in 16-bit address 6000H. 2<sup>4</sup>= 16 image addresses
- (b) The image addresses are: 6000, 6200, 6400, 6600, 6800, 6A00, 6C00, 6E00, 7000, 7200, 7400, 7600, 7800, 7A00, 7C00, 7E00

Table

A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	<b>A</b> 1	A0	Address
0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	6000
0	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	6200
0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	6400
0	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	6600
0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	6800
0	1	1	0	1	0	1	0	0	0	0	0	0	0	0	0	6A00
0	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	6C00
0	1	1	0	1	1	1	0	0	0	0	0	0	0	0	0	6E00
0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	7000
0	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	7200
0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	7400
0	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	7600
0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	7800
0	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	7A00
0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	7C00
0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	7E00