# Austin Jones

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**Objective**: Computer Engineering Senior looking for full time position for Jan 2022. Interested in working with Embedded Systems, Operating Systems, IoT, and Networking.

## Skills

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|---|---|
| Working Knowledge: | POSIX, Xilinx MPSoCs/FPGAs, Petalinux, Intel FPGAs, Visual Studio |
| Professional Languages: | C++, C, C#, Python, Shell, VHDL |
| Recreational Languages: | Rust, Haskell, BF, Groff |
| Office Skills: | Excel, GIMP, Git, Gerrit, Visio, LaTeX, Markdown |

## Education

**University of Tennessee Knoxville** — Anticipated Graduation: Dec 2021
Bachelor of Science in Computer Engineering — Gpa: 4.0/4.0

*Current* – Aug 2019 | **Engineering Professional Practice Office** - University of Tennessee
*Student Ambassador*
Assist UT engineering students to improve resumes, interview/inter-person skills, and overall professional skill. Work with other ambassadors to facilitate networking events and career fairs.

May 2019 – Apr 2019 | **Password Manager Browser Extension** - University of Tennessee
*Data Structures and Algorithms Project*
Collaborated with a group of peers to create a Chrome extension that manages a user's passwords for multiple sites.

- Parsed web pages to find password/username fields and stored users data in cloud.
- User data encrypted with the RC4 symmetric stream based encryption algorithm.

## Work Experience

*Current* – May 2021 | **Garmin International** - Olathe, KS
*Embedded Software Intern*
Developed code for microcontroller to control power on logic for an unreleased product.

- Utilized MCU to monitor main board status and determine the power state of the board.
- Referenced PCB schematics and MCU documentation to integrate software and hardware.
- Learned the software development process of Garmin Marine.

May 2021 – Jan 2019 | **Siemens Molecular Imaging** - Knoxville, TN
*Electrical R&D Intern*
Conduct projects, both individually and collaboratively, to provide value to the ER&D team:

- Presented findings from work to a large technical audience to demonstrate the validity of a new system architecture.
- Long term projects:
  - Utilize a Xilinx MPSoC to test limitations of ARM Core processing vs an FPGA implementation to assess cost reductions.
  - Used multiple Raspberry Pis with off the shelf networking hardware to conduct data path tests and compare the bandwidth with current custom solutions.
  - Developed Firmware, Embedded Software, and Application Software for a unit test fixture that is used during production of PET electronics.
- Assist engineers with general tasks (e.g. wiring, testing, Python scripts, documentation).

## Scholarships and Honors

May 2019 — Gonzalez Family Awards for Outstanding Computer Engineering Junior
Aug 2019 - *Current* — S. T. Harris Scholarship

## Interests and Hobbies

- Raspberry Pi, Arduino, Self-Hosting, IoT Projects
- VolHacks, Custom Mechanical Keyboards, Basic Electrical Projects
- Rock Climbing, Cooking, Specialty Coffee, Digital Art