Austin Jones

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Education

University of Tennessee Knoxville	Anticipated Graduation: Dec 2021
Bachelor of Science in COMPUTER ENGINEERING	GPA: 4.0/4.0

Current
AUG 2019

Engineering Professional Practice Office - University of Tennessee
Student Ambassador

Assist UT engineering students to improve resumes, interview/inter-person skills, and overall professional skill. Work with other ambassadors to facilitate networking vents and career fairs.

APR 2019

Password Manager Browser Extension - University of Tennessee
Data Structures and Algorithms Project

Collaborated with a group of peers using Github to create a functional Chrome extension that manages a user's passwords for multiple sites. Application parsed web pages to find password/username fields and stored users data in cloud. When being entered or retrieved user data encrypted/decrypted with the RC4 symmetric stream based encryption algorithm.

Work Experience

Current
JAN 2019

Siemens Molecular Imaging - Knoxville
Electrical R&D Intern

Conduct projects, both individually and collaboratively, to provide value to the ER&D team. Work involved both product and research driven projects. Product driven work would involve sustaining, documentation, and more inter-departmental communication. Research involved proof of concept projects that would extrapolate to larger implementation.

- Presented findings from work to a large technical audience to demonstrate the validity of a new system architecture.
- Lead long term projects:
  - Utilize a Xilinx MPSoC to test limitations of ARM Core processing vs an FPGA implementation to assess cost reductions.
  - Used multiple Raspberry Pis with off the shelf networking hardware to conduct data path tests and compare the bandwidth with current custom solutions.
  - Work with a team of Engineers using GitLab to write various driver code for a custom Linux image.
  - Created a functional test to insure proprietary hardware functioned to spec.
  - Collaborated with fellow intern to make a suite of software tools to monitor systems for various needs and engineers.
- Update and maintain Engineering Documentation for various projects.
- Assist engineers with general tasks (e.g. wiring, testing, Python scripts to process files)

Skills

Working Knowledge:	Xilinx FPGAs, Petalinux, Intel FPGAS,.NET, POSIX
Professional Languages:	C++, C, C#, Python, Shell, VHDL
Recreational Languages:	Rust, Haskell, BF, Groff
Office Skills:	Excel, GIMP, Visio, LaTeX

Scholarships and Honors

MAY 2019	Gonzalez Family Awards for Outstanding Computer Engineering Junior
AUG 2019 - *Current*	S. T. Harris Scholarship
AUG 2019 - *Current*	Herschel C. and Louise Runnion Brand Engineering Scholarship