

Austin Jones

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Objective: Computer Engineering Senior looking for a summer internship 2021 or full time position for Jan 2022. Interested in working with Embedded Systems, IoT, and Networking.

Education

University of Tennessee Knoxville

Anticipated Graduation: Dec 2021

Bachelor of Science in COMPUTER ENGINEERING

GPA: 4.0/4.0

<i>Current</i> AUG 2019	Engineering Professional Practice Office - University of Tennessee <i>Student Ambassador</i> Assist UT engineering students to improve resumes, interview/inter-person skills, and overall professional skill. Work with other ambassadors to facilitate networking events and career fairs.
MAY 2019 APR 2019	Password Manager Browser Extension - University of Tennessee <i>Data Structures and Algorithms Project</i> Collaborated with a group of peers using Github to create a functional Chrome extension that manages a user's passwords for multiple sites. <ul style="list-style-type: none">• Parsed web pages to find password/username fields and stored users data in cloud.• User data encrypted with the RC4 symmetric stream based encryption algorithm.

Work Experience

<i>Current</i> JAN 2019	Siemens Molecular Imaging - Knoxville <i>Electrical R&D Intern</i> Conduct projects, both individually and collaboratively, to provide value to the ER&D team: <ul style="list-style-type: none">• Presented findings from work to a large technical audience to demonstrate the validity of a new system architecture.• Lead long term projects:<ul style="list-style-type: none">– Utilize a Xilinx MPSoC to test limitations of ARM Core processing vs an FPGA implementation to assess cost reductions.– Used multiple Raspberry Pis with off the shelf networking hardware to conduct data path tests and compare the bandwidth with current custom solutions.– Work with a team of engineers using GitLab to write various driver code for a custom Linux image.– Created a functional test to insure proprietary hardware functioned to specification.– Collaborated with a fellow intern to build a GUI Application that monitored and report the status of and entire system including all sub-assemblies.• Update and maintain engineering documentation for various projects.• Assist engineers with general tasks (e.g. wiring, testing, Python scripts to process files)
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Skills

Working Knowledge:	POSIX, Xilinx FPGAs, Petalinux, Intel FPGAS, Visual Studio
Professional Languages:	C++, C, C#, Python, Shell, VHDL
Recreational Languages:	Rust, Haskell, BF, Groff
Office Skills:	Excel, GIMP, Git, Visio, L ^A T _E X, Markdown

Scholarships and Honors

MAY 2019	Gonzalez Family Awards for Outstanding Computer Engineering Junior
AUG 2019 - <i>Current</i>	S. T. Harris Scholarship
AUG 2019 - <i>Current</i>	Herschel C. & Louise Runnion Brand Engineering Scholarship

Interests and Hobbies

- Raspberry Pi, Arduino, Self-Hosting, IoT Projects
- VolHacks, Custom Mechanical Keyboards, Basic Electrical projects
- Rock Climbing, Cooking, Coffee, Digital Art