Austin Jones

Address: 1004 Game Day Way Apt 201, Knoxville, TN – Phone: 615-962-3732
Email: ajone239@vols.utk.edu – GitHub: https://github.com/ajone239

Objective: Computer Engineering Senior looking for full time position for Jan 2022. Interested in working with Embedded Systems, Operating Systems, IoT, and Networking.

Skills

Working Knowledge:	POSIX, Xilinx MPSoCs/FPGAs, Petalinux, Intel FPGAs, ARM MCUs
Professional Languages:	C++, C, C#, Python, Shell, VHDL
Recreational Languages:	Rust, Haskell, BF
Office Skills:	Excel, GIMP, Git, Gerrit, Visio, LaTeX, Markdown, Groff

Education

University of Tennessee Knoxville — Anticipated Graduation: Dec 2021
Bachelor of Science in Computer Engineering — Gpa: 4.0/4.0

Current – Aug 2019 | **Engineering Professional Practice Office** - University of Tennessee
Student Ambassador
Assist UT engineering students to improve resumes, interview/inter-person skills, and overall professional skill. Work with other ambassadors to facilitate networking events and career fairs.

May 2019 – Apr 2019 | **Password Manager Browser Extension** - University of Tennessee
Data Structures and Algorithms Project
Collaborated with a group of peers to create a Chrome extension that manages a user's passwords for multiple sites.

- Parsed web pages to find password/username fields and stored users data in cloud.
- User data encrypted with the RC4 symmetric stream based encryption algorithm.

Work Experience

Current – May 2021 | **Garmin International** - Olathe, KS
Embedded Software Intern
Developed code for microcontroller to control power on logic for an unreleased product.

- Utilized MCU to monitor main board status and determine the power state of the board.
- Referenced PCB schematics and MCU documentation to integrate software and hardware.
- Gained strong familiarity with ARM/STM microcontrollers.

May 2021 – Jan 2019 | **Siemens Molecular Imaging** - Knoxville, TN
Electrical R&D Intern
Conduct projects, both individually and collaboratively, to provide value to the ER&D team:

- Presented findings from work to a large technical audience to demonstrate the validity of a new system architecture.
- Long term projects:
  - Utilize a Xilinx MPSoC to test limitations of ARM Core processing vs an FPGA implementation to assess cost reductions.
  - Used multiple Raspberry Pis with off the shelf networking hardware to conduct data path tests and compare the bandwidth with current custom solutions.
  - Developed Firmware, Embedded Software, and Application Software for a unit test fixture that is used during production of PET electronics.
- Assist engineers with general tasks (e.g. wiring, testing, Python scripts, documentation).

Interests and Hobbies

- Raspberry Pi, Arduino, Self-Hosting, IoT Projects
- VolHacks, Custom Mechanical Keyboards, Basic Electrical Projects
- Rock Climbing, Cooking, Specialty Coffee, Digital Art