```
PINASSIGN2
SWM PINASSIGN2 reg t
                           PINENABLE1
SWM PINENABLE1 reg t
                           PINENABLE0
SWM PINENABLE0 reg t
                           PINASSIGN5
SWM PINASSIGN5 reg t
                          PINASSIGN12
SWM PINASSIGN12 reg t
SWM PINASSIGN6 reg t
                           PINASSIGN6
SWM PINASSIGN11 reg t
                          PINASSIGN11
SWM PINASSIGN3 reg t
                           PINASSIGN3
                           PINASSIGN0
                                           SWM per t
SWM PINASSIGN0 reg t
                          PINASSIGN10
                           PINASSIGN4
SWM PINASSIGN10 reg t
                           PINASSIGN9
SWM PINASSIGN4 reg t
SWM PINASSIGN9 reg t
                           PINASSIGN7
SWM_PINASSIGN7_reg_t
                           PINASSIGN8
SWM_PINASSIGN8_reg_t
                          PINASSIGN14
SWM_PINASSIGN14_reg_t
                          PINASSIGN13
SWM_PINASSIGN13_reg_t
                           PINASSIGN1
SWM_PINASSIGN1_reg_t
```