Automated Analysis of Weak Memory Models

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Task statement

► Improve the proof-of-concept memory model-aware analysis tool Porthos [2] by extending support for the input language.

Task specification

- Study the general framework for memory model-aware analysis of concurrent programs [1];
- Review existing tools for memory model-aware analysis;
- ▶ Develop a *C compiler infrastructure* as a part of an abstract interpretation engine for the new tool PorthosC;
- ▶ Improve the SMT-encoding scheme for an arbitrary control-flow;
- ► Enhance performance, extensibility, reliability and maintainability of the tool.

Motivating example: Write-write reordering (compiler relaxations)

$\{ x=0;$	y=0; }	
P	Q	
$p_0: x \leftarrow 1$	$q_0: y \leftarrow 1$ $q_1: r_q \leftarrow x$	
$p_1: r_p \leftarrow y$	$q_1: r_q \leftarrow x$	
exists $(r_p = 0 \land r_q = 0)$		

Motivating example: Write-write reordering (compiler relaxations)

y=0; }		
Q		
$\begin{array}{ll} q_0: & y \leftarrow 1 \\ q_1: & r_q \leftarrow x \end{array}$		
$q_1: r_q \leftarrow x$		
exists $(r_p = 0 \land r_q = 0)$		

Single thread (no concurrency)

$$p_0, p_1, q_0, q_1$$
 (0; 1)
 q_0, q_1, p_0, p_1 (1; 0)

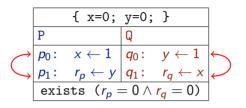
Motivating example: Write-write reordering (compiler relaxations)

	$\{ x=0;$	y=0;	}
P		Q	
<i>p</i> ₀ :	$x \leftarrow 1 \\ r_p \leftarrow y$	q_0 :	<i>y</i> ← 1
p_1 :	$r_p \leftarrow y$	q_1 :	$r_q \leftarrow x$
exists $(r_p = 0 \land r_q = 0)$			

Sequential Consistency (classical concurrency)

$$p_0, p_1, q_0, q_1$$
 (0; 1)
 q_0, q_1, p_0, p_1 (1; 0)
 p_0, q_0, p_1, q_1 (1; 1)
 p_0, q_0, q_1, p_1 (1; 1)
 q_0, p_0, p_1, q_1 (1; 1)
 q_0, p_0, q_1, p_1 (1; 1)

Motivating example: Write-write reordering (compiler relaxations)



Total Store Order (e.g., ×86)

Motivating example: Write-write reordering (compiler relaxations)

		{ x=0;	y=0;	}	
	P		Q		
\rightarrow	<i>p</i> ₀ :	$x \leftarrow 1$ $r_p \leftarrow y$ sts $(r_p = 1)$	<i>q</i> ₀ :	<i>y</i> ← 1	\
\rightarrow	p_1 :	$r_p \leftarrow y$	q_1 :	$r_q \leftarrow x$	\downarrow
	exis	sts (r_p =	$= 0 \wedge I$	$r_q = 0$	

Total Store Order (e.g., ×86)

Motivating example: Store buffering (hardware relaxations)

{ x=0;	y=0; }	
P	Q	
$p_0: x \leftarrow 1$	$q_0: y \leftarrow 1$	
$p_1: r_p \leftarrow y$	$q_1: r_q \leftarrow x$	
exists $(r_p = 0 \land r_q = 0)$		

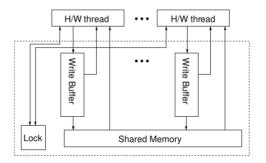


Figure: An x86-TSO abstract machine [4]

Motivating example: Store buffering (hardware relaxations)

	${x=0;}$	y=0;	}
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<i>p</i> ₀ :	$x \leftarrow 1$	<i>q</i> ₀ :	<i>y</i> ← 1
p_1 :	$r_p \leftarrow y$	q_1 :	$r_q \leftarrow x$
exists $(r_p = 0 \land r_q = 0)$			

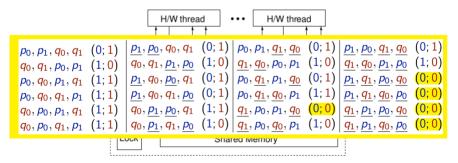


Figure: An x86-TSO abstract machine [4]

Event-based program representation

- ▶ EventСобытие $\in \mathbb{E}$, a low-level primitive operation:
 - ▶ memory event $\in \mathbb{M} = \mathbb{R} \cup \mathbb{W}$: access to a local/shared memory,
 - ightharpoonup computational event $\in \mathbb{C}$: computation over local memory, and
 - ▶ barrier event $\in \mathbb{B}$: synchronisation fences;
- ▶ Relation $\subseteq \mathbb{E} \times \mathbb{E}$:
 - basic relations:
 - ▶ program-order relation po $\subseteq \mathbb{E} \times \mathbb{E}$: (control-flow),
 - read-from relation $\mathtt{rf} \subseteq \mathbb{W} \times \mathbb{R}$: (data-flow, and)
 - ▶ coherence-order relation $co \subseteq W \times W$: (data-flow);
 - derived relations:
 - ▶ union r1 | r2,
 - sequence r1; r2,
 - transitive closure r+,
 - **>** ...:
- ▶ Assertion over relations or sets of events :
 - acyclicity, irreflexivity or emptiness.



Testing candidate executions

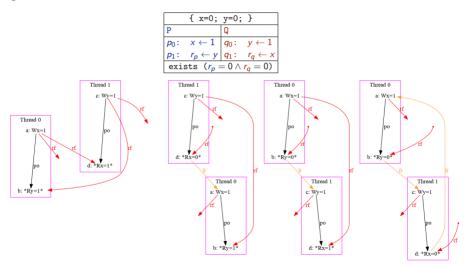


Figure: The four candidate executions allowed under x86-TSO

Testing candidate executions

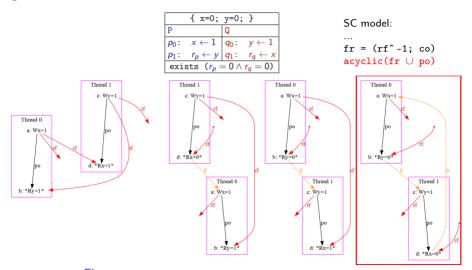


Figure: The four candidate executions allowed under x86-TSO

Testing candidate executions

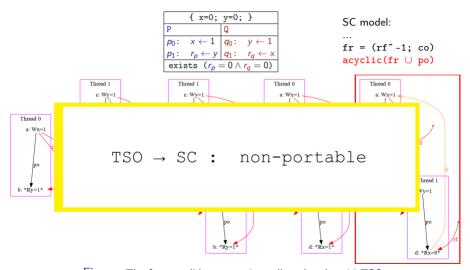


Figure: The four candidate executions allowed under x86-TSO

Portability analysis

The Porthos tool

```
/* Prints Hello World */
#include <stdio.h>
int main (void) {
    printf ("Hello World!");
    return 0;
}

x86 Compiler ARM Compiler
Optimizations

?

ARM
```

Figure: The illustration of the portability problem [3]

Portability analysis

The Porthos tool



Figure: The illustration of the portability problem [3]

Definition (Portability [2])

The program P is portable from the source memory model $\mathcal{M}_{\mathcal{S}}$ to the target model $\mathcal{M}_{\mathcal{T}}$ if $cons_{\mathcal{M}_{\mathcal{T}}}(P) \subseteq cons_{\mathcal{M}_{\mathcal{S}}}(P)$

- ▶ Portability as an SMT-based bounded reachability problem: $\phi = \phi_{CF} \land \phi_{DF} \land \phi_{\mathcal{M}_{\mathcal{T}}} \land \phi_{\neg \mathcal{M}_{\mathcal{S}}}$
- ightharpoonup SAT $(\phi) \Longrightarrow$ portability bug

Portability analysis

The Porthos tool



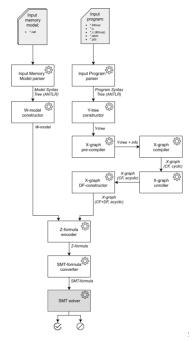
Figure: The illustration of the portability problem [3]

Definition (Portability [2])

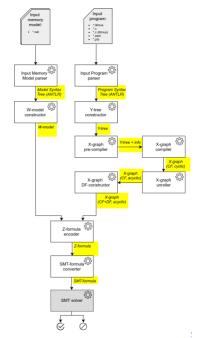
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- ightharpoonup SAT $(\phi) \Longrightarrow$ portability bug

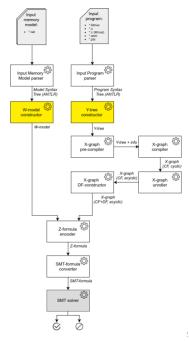
Main components



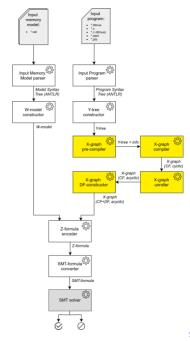
Main components



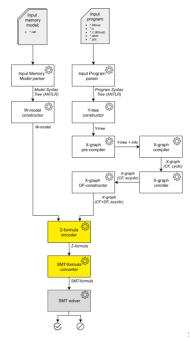
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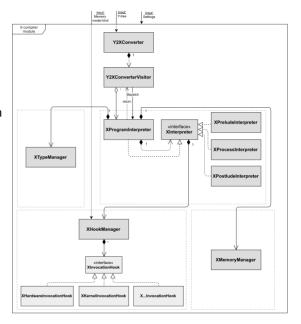
Main components



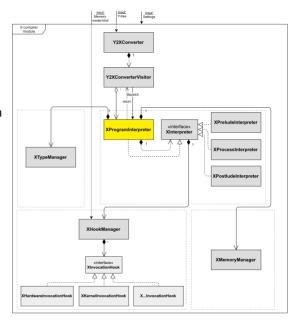
Main components



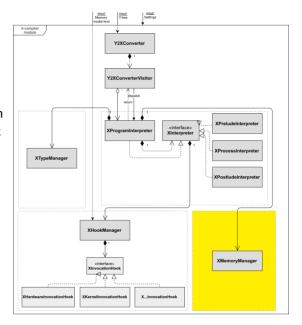
Abstract interpretation module



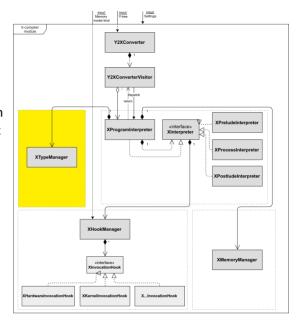
Abstract interpretation module



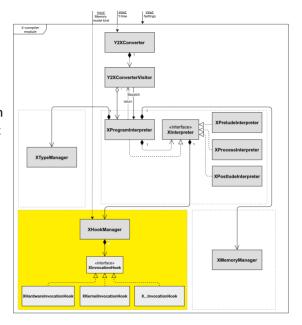
Abstract interpretation module



Abstract interpretation module



Abstract interpretation module



```
{ x, y }
thread t0 {
    r0 <- 1;
    r1 <- (2 + r0) * 3;
    y := r2;
    while (r0 > 4) {
        r0 <:- x;
        r1 <- (r0 + 5);
        x.store(_rx, r1);
        y = x.load(_rx)
    };
}
exists x = 1, y = 2, 0:r1 = 3,</pre>
```

Figure: A program example in the Porthos v1 input language

```
{ x, y }
thread t0 {
    r0 <- 1;
    r1 <- (2 + r0) * 3;
    y := r2;
    while (r0 > 4) {
       r0 <:- x;
       r1 <- (r0 + 5);
       x.store(_rx, r1);
       y = x.load(_rx)
    };
}
exists x = 1, y = 2, 0:r1 = 3,</pre>
```

Figure: A program example in the Porthos v1 input language

```
(instr)
    : (atom)
    | '(' (instr) ')'
    | (instr) ';' (instr)
    | 'while' '(' (bool-expr) ')' (instr)
    | 'if' (bool-expr) '(' (instr) ')' (instr)
    | (atom)
    : (reg) '<-' (expression)
    | (reg) '<-' (loc)
    | (loc) ':-' (reg)
    | (loc) ':-' (reg)
    | (reg) '=-' (loc) '.' 'store' '(' (atomic) ',' (reg) ')'
    | (reg) '=-' (loc) '.' 'load' '(' (atomic) ')'
    | ('mfence' | 'sync' | 'lwync' | 'isync')
    ;
}</pre>
```

Figure: A sketch of the Porthos v1 input language grammar

Static syntactic determination of the variables kind

```
{ x, y }

thread t0 {
    r0 <= 1;
    r1 <= (2 + r0) * 3;
    y := r2;
    while (r0 > 4) {
       r0 <= x;
       r1 <= (r0 + 5);
       x.store(_rx, r1);
       y = x.load(_rx)
    };
}

exists x = 1, y = 2, 0:r1 = 3,</pre>
```

Figure: A program example in the Porthos v1 input language

```
(instr)
(instr)
(instr)
(instr)';' (instr)
(instr)';' (instr)
('while''(' (bool-expr)')' (instr)
(if' (bool-expr)'(' (instr)')' (instr)
(atom)
(argg) 'sar' (expression)
( (reg) 'sar' (loc)
( (loc) 'sar' (reg)
( (reg) 'sar' (loc) '.' 'store' '(' (atomic) ',' (reg) ')'
( (reg) 'sar' (loc) '.' 'load' '(' (atomic) ')'
( 'mfence' | 'sync' | 'lwsync' | 'isync')
;
```

Figure: A sketch of the Porthos v1 input language grammar

Lack of support for functions invocations

```
{ x, y }
thread t0 {
   r0 <- 1;
   r1 <- (2 + r0) * 3;
   y := r2;
   while (r0 > 4) {
    r0 <- x;
    r1 <- (r0 + 5);
    x.store(_rx, r1);
   y = x.load(_rx)
   };
}
exists x = 1, y = 2, 0:r1 = 3,</pre>
```

Figure: A program example in the Porthos v1 input language

```
...
(instr)
: (atom)
| '(' (instr) ')'
| (instr) '; (instr)
| 'while '(' (bool-expr) ')' (instr)
| 'if' (bool-expr) '(' (instr) ')' (instr)
;
(atom)
: (reg) '<-' (expression)
| (reg) '<-' (loc)
| (loc) ':=' (reg)
| (reg) '=' (loc) '.' 'store' '(' (atomic) ',' (reg) ')'
| (reg) '=' (loc) '.' 'load' '(' (atomic) ')'
| ('mfence' | 'sync' | 'lwsync' | 'isync');
;</pre>
```

Figure: A sketch of the Porthos v1 input language grammar

Lack of support for unconditional jumps

```
{ x, y }

thread t0 {
   r0 <- 1;
   r1 <- (2 + r0) * 3;
   y := r2;
   while (r0 > 4) {
      r0 <:- x;
      r1 <- (r0 + 5);
      x.store(_rx, r1);
      y = x.load(_rx)
   };
}

exists x = 1, y = 2, 0:r1 = 3,</pre>
```

Figure: A program example in the Porthos v1 input language

```
(instr)
(instr)
('(instr)';')
(instr)';'(instr)
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```

Figure: A sketch of the Porthos v1 input language grammar

```
\{ int *x = 1; \}
extern int z;
void thread_0(int &x, int &y) {
 I.0: x = 0:
 int r;
 while (x * (5 + 4 / 2) \% 3 == 1) {
   if(x != 0)
     goto LO;
   if (v > 6)
     continue;
   else if (++v > 7) {
    r = r + 10:
     break;
   else
     goto L1:
   r = 11;
 y = x.load(_rx) + 1;
 L1: x.store(_rx, r):
exists (v == x + 1 &\& thread 0:r > 21)
```

Figure: A program example in C

```
\{ int *x = 1; \}
extern int z:
void thread_0(int &x, int &y) {
 I.0: x = 0:
 int r:
 while (x * (5 + 4 / 2) \% 3 == 1)  {
   if(x != 0)
     goto LO:
   if (v > 6)
     continue:
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 v = x.load(rx) + 1:
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Figure: A program example in C

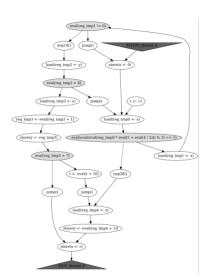


Figure: The compiled event-flow graph

Pre-compilation phase for the syntactic determination of the variables kind

```
\{ int *x = 1; \}
extern int z:
void thread_0(int &x, int &y) {
 I.0: x = 0:
 int r:
 while (x * (5 + 4 / 2) \% 3 == 1) {
   if(x != 0)
     goto LO:
   if (v > 6)
     continue:
   else if (++v > 7) {
     r = r + 10:
     break;
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     goto L1:
   r = 11;
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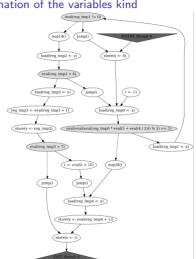


Figure: The compiled event-flow graph

Support for functions invocations: The invocation hooking mechanism

```
\{ int *x = 1; \}
extern int z:
void thread_0(int &x, int &y) {
 I.0: x = 0:
 int r:
 while (x * (5 + 4 / 2) \% 3 == 1) {
   if(x != 0)
     goto LO:
   if (v > 6)
     continue:
   else if (++v > 7) {
     r = r + 10:
     break;
   معام
     goto L1:
   r = 11;
 v = x.load(_rx) + 1:
 L1: x.store(_rx, r);
exists (v == x + 1 &\& thread 0:r > 21)
```

Figure: A program example in C

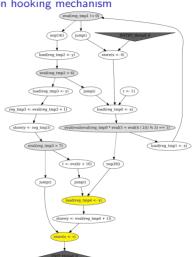


Figure: The compiled event-flow graph

Support for an arbitrary control-flow

```
\{ int *x = 1; \}
extern int z:
void thread_0(int &x, int &y) {
 I.0: x = 0:
 int r:
 while (x * (5 + 4 / 2) \% 3 == 1)  {
   if(x != 0)
     goto LO:
   if (v > 6)
     continue:
   else if (++v > 7) {
     r = r + 10:
     break;
   معام
     goto L1:
   r = 11;
 v = x.load(rx) + 1:
 L1: x.store(_rx, r);
exists (v == x + 1 &\& thread 0:r > 21)
```

Figure: A program example in C

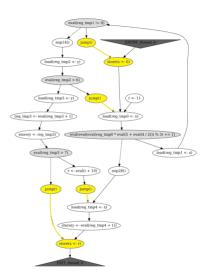


Figure: The compiled event-flow graph

PorthosC: The new control-flow encoding scheme

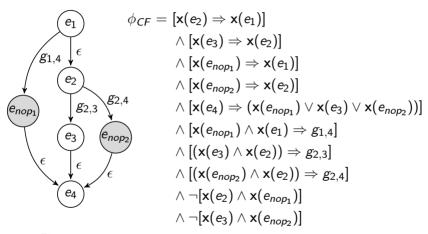


Figure: Example of encoding for the control-flow of the event-flow graph

PorthosC: The new DFS-based X-graph unrolling scheme

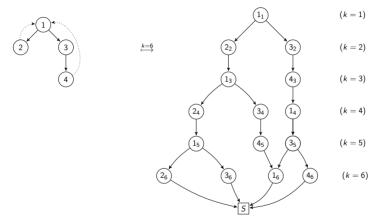


Figure: Example of the flow graph unrolling up to bound k = 6

Evaluation

The new unrolling scheme

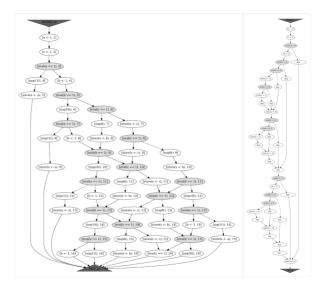
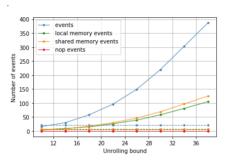
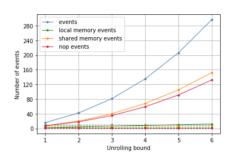


Figure: Illustration of differences in unrolling schemes of PorthosC (left) and Porthos v1 (right)

Evaluation

Overhead of the new unrolling scheme: Number of events



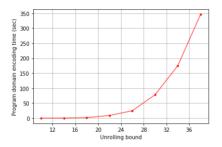


- (a) The graph unrolled by PorthosC
 - (b) The graph unrolled by Porthos v1

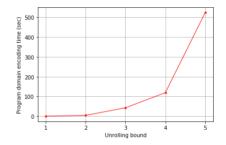
Figure: Dependency of unrolled events number on the unrolling bound k

Evaluation

Overhead of the new unrolling scheme: Execution time



(a) Program domain encoding time of the graph unrolled by PorthosC



(b) Program domain encoding time of the graph unrolled by PorthosC

Figure: Dependency of program domain encoding time (in seconds) on the unrolling bound

Summary

- ► The result of the work is the generalised framework for memory model-aware analysis PorthosC;
 - ► The *input language* has been extended to a higher subset of C language (including unconditional control-flow jumps);
 - ► The old architecture of Porthos has been revised and redesigned for PorthosC;
 - ► The *new unrolling scheme* produces complete state space of the analysing program within the user-defined bound *k*, though the encoding time growth rapidly (exponentially) as the unrolling bound grows;
 - ▶ The *invocation hooking mechanism* is an important part of the abstract interpretation engine, that serves as a knowledge base for the program domain and increases the extensibility of the tool.

Directions for future work

- ► Extending the *knowledge base* of domain-specific functions to model synchronisation primitives;
- Supporting new input languages (e.g., different assembly languages);
- Supporting complex data types (s.a. arrays, pointers, structures, etc.);
- Adding the inter-procedural analysis mode;
- ▶ Handling the *state explosion problem* (with standard model-checking techniques adjusted by the information about the weak memory model of the execution environment).

Thank you for attention



Bibliography I



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