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 This schematic has not been validated in hardware
 and may contain errors.

Versatile Clock Generator
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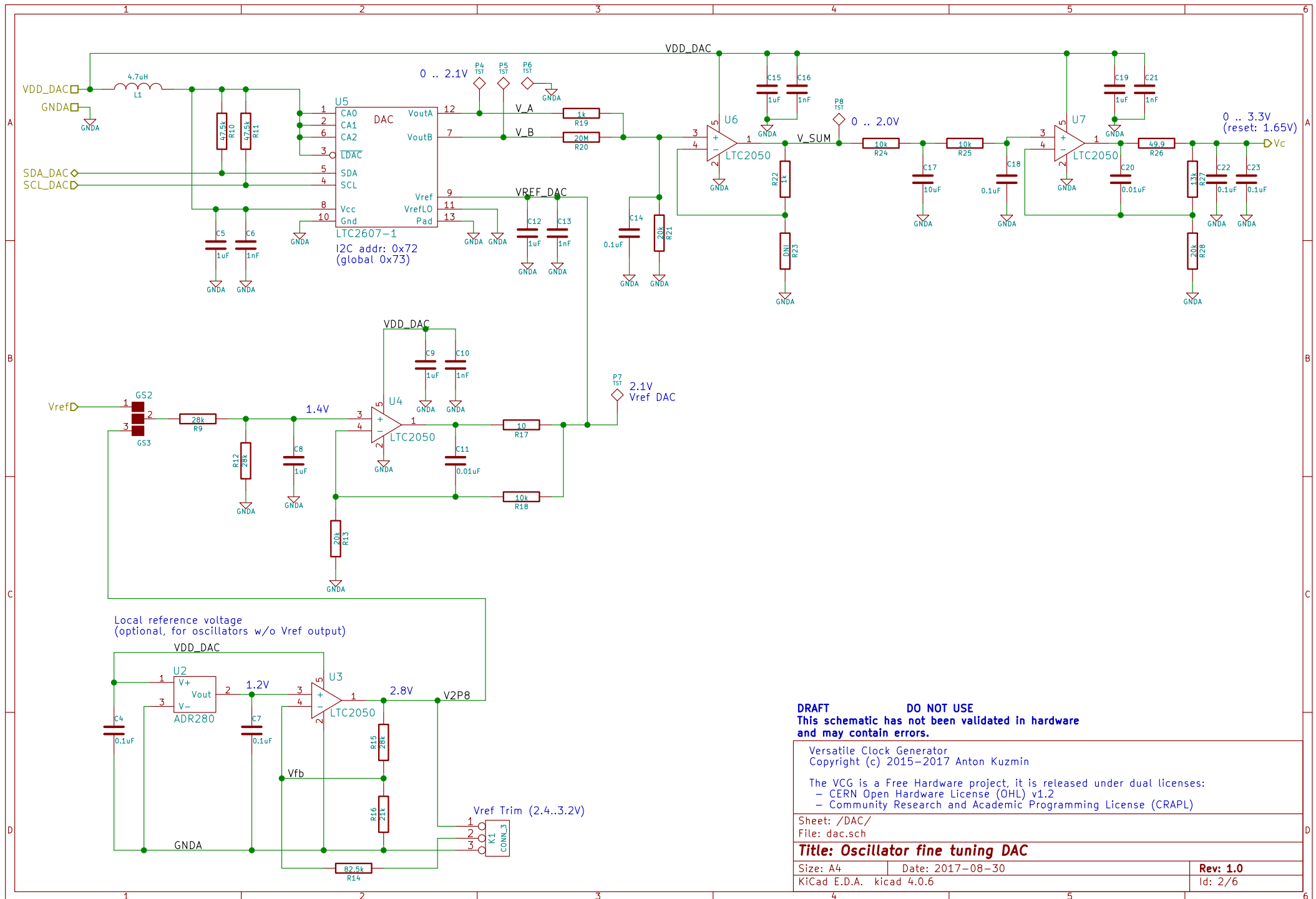
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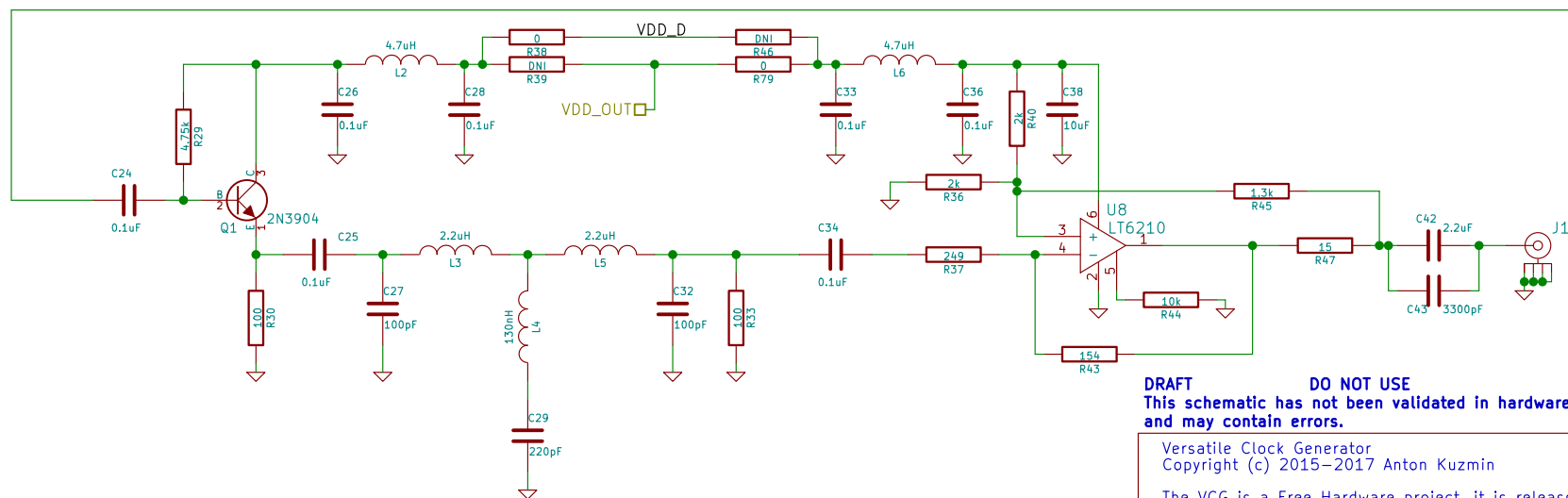
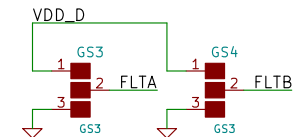
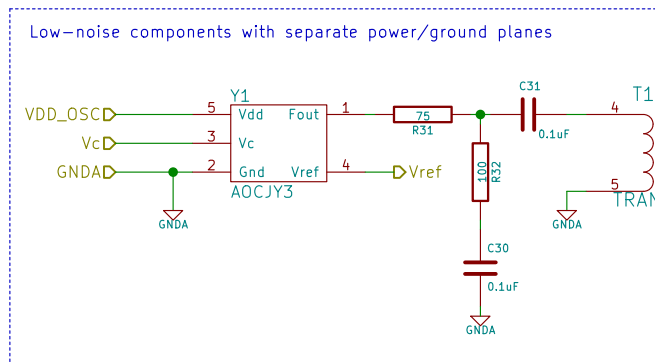
Sheet: /
 File: vcg.sch

Title: VCG structure, Controller connector

Size: A4 Date: 2017–08–30
 KiCad E.D.A. kicad 4.0.6

Rev: 1.0
 Id: 1/6





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Sheet: /Oscillator/
File: osc.sch

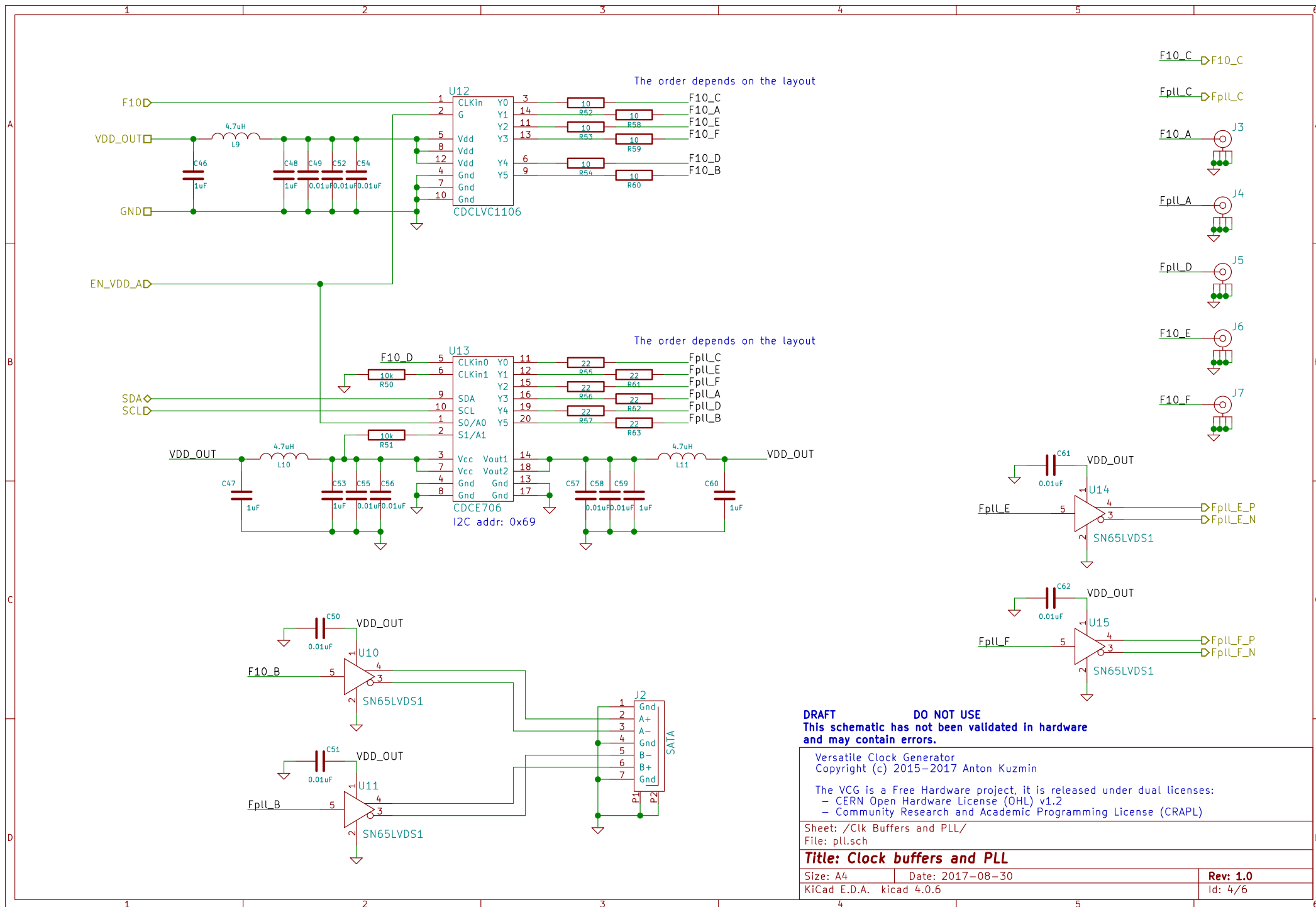
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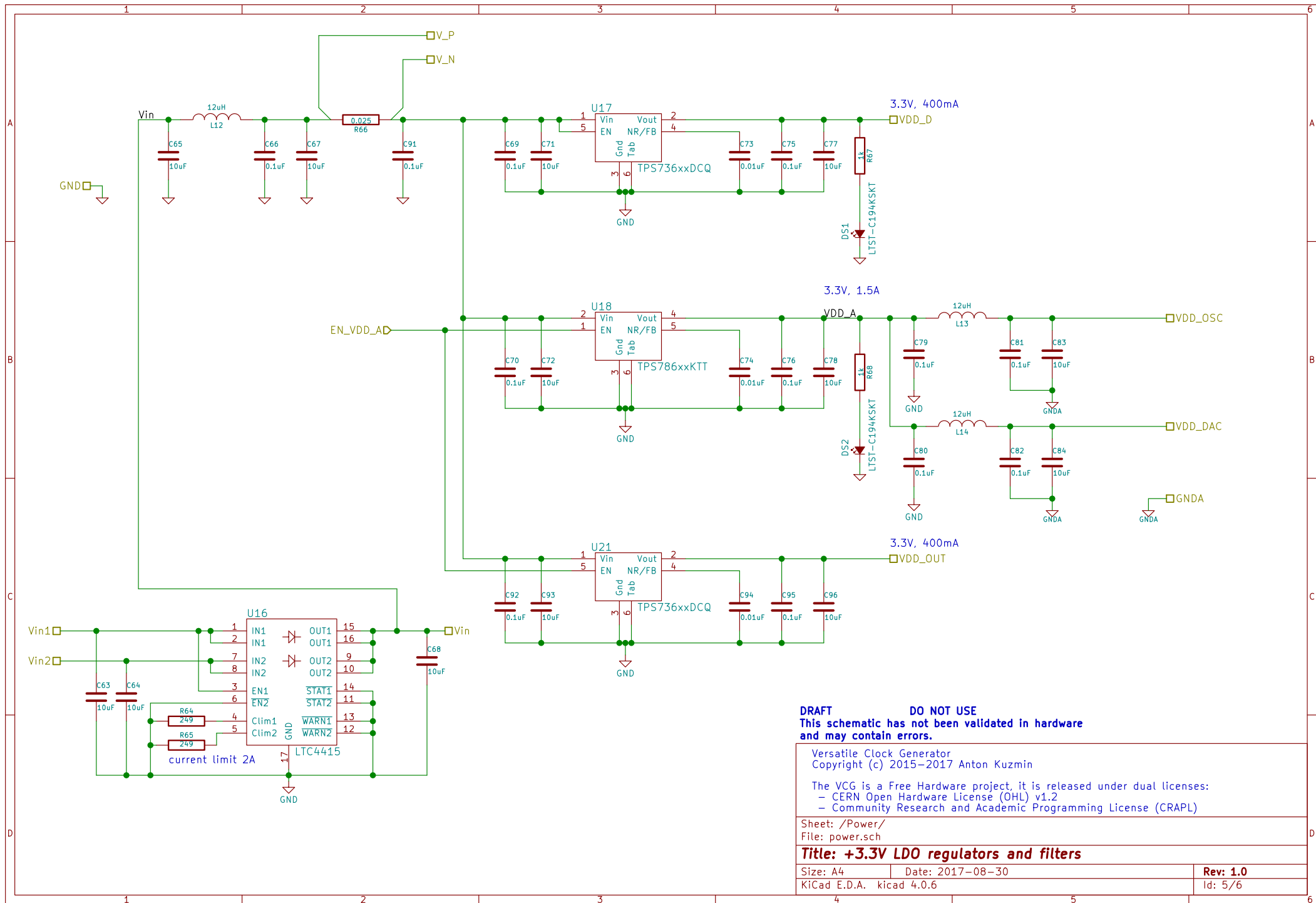
Size: A4 Date: 2017–08–30

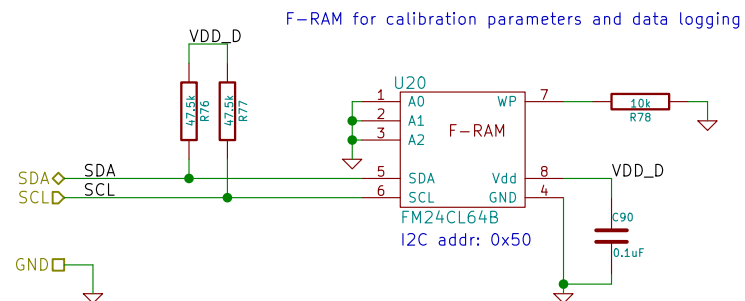
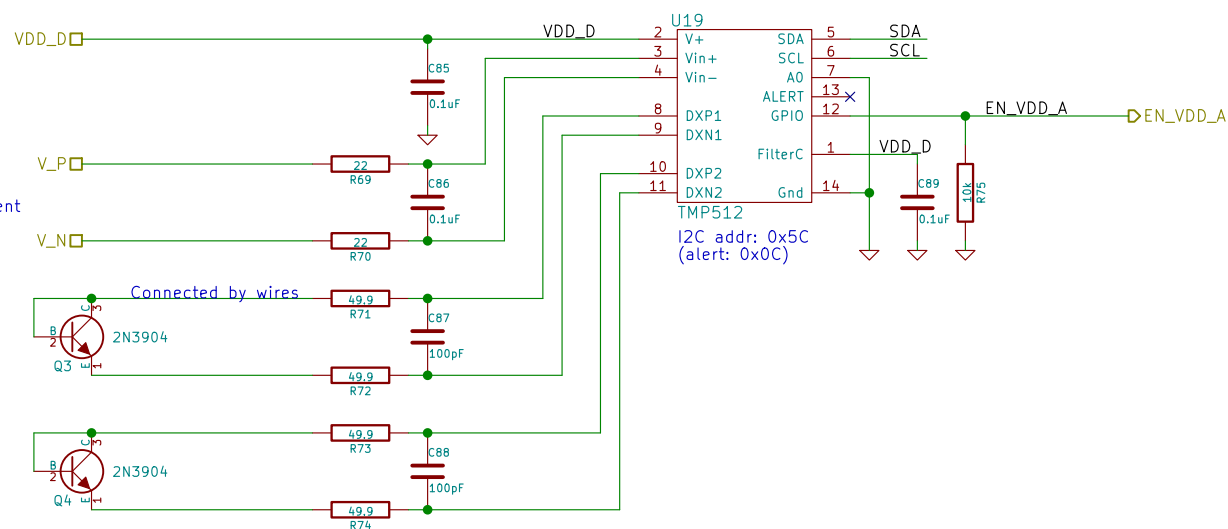
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Rev: 1.0

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Id: 6/6