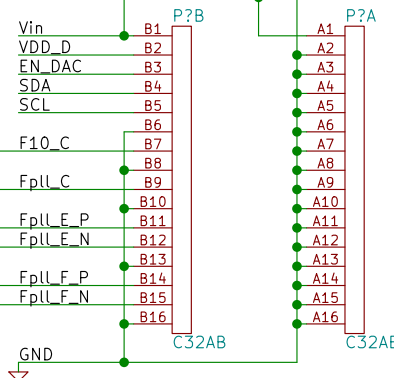
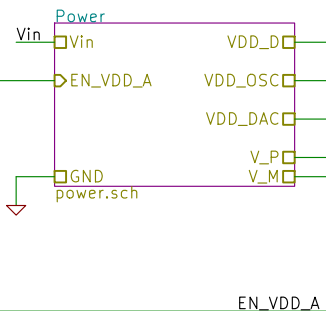
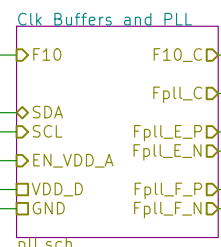
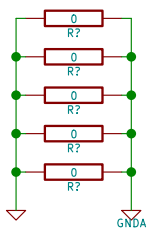
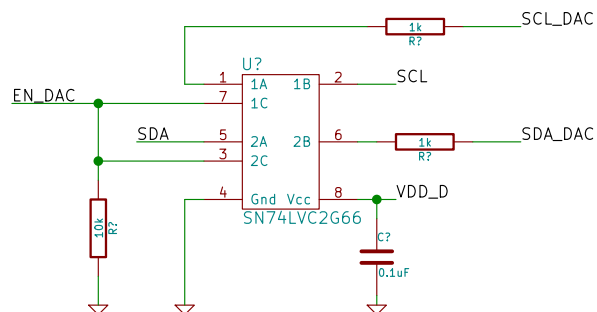
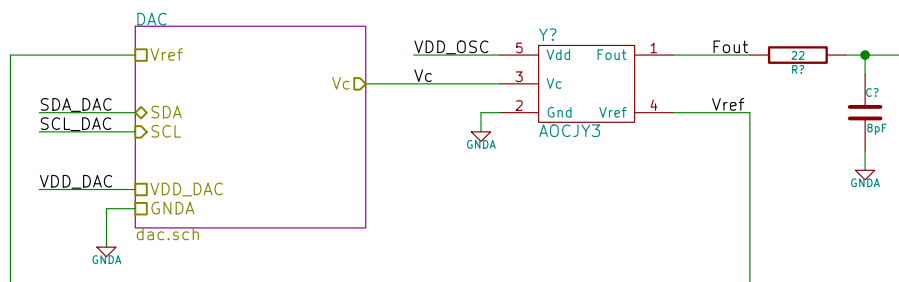


Low-noise components with separate power/ground planes

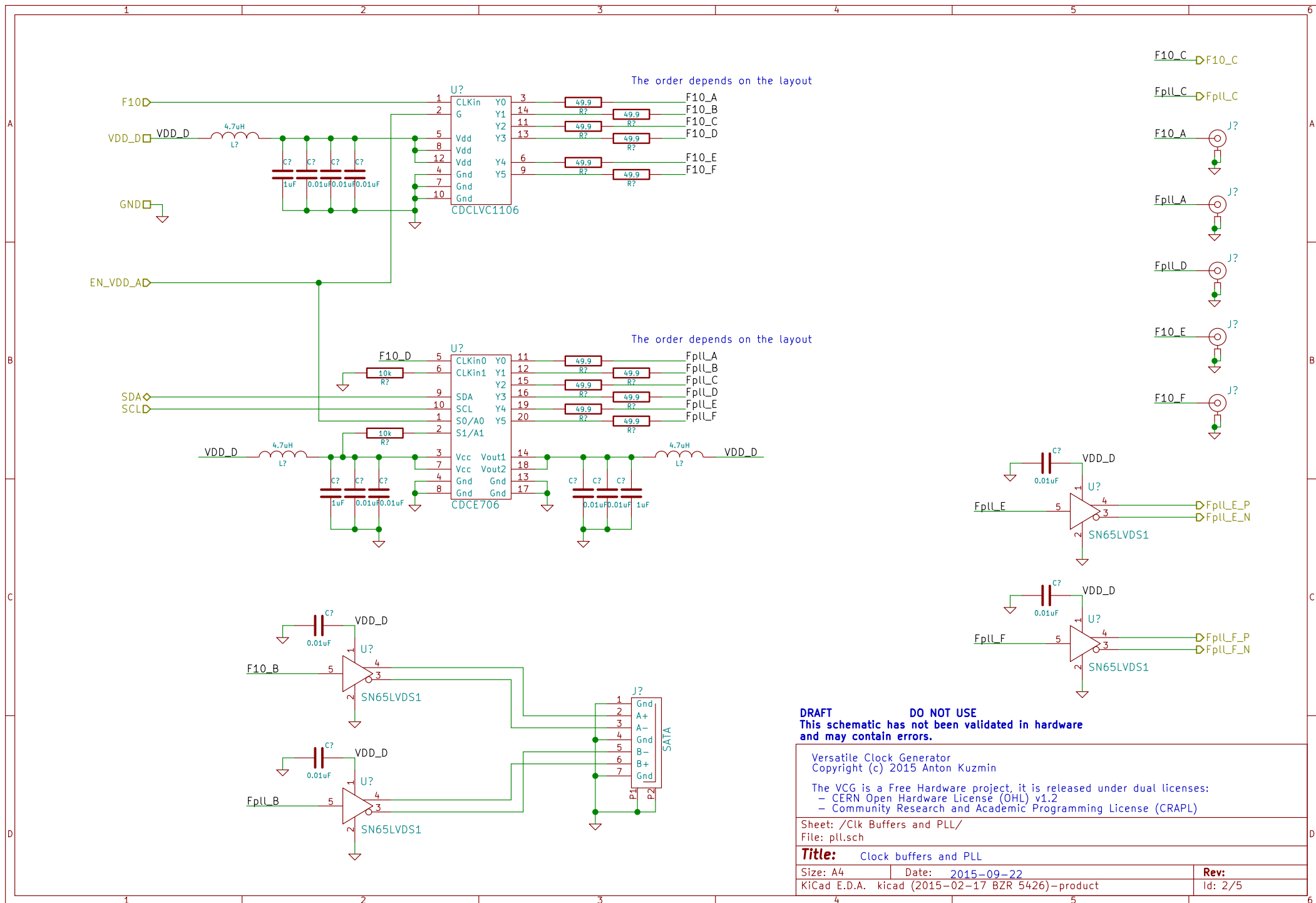


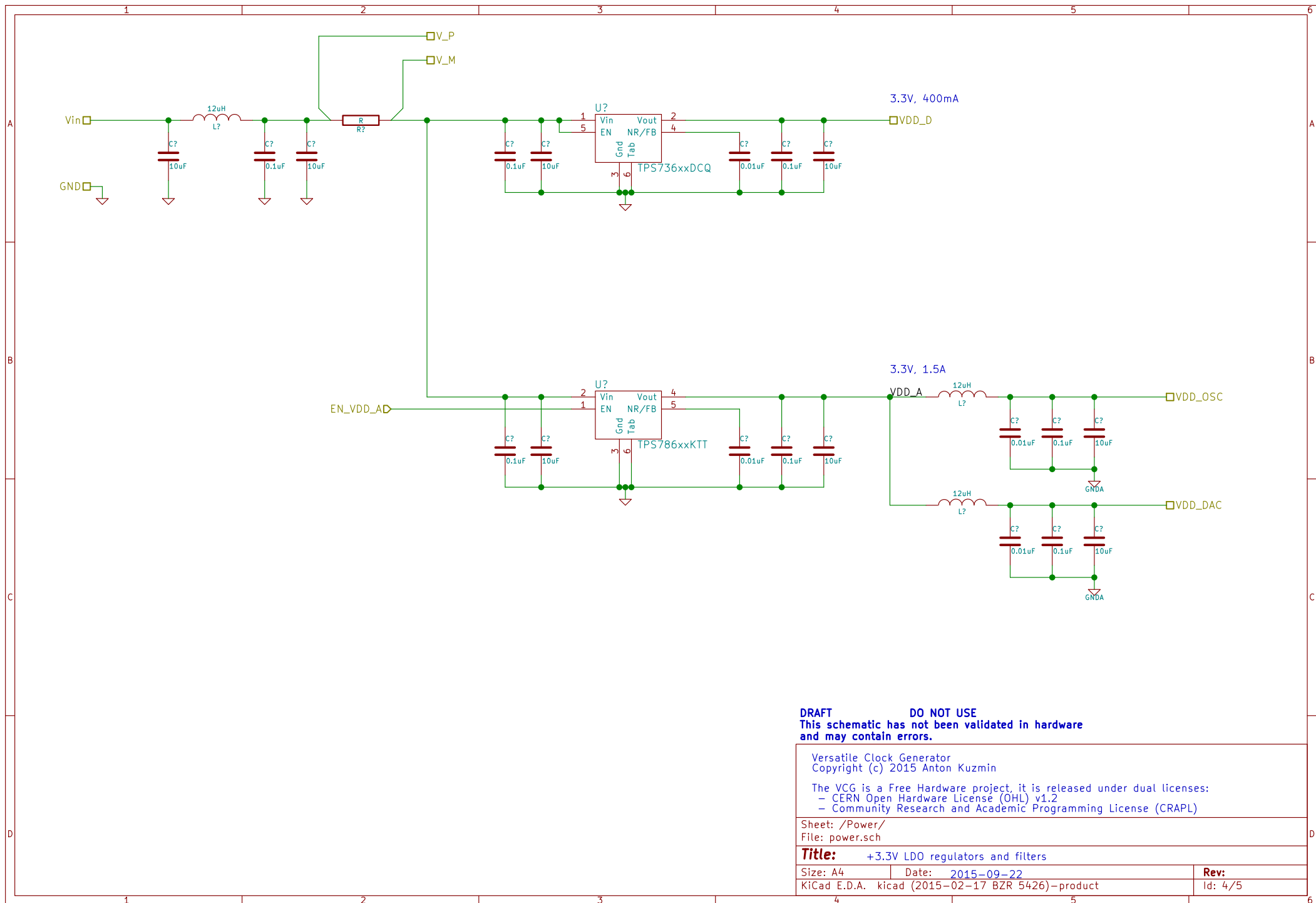
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Sheet: /	
File: vcg.sch	
Title: Top sheet, controller connector	
Size: A4	Date: 2015-09-22
KiCad E.D.A. kicad (2015-02-17 BZR 5426)-product	
Rev:	
Id: 1/5	





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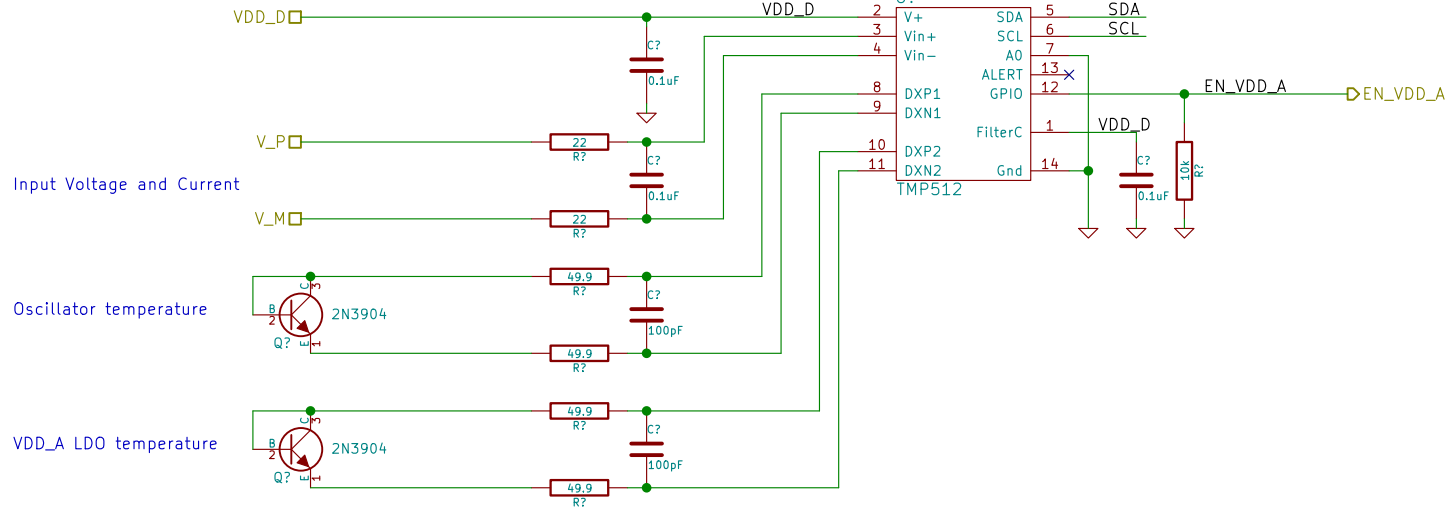
Sheet: /Power/
 File: power.sch

Title: +3.3V LDO regulators and filters

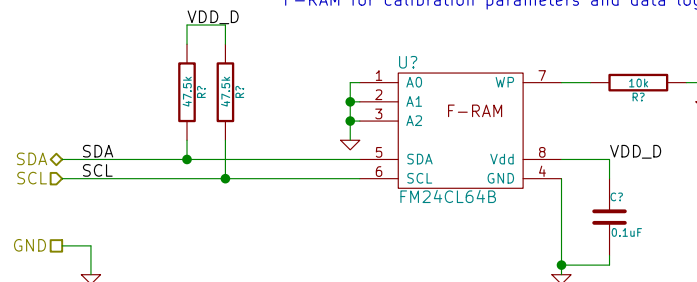
Size: A4 Date: 2015-09-22
 KiCad E.D.A. kicad (2015-02-17 BZR 5426)-product

Rev:
 Id: 4/5

System monitoring (power consumption and temperature)



F-RAM for calibration parameters and data logging



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Sheet: /I2C/
File: i2c.sch

Title: I2C devices: system monitoring and F-RAM

Size: A4 Date: 2015-09-22

KiCad E.D.A. kicad (2015-02-17 BZR 5426)-product

Rev:
Id: 5/5