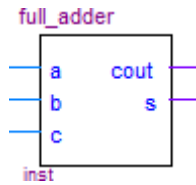


Report

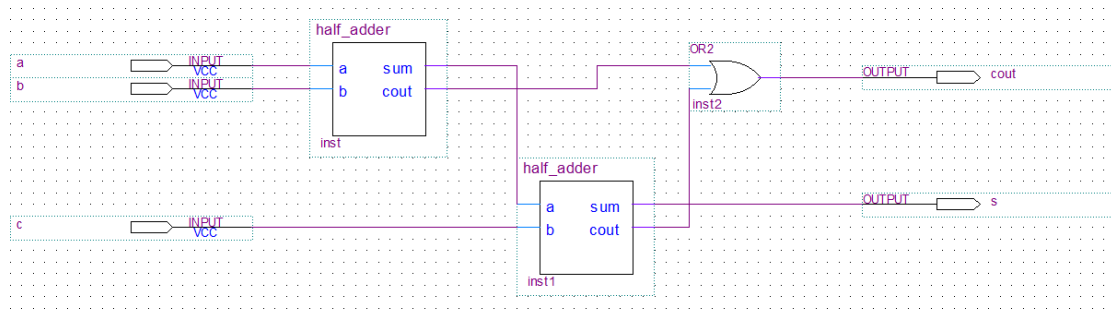
Name: full_adder

Description: binary full adder.

Symbol:



Circuit:



Simulation:

