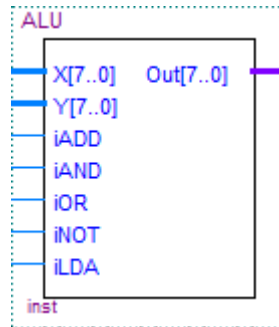


Report

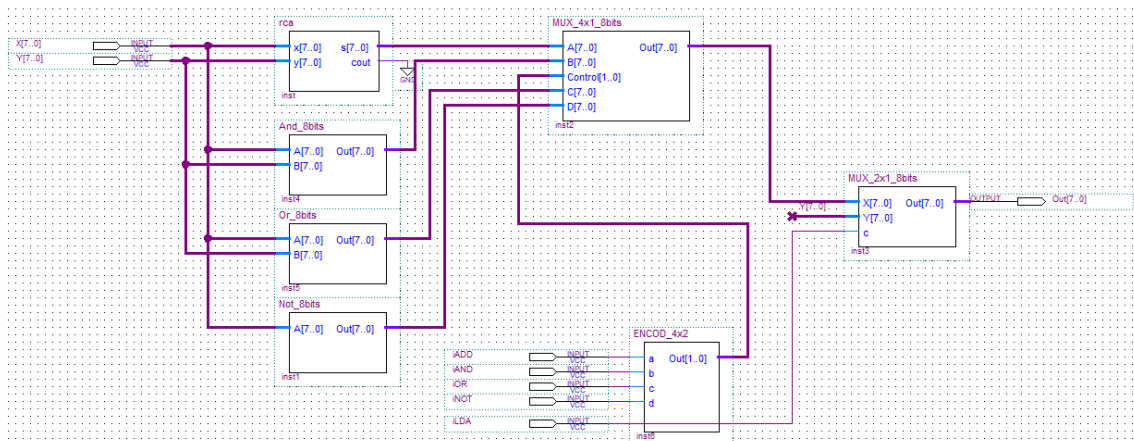
Name: ALU

Simulation: 8 bits arithmetic logic unit which will be used to construct neander.

Symbol:



Circuit:



Simulation:

