ARI KAMLANI

DATA SCIENTIST | TECHNOLOGIST

@akamlani ❖ arikamlani.com

(415) 926-1221 ✉ akamlani@gmail.com

San Francisco, CA

Data Scientist, former *Technologist* with 15+ years of experience, delivering strategic large-scale projects. Focused on advancing industry initiatives per alleviating user and technology pain points. Prior domain expertise includes IoT, Wireless Technology, and Semiconductor Reference designs.

EDUCATION

Data ScienceTech Institute (DSTI), Paris, France | (Oct 2015-May 2016) | Part-Time Data Scientist Designer Program
General Assembly, San Francisco, CA | (Oct 2014 – Dec 2014) | Part-Time Data Science Program
Lehigh University, Bethlehem, PA | (Aug 1995 - May 1999) | B.S. in Electrical Engineering

TECHNICAL SKILLS

Distributed Systems
- Apache Spark
- Apache Hadoop, Map Reduce
- IBM Biginsights
- AWS EC2 + S3
- IBM Bluemix

Data Analysis
- SAS Enterprise Miner, Visual Analytics
- IBM SPSS Modeler, Watson
- Jupyter, Zeppelin, Rodeo, RStudio
- IPython, IntelliJ, Spyder, Eclipse
- Sklearn, NLTK, GraphLab

Programming
- Python, R, Scala, SQL
- C, C++, ARM, UML, OCL
- PostgreSQL, MongoDB, SQLite
- Javascript, HTML, CSS
- D3.js, Bootstrap, Jekyll
- Linux/Unix, QNX, WinCE

PATENTS

- Interference Control in Wireless Communication; United States 9,357,404 | Awarded *May 2016*
- Device Localization Based on a Learning Model; United States 14/311,077 | Filed *June 2014*

EXPERIENCE

Galvanize | San Francisco, CA *June 2016 – Aug 2016*

Data Science Fellow – Immersive Bootcamp Program

Practical immersive program concentrating on Data Science workflows and Machine Learning algorithms via Python
- Successfully delivered Cooper Hewitt Museum Capstone project providing insights into visitor behavior patterns
- Developed peer case studies regarding credit fraud detection, churn prediction and recommendation engines
- Applied industry standards methods in machine learning algorithms, natural language processing, distributed systems, statistical analysis, and experimental design

Inria Research Institute | Sophia Antipolis, France *Jan 2016 – Apr 2016*

Part-Time Research Assistant – STARS (Spatio-Temporal Activity Recognition Systems) Research Team

Computer vision Semantic Scene interpretation per preventive care and diagnosis for the elderly
- Enhanced event recognition models of manually identified zones resulting in improved accuracy detection
- Improved structure interpretation of unsupervised zone predictions providing additional scene context

Nagra Kudelski Group | San Francisco, CA *Sep 2012-Oct 2015*

Software Expert – Group Incubation & Innovation

Technologist within agile R&D Innovation Group responsible for future Technology and Application Advancement
- Initiated proposals per Intellectual Property (IP), creation of Patents, and formation of new business units
- Successfully executed multiple technology special project research directives to assess portfolio value-add
- Formulated new strategic Partner Vendor relationships to strengthen Digital TV and Public Access sectors
- Led Proof of Concept (POC) designs centered on long-range distance detection, achieving similar results to Active RFID via a Battery-Assisted Passive (BAP) RFID design at a fraction of the cost

SPORTVISION | Mountain View, CA *Oct 2011-Apr 2012*

Embedded Software Consultant – Motorsports Division

Special Projects Consultant reporting to office of CTO for Motorsports (NASCAR Trucks) division, advising in the development of vehicle tracking, local base station communication, and sensor measurement

- Advised successful POC execution via SoC peripheral selection and prioritized feature development actions
- Successfully delivered customized Embedded Linux Kernel (2.6.35) and Root Filesystem (RFS) distribution to improve stability and performance of predecessor system, notably reducing crashes, latency and boot-time
- Provided critical features to diagnose defects in the field and update images during race day

Broadcom | Sunnyvale, CA *Feb 2011-Oct 2011*

Principal Engineer – Systems Engineering, Cellular Division

Responsible for WiMAX and LTE radio network driver systems software architecture mobile reference designs

- Directed technology teams per next generation mobile platform processor architecture and requirements and integration of vendor Voice over LTE (VoLTE) stack into reference design to improve product offering
- Managed multi-site coordination per customer (RIM) migration platform architecture from Linux to QNX to achieve successful WiMAX certification

Qualcomm | Raleigh, NC *Aug 2007-Jan 2010*

Staff Engineer – Computing and Consumer Division

Responsible for Windows Mobile board support package (BSP) Snapdragon ARM Cortex based reference designs

- Initiated cross-disciplinary multi-site technology reviews to access processor requirements, identify next generation features, and improve upon current reference design implementations
- Enhanced systems performance initiatives, to further optimize boot time, performance monitors and latency
- Advised OEMs in custom feature development and in successfully achieving certification requirements

Previous Experiences:

TapRoot Systems | Morrisville, NC | 2003-2007 | Principal Engineer/Technical Lead – Mobile Products and Services
Panasonic Mobile Communications | Suwanee, GA | 2000-2003 | Senior Software Engineer – Mobile Platforms
Verizon Wireless | Plymouth Meeting, PA | 1999-2000 | RF Systems Performance Engineer – Systems Performance

Additional Skills

Project/Requirements Mgmt	Agile Scrum (Pivotal Tracker), Doors
SCM/Tracking	Git, Perforce, ClearCase, ClearQuest, SVN, PVCS, Bugzilla