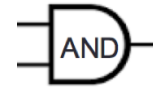


Inputs		Output
x	y	x AND y
1	1	1
1	0	0
0	1	0
0	0	0



Inputs		Output
x	y	x XOR y
1	1	0
1	0	1
0	1	1
0	0	0



Input	Output
x	NOT x
1	0
0	1

